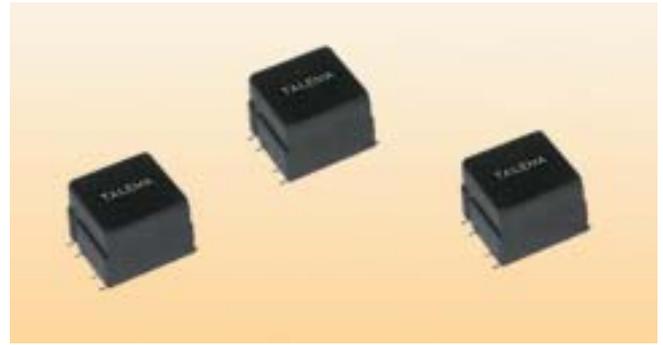




## ISDN - U<sub>PO</sub> SMD Single Interface Transformers

### Features

- matched to Infineon's PEB2095 & PEB20950 and AMD's AM2095 & AM20950 chipsets
- excellent and consistent balance between windings
- compact sizes in through-hole and surface mount packages
- complies fully with all international standards for U-Interface
- manufactured in ISO-9001 approved Talema facility
- operating temperature: -40° to +85°C



### Electrical Specifications @ 25°C

Turns Ratio: **Bold** = IC Side Windings

### U<sub>PO</sub> SMD Interface Transformers Complies with Basic Insulation Level EN60950, UL1950 and UL1459

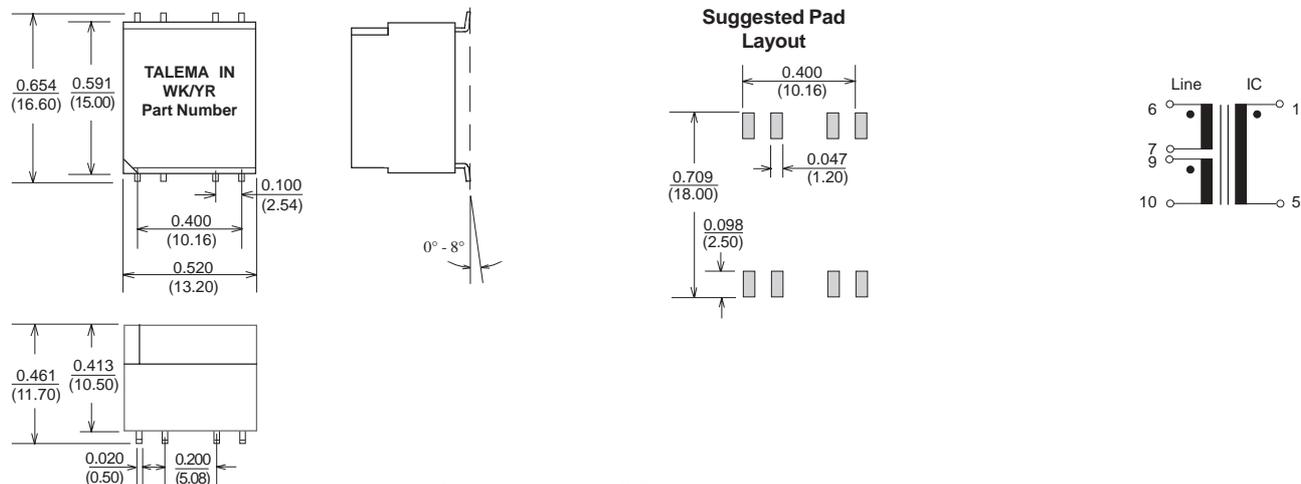
Part Number	Turns Ratio ±2%	L <sub>P</sub> (mH min)	L <sub>L</sub> (μH)	I <sub>DC</sub> <sup>1</sup> (mA)	C <sub>C</sub> (pF Max)	DCR (Ohms)		V <sub>P</sub> (Vrms)
						Primary	Secondary	
UWJ-180	<b>1:1.4</b>	1.7	10	140	100	1.5	5.2	500

### Test Conditions:

Inductance:	Line side windings in series - measurement @ 10kHz, 100mVrms
Leakage Inductance:	Line side windings in series, IC side winding short circuited - measurement @ 100kHz, 100mVrms
Coupling Capacitance:	IC side winding to Line side windings in series @ 10kHz, 100mVrms
Test Voltage:	Line side windings in series to IC side winding

### Dimensions & Schematic

#### UWJ



Surface Coplanarity will be 0.004(0.10)

Dimensions: Inches (Millimeters)  
Tolerance: ±0.010 (0.25) unless specified otherwise

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