October 2004

# **ISL9V5036S3S / ISL9V5036P3 / ISL9V5036S3** EcoSPARK<sup>™</sup> 500mJ, 360V, N-Channel Ignition IGBT

## **General Description**

**FAIRCHILD** 

The ISL9V5036S3S, ISL9V5036P3, and ISL9V5036S3 are the next generation IGBTs that offer outstanding SCIS capability in the D<sup>2</sup>-Pak (TO-263) and TO-220 plastic package. These devices are intended for use in automotive ignition circuits, specifically as coil drivers. Internal diodes provide voltage clamping without the need for external components.

EcoSPARK<sup>™</sup> devices can be custom made to specific clamp voltages. Contact your nearest Fairchild sales office for more information.

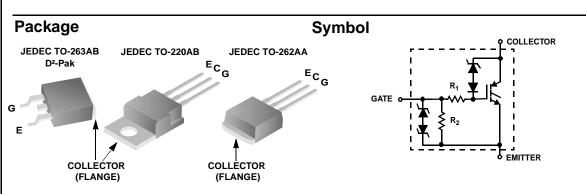
Formerly Developmental Type 49443

## Applications

- Automotive Ignition Coil Driver Circuits
- Coil-On Plug Applications

#### Features

- Industry Standard D<sup>2</sup>-Pak package
- SCIS Energy = 500mJ at  $T_1 = 25^{\circ}C$
- Logic Level Gate Drive

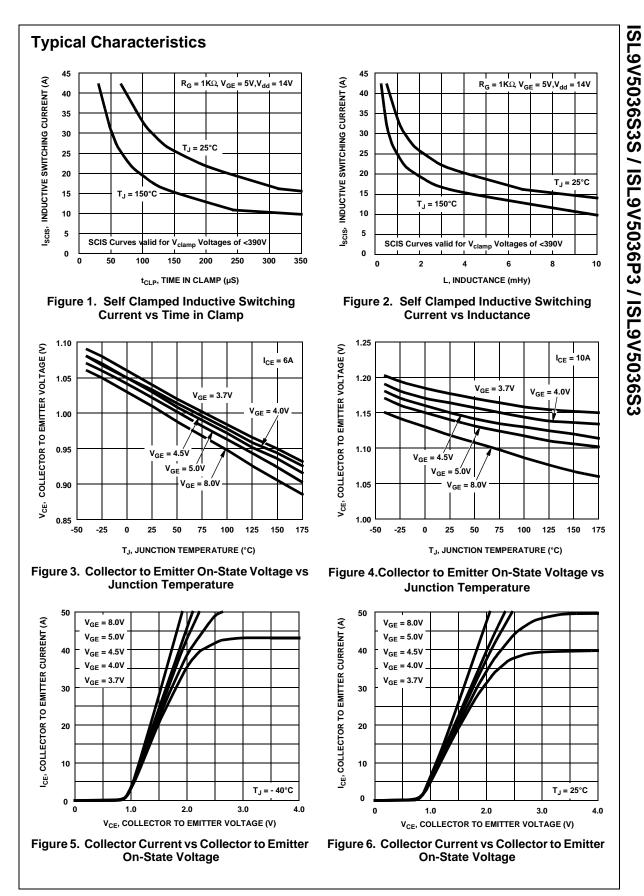


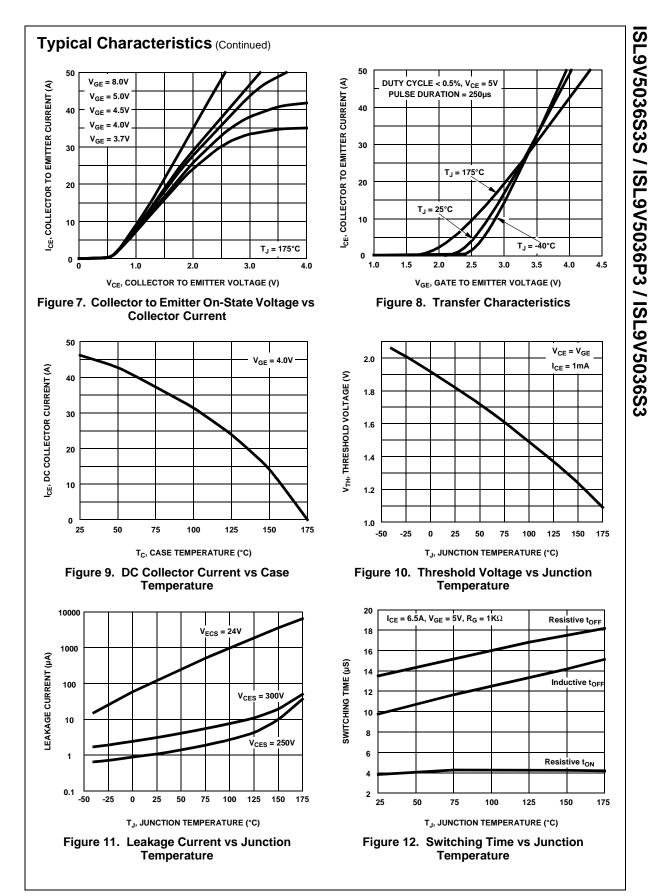
## Device Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

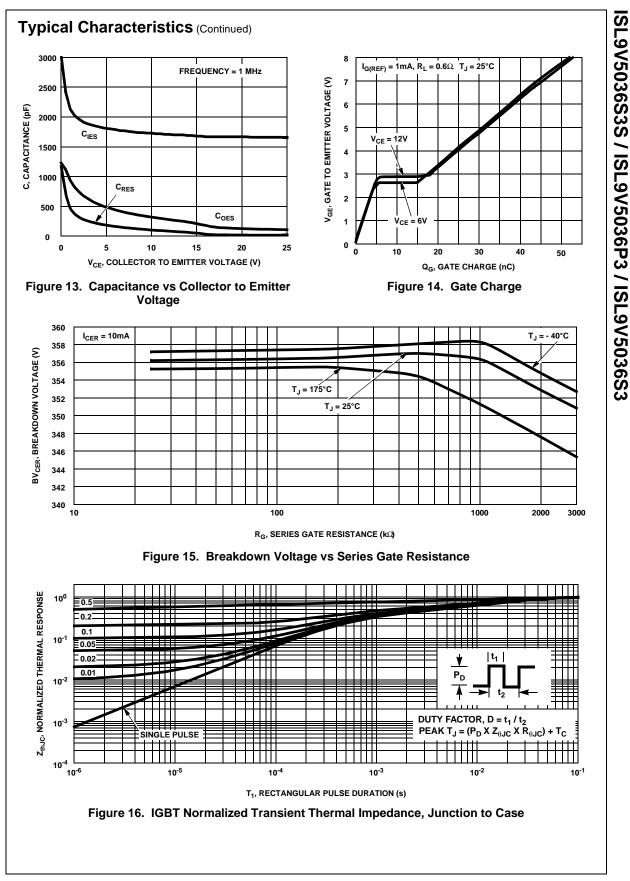
Symbol	Parameter	Ratings	Units V	
BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage (I <sub>C</sub> = 1 mA)	390		
BV <sub>ECS</sub>	Emitter to Collector Voltage - Reverse Battery Condition (I <sub>C</sub> = 10 mA)	24	V	
E <sub>SCIS25</sub>	At Starting $T_J = 25^{\circ}$ C, $I_{SCIS} = 38.5$ A, L = 670 $\mu$ Hy	500	mJ	
E <sub>SCIS150</sub>	At Starting $T_J = 150^{\circ}$ C, $I_{SCIS} = 30$ A, $L = 670 \mu$ Hy	300	mJ	
I <sub>C25</sub>	Collector Current Continuous, At T <sub>C</sub> = 25°C, See Fig 9	46	Α	
I <sub>C110</sub>	Collector Current Continuous, At T <sub>C</sub> = 110°C, See Fig 9	31	Α	
V <sub>GEM</sub>	Gate to Emitter Voltage Continuous	±10	V	
PD	Power Dissipation Total $T_C = 25^{\circ}C$	250	W	
	Power Dissipation Derating $T_{C} > 25^{\circ}C$	1.67	W/°C	
TJ	Operating Junction Temperature Range	-40 to 175	°C	
T <sub>STG</sub>	Storage Junction Temperature Range	-40 to 175	°C	
ΤL	T <sub>L</sub> Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)		°C	
T <sub>pkg</sub>	T <sub>pkg</sub> Max Lead Temp for Soldering (Package Body for 10s)		°C	
ESD	Electrostatic Discharge Voltage at 100pF, 1500 $\Omega$	4	kV	

	vice Marking Device		Package Reel Size		e	Tape Wid	dth	Quantity		
V50	036S ISL9V5036S3ST		TO-263AB	330mm		24mm		800		
V5036P ISL9V5036P3		TO-220AA	Tube		N/A		50			
V5036S		ISL9V5036S3		TO-262AA	Tube	N/A			50	
V5036S ISL		ISL9V5036S3S		TO-263AB Tube		N/A			50	
ectrica	al Chara	acteristics T <sub>A</sub> = 2	5°C un	less otherwise n	oted					
Symbol	Parameter		Test Conditions		Min	Тур	Max	Units		
f State	Characte	ristics								
BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage		$I_{C} = 2mA, V_{GE} = 0,$ $R_{G} = 1K\Omega$ , See Fig. 15 $T_{J} = -40 \text{ to } 150^{\circ}\text{C}$		330	360	390	V		
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage		$I_{C} = 10mA, V_{GE} = 0,$ $R_{G} = 0, See Fig. 15$ $T_{J} = -40 \text{ to } 150^{\circ}\text{C}$		360	390	420	V		
BV <sub>ECS</sub>	Emitter to Collector Breakdown Voltage		$I_C = -75$ mA, $V_{GE} = 0$ V, $T_C = 25$ °C		30	-	-	V		
$BV_{GES}$		nitter Breakdown Voltag		$I_{GES} = \pm 2mA$		±12	±14	-	V	
I <sub>CER</sub>	Collector t	o Emitter Leakage Curr	ent	$V_{CER} = 250V,$	$T_{C} = 25^{\circ}C$	-	-	25	μA	
				R <sub>G</sub> = 1KΩ, See Fig. 11	T <sub>C</sub> = 150°C	-	-	1	mA	
I <sub>ECS</sub>	Emitter to	Collector Leakage Curr	ent	V <sub>EC</sub> = 24V, See Fig. 11	$T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$	-	-	1 40	mA mA	
R <sub>1</sub>	Series Gate Resistance			•	-	75	-	Ω		
$R_2$	Gate to Emitter Resistance									
	Gale to Er	nitter Resistance				10K	-	30K	Ω	
	Characte	ristics	Itage	L = 10A	T 25°C	10K	-	i		
V <sub>CE(SAT)</sub>	Characte Collector t	ristics o Emitter Saturation Vol	-	$I_{C} = 10A,$ $V_{GE} = 4.0V$	T <sub>C</sub> = 25°C, See Fig. 4	-	-	1.60	V	
	Characte Collector t	ristics	-	•		10K - -	- 1.17 1.50	i	V	
V <sub>CE(SAT)</sub> V <sub>CE(SAT)</sub>	Characte Collector t	ristics o Emitter Saturation Vol o Emitter Saturation Vol	-	$V_{GE} = 4.0V$ I <sub>C</sub> = 15A,	See Fig. 4	-		1.60	V	
V <sub>CE(SAT)</sub> V <sub>CE(SAT)</sub>	Characte Collector t	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics	-	$V_{GE} = 4.0V$ I <sub>C</sub> = 15A,	See Fig. 4 T <sub>C</sub> = 150°C	-		1.60	V	
V <sub>CE(SAT)</sub> V <sub>CE(SAT)</sub> /namic	Characte Collector t Collector t Characte Gate Char	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics	ltage	$V_{GE} = 4.0V$ $I_C = 15A,$ $V_{GE} = 4.5V$ $I_C = 10A, V_{CE} =$ $V_{GE} = 5V, See$ $I_C = 1.0mA,$	See Fig. 4 T <sub>C</sub> = 150°C	-	1.50	1.60	V V	
$V_{CE(SAT)}$ $V_{CE(SAT)}$ $/namic$ $Q_{G(ON)}$ $V_{GE(TH)}$	Characte Collector t Collector t Characte Gate Char Gate to Er	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics rge mitter Threshold Voltage	ltage	$V_{GE} = 4.0V$ $I_C = 15A,$ $V_{GE} = 4.5V$ $I_C = 10A, V_{CE} =$ $V_{GE} = 5V,$ See $I_C = 1.0mA,$ $V_{CE} = V_{GE,}$ See Fig. 10	See Fig. 4 $T_{C} = 150^{\circ}C$ = 12V, Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$	-	1.50 32	1.60	V           V           nC           V           V	
V <sub>CE(SAT)</sub> V <sub>CE(SAT)</sub> /namic Q <sub>G(ON)</sub>	Characte Collector t Collector t Characte Gate Char Gate to Er	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics rge	ltage	$V_{GE} = 4.0V$ $I_C = 15A,$ $V_{GE} = 4.5V$ $I_C = 10A, V_{CE} =$ $V_{GE} = 5V, See$ $I_C = 1.0mA,$ $V_{CE} = V_{GE},$	See Fig. 4 T <sub>C</sub> = 150°C = 12V, Fig. 14 T <sub>C</sub> = 25°C	1.3	1.50 32	1.60 1.80 - 2.2	V V nC V	
V <sub>CE(SAT)</sub> V <sub>CE(SAT)</sub> /namic Q <sub>G(ON)</sub> V <sub>GE(TH)</sub> V <sub>GEP</sub>	Characte Collector t Collector t Characte Gate Char Gate to Er	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics rge mitter Threshold Voltage	ltage	$V_{GE} = 4.0V$ $I_C = 15A,$ $V_{GE} = 4.5V$ $I_C = 10A, V_{CE} =$ $V_{GE} = 5V,$ See $I_C = 1.0mA,$ $V_{CE} = V_{GE,}$ See Fig. 10	See Fig. 4 $T_{C} = 150^{\circ}C$ = 12V, Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$	- - 1.3 0.75	1.50       32       -       -	1.60 1.80 - 2.2 1.8	V           V           nC           V           V	
V <sub>CE(SAT)</sub> V <sub>CE(SAT)</sub> /namic Q <sub>G(ON)</sub> V <sub>GE(TH)</sub> V <sub>GEP</sub> witching	Characte Collector t Collector t Characte Gate Char Gate to Er Gate to Er	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics rge mitter Threshold Voltage	e ltage	$V_{GE} = 4.0V$ $I_C = 15A,$ $V_{GE} = 4.5V$ $I_C = 10A, V_{CE} =$ $V_{GE} = 5V,$ See $I_C = 1.0mA,$ $V_{CE} = V_{GE,}$ See Fig. 10	See Fig. 4 $T_{C} = 150^{\circ}C$ = 12V, Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$	- - 1.3 0.75	1.50       32       -       -	1.60 1.80 - 2.2 1.8	V           V           nC           V           V	
V <sub>CE(SAT)</sub> V <sub>CE(SAT)</sub> /namic Q <sub>G(ON)</sub> V <sub>GE(TH)</sub> V <sub>GEP</sub>	Characte Collector t Collector t Characte Gate Char Gate to Er Gate to Er Charact	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics rge mitter Threshold Voltage mitter Plateau Voltage eristics	e ltage	$\label{eq:VGE} \begin{array}{l} \overline{V_{GE}} = 4.0V \\ \overline{I_C} = 15A, \\ \overline{V_{GE}} = 4.5V \\ \end{array}$ $\begin{array}{l} \overline{I_C} = 10A, \ \overline{V_{CE}} = \\ \overline{V_{GE}} = 5V, \ \overline{See} \\ \overline{I_C} = 1.0mA, \\ \overline{V_{CE}} = V_{GE}, \\ \overline{See} \ \overline{Fig.} \ 10 \\ \overline{I_C} = 10A, \\ \end{array}$ $\begin{array}{l} \overline{V_{CE}} = 14V, \ \overline{R_L} = \\ \overline{V_{GE}} = 5V, \ \overline{R_G} = \\ \overline{T_J} = 25^\circ C, \ \overline{See} \end{array}$	See Fig. 4 $T_{C} = 150^{\circ}C$ = 12V, Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$ $= 1\Omega,$ $1K\Omega$ Fig. 12	- - 1.3 0.75 -	1.50       32       -       -       3.0	1.60 1.80 - 2.2 1.8 -	V           V           nC           V           V           V	
$V_{CE(SAT)}$ $V_{CE(SAT)}$ $V_{CE(SAT)}$ $V_{GE(ON)}$ $V_{GE(TH)}$ $V_{GEP}$ $vitching$ $t_{d(ON)R}$	Characte Collector t Collector t Characte Gate Char Gate to Er Gate to Er Charact Current Tu Current Ri	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics rge mitter Threshold Voltage mitter Plateau Voltage eristics urn-On Delay Time-Resi	Itage	$\label{eq:VGE} \begin{split} & \overline{V_{GE}} = 4.0V \\ & \overline{I_C} = 15A, \\ & \overline{V_{GE}} = 4.5V \\ \end{split} \\ & \overline{I_C} = 10A, \\ & \overline{V_{CE}} = 5V, \\ & \overline{I_C} = 1.0mA, \\ & \overline{V_{CE}} = V_{GE}, \\ & \overline{See \ Fig. \ 10} \\ & \overline{I_C} = 10A, \\ \hline & \overline{V_{CE}} = 5V, \\ & \overline{R_G} = \\ & \overline{T_J} = 25^\circ C, \\ & \overline{See} \\ & \overline{V_{CE}} = 300V, \\ & \overline{L} = 100V, \\ \hline & \overline{L} = 10V, \\ \hline $	See Fig. 4 $T_{C} = 150^{\circ}C$ = 12V, Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$ $= 1\Omega,$ $1K\Omega$ Fig. 12 = 2mH,	- - 1.3 0.75 -	1.50       32       -       3.0       0.7	1.60 1.80 - 2.2 1.8 - 4	V           V           N           nC           V           V           V           V           V           V           V           V           V           V           V           V           V           V           V           V           V	
$V_{CE(SAT)}$ $V_{CE(SAT)}$ $V_{CE(SAT)}$ $V_{GE(ON)}$ $V_{GE(TH)}$ $V_{GEP}$ $vitching$ $t_{d(ON)R}$ $t_{rR}$	Characte Collector t Collector t Collector t Characte Gate Char Gate to Er Gate to Er Charact Current Tu Current Tu Current Tu	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics rge mitter Threshold Voltage mitter Plateau Voltage eristics urn-On Delay Time-Resi ise Time-Resistive	Itage	$\label{eq:VGE} \begin{split} & \overline{V_{GE}} = 4.0V \\ & \overline{I_C} = 15A, \\ & \overline{V_{GE}} = 4.5V \\ \hline \\ & \overline{I_C} = 10A, \\ & \overline{V_{CE}} = 5V, \\ & \overline{See} \\ & \overline{I_C} = 1.0mA, \\ & \overline{V_{CE}} = V_{GE}, \\ & \overline{See} \\ & \overline{Fig. 10} \\ \hline \\ & \overline{I_C} = 10A, \\ \hline \\ & \overline{V_{CE}} = 5V, \\ & \overline{R_G} = \\ & \overline{T_J} = 25^\circ\text{C}, \\ & \overline{See} \\ & \overline{V_{CE}} = 300V, \\ & \overline{L} = \\ & \overline{V_{GE}} = 5V, \\ & \overline{R_G} = \\ & \overline{T_J} = 25^\circ\text{C}, \\ & \overline{See} \\ \hline \end{array}$	See Fig. 4 $T_{C} = 150^{\circ}C$ $T_{C} = 150^{\circ}C$ $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$ $V_{CE} = 12V$ $V_{CE} = 12V$ $T_{C} = 12V$	- - 1.3 0.75 - -	1.50       32       -       3.0       0.7       2.1	1.60       1.80       -       2.2       1.8       -       4       7	V           V           N           nC           V           V           V           V           V           V           V           V           V           V           V           V           V           V           V           V           μs	
$V_{CE(SAT)}$ $V_{CE(SAT)}$ $V_{CE(SAT)}$ $V_{GE(SAT)}$ $V_{GE(TH)}$ $V_{GE(TH)}$ $V_{GEP}$ $witching$ $t_{d(OFF)L}$	Characte Collector t Collector t Collector t Characte Gate Char Gate to Er Gate to Er Charact Current Tu Current Ri Current Tu Current Tu	ristics o Emitter Saturation Vol o Emitter Saturation Vol ristics rge mitter Threshold Voltage mitter Plateau Voltage eristics urn-On Delay Time-Resi ise Time-Resistive urn-Off Delay Time-Indu	Itage	$\label{eq:VGE} \begin{array}{l} \overline{V_{GE}} = 4.0V \\ \overline{I_C} = 15A, \\ \overline{V_{GE}} = 4.5V \\ \end{array} \\ \begin{array}{l} \overline{I_C} = 10A, \ \overline{V_{CE}} = \\ \overline{V_{GE}} = 5V, \ \overline{See} \\ \overline{I_C} = 1.0mA, \\ \overline{V_{CE}} = V_{GE}, \\ \overline{See} \ \overline{Fig.} \ 10 \\ \overline{I_C} = 10A, \\ \end{array} \\ \begin{array}{l} \overline{V_{CE}} = 14V, \ \overline{R_L} = \\ \overline{V_{GE}} = 5V, \ \overline{R_G} = \\ \overline{T_J} = 25^\circ C, \ \overline{See} \\ \overline{V_{CE}} = 300V, \ \overline{L} = \\ \overline{V_{GE}} = 5V, \ \overline{R_G} = \\ \end{array}$	See Fig. 4 $T_{C} = 150^{\circ}C$ Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$ $V_{CE} = 12V$ $V_{CE} = 12V$ Fig. 12 $= 2mH, -1K\Omega$ Fig. 12 Fig. 12 $= 2mH, -1K\Omega$ Fig. 12 $= 200$ $\mu$ H, $T_{C} = 120$	- - - - - - - - - - -	1.50         32         -         3.0         0.7         2.1         10.8	1.60         1.80         -         2.2         1.8         -         4         7         15	V           V           N           nC           V           V           V           V           V           V           V           V           V           V           V           V           V           V           V           μs           μs           μs	
$V_{CE(SAT)}$ $V_{CE(SAT)}$ $V_{CE(SAT)}$ $V_{GE(SAT)}$ $V_{GE(TH)}$ $V_{GEP}$ $vitching$ $t_{rR}$ $t_{d(O)R}$ $t_{rR}$ $t_{d(OFF)L}$ $t_{fL}$ SCIS	Characte Collector t Collector t Collector t Characte Gate Char Gate to Er Gate to Er Charact Current Tu Current Ri Current Tu Current Tu	ristics o Emitter Saturation Vol o Emitter Saturation Vol o Emitter Saturation Vol ristics rge mitter Threshold Voltage mitter Plateau Voltage eristics urn-On Delay Time-Resi ise Time-Resistive urn-Off Delay Time-Indu all Time-Inductive ped Inductive Switching	Itage	$\label{eq:VGE} \begin{split} & V_{GE} = 4.0V \\ & I_C = 15A, \\ & V_{GE} = 4.5V \\ \hline \\ & I_C = 10A, V_{CE} = \\ & V_{GE} = 5V, See \\ & I_C = 1.0mA, \\ & V_{CE} = V_{GE}, \\ & See Fig. 10 \\ & I_C = 10A, \\ \hline \\ & V_{GE} = 5V, R_G = \\ & T_J = 25^\circ C, See \\ & V_{CE} = 300V, L = \\ & V_{GE} = 5V, R_G = \\ & T_J = 25^\circ C, See \\ & T_J = 25^\circ C, See \\ \hline \\ & T_J = 25^\circ C, L = 0 \\ & R_G = 1K\Omega, V_{GE} \\ \end{split}$	See Fig. 4 $T_{C} = 150^{\circ}C$ Fig. 14 $T_{C} = 25^{\circ}C$ $T_{C} = 150^{\circ}C$ $V_{CE} = 12V$ $V_{CE} = 12V$ $V_{CE} = 12V$ Fig. 12 $= 2mH, -1K\Omega$ Fig. 12 Fig. 12 $= 2mH, -1K\Omega$ Fig. 12 $= 200$ $\mu$ H, $T_{C} = 120$	- - 1.3 0.75 - - - - -	1.50         32         -         3.0         0.7         2.1         10.8         2.8	1.60         1.80         -         2.2         1.8         -         4         7         15         15	V           V           N           nC           V           V           V           V           V           V           V           V           V           V           V           V           V           V           V           μs           μs           μs	

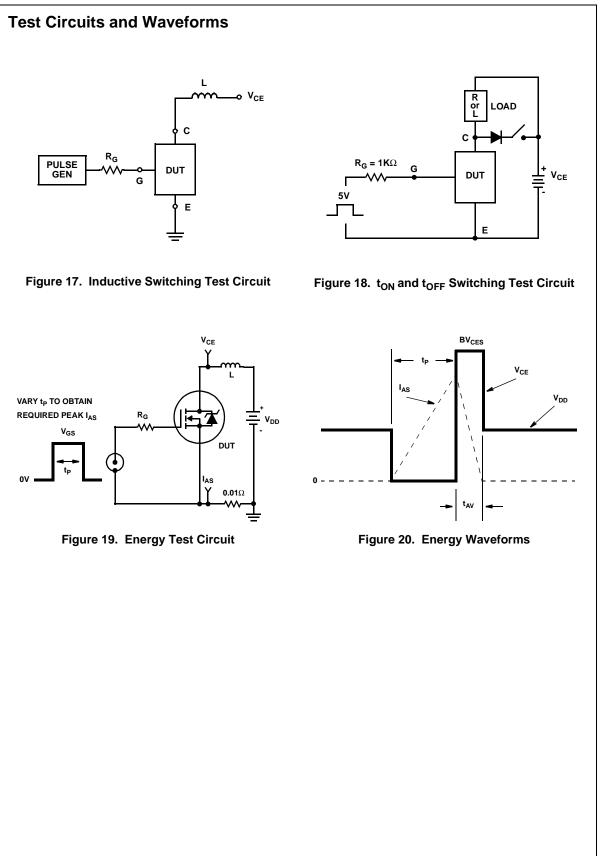
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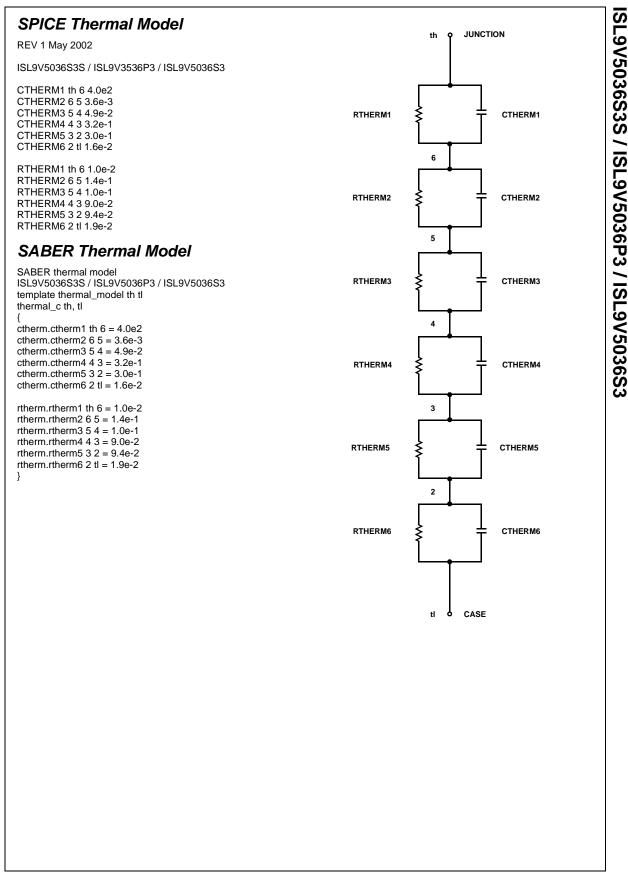




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CoolFET™	FRFET™	MicroFET™	PowerTrench <sup>®</sup>	SuperSOT™-6
CROSSVOLT™	GlobalOptoisolator™	MicroPak™	QFET <sup>®</sup>	SuperSOT™-8
DOME™	GTO™	MICROWIRE™	QS™	SyncFET™
EcoSPARK™	HiSeC™	MSX™	QT Optoelectronics <sup>™</sup>	TinyLogic®
E <sup>2</sup> CMOS <sup>™</sup>	I²C™	MSXPro™	Quiet Series <sup>™</sup>	TINYOPTO™
EnSigna™	<i>i-Lo</i> ™	OCX™	RapidConfigure™	TruTranslation™
FACT™	ImpliedDisconnect™	OCXPro™	RapidConnect™	UHC™
FACT Quiet Series <sup>™</sup>		OPTOLOGIC <sup>®</sup>	µSerDes™	UltraFET <sup>®</sup>
Across the board. Around the world. <sup>™</sup> The Power Franchise <sup>®</sup> Programmable Active Droop <sup>™</sup>		OPTOPLANAR™ PACMAN™ POP™	SILENT SWITCHER <sup>®</sup> SMART START™ SPM™	VCX™

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#### **PRODUCT STATUS DEFINITIONS**

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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		Rev. 113