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December 2006

FDC638APZ P-Channel 2.5V PowerTrench[®] Specified MOSFET –20V, –4.5A, 43mΩ

Features

- Max $r_{DS(on)}$ = 43m Ω at V_{GS} = -4.5V, I_D = -4.5A
- Max $r_{DS(on)}$ = 68m Ω at V_{GS} = -2.5V, I_D = -3.8A
- Low gate charge (8nC typical).
- High performance trench technology for extremely low r_{DS(on)}.
- SuperSOTTM –6 package:small footprint (72% smaller than standard SO–8) low profile (1mm thick).
- RoHS Compliant



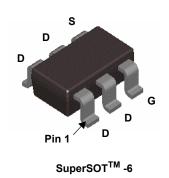
General Description

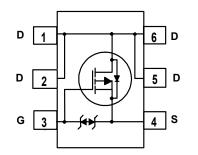
This P-Channel 2.5V specified MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench[®] process that has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance

These devices are well suited for battery power applications:load switching and power management,battery charging circuits,and DC/DC conversion.

Application

■ DC - DC Conversion





MOSFET Maximum Ratings TA= 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units		
V _{DS}	Drain to Source Voltage		-20	V	
V _{GS}	Gate to Source Voltage		±12	V	
I _D	Drain Current -Continuous	(Note 1a)	-4.5	Α	
	-Pulsed		-20		
P _D	Power Dissipation	(Note 1a)	1.6		
	Power Dissipation	(Note 1b)	0.8		
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	°C	

Thermal Characteristics

R_{\thetaJA}	Thermal Resistance, Junction to Ambient	(Note 1a)	78	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	156	C/VV

Package Marking and Ordering Information

Device Marking	vice Marking Device		Tape Width	Quantity
.638Z	FDC638APZ	7"	8mm	3000 units

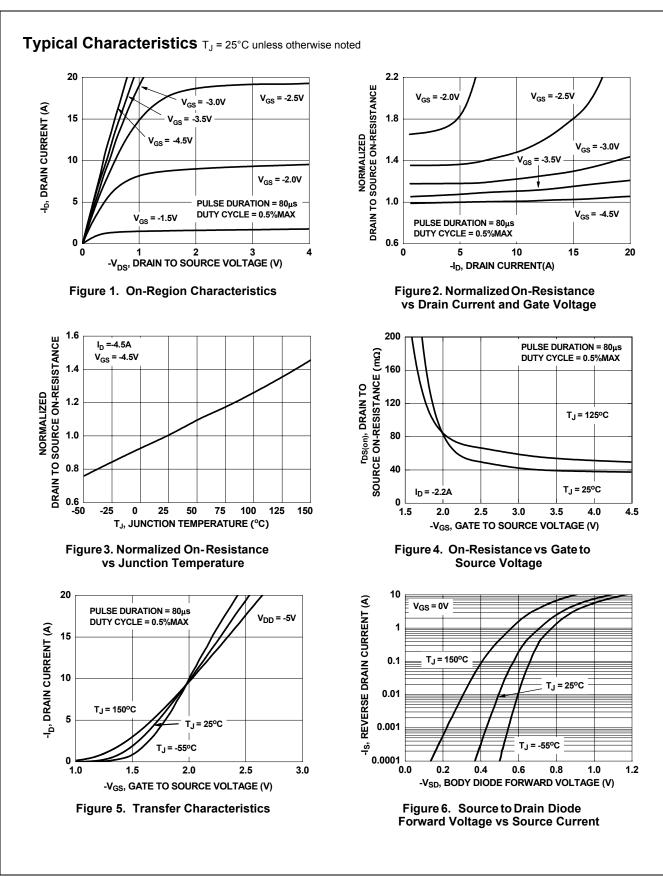
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
	cteristics				l	
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = -250μA, V _{GS} = 0V	-20			V
∆BV _{DSS}	Breakdown Voltage Temperature		20			
ΔT_{J}	Coefficient	$I_D = -250\mu A$, referenced to $25^{\circ}C$		-9.4		mV/°C
1	Zero Gate Voltage Drain Current	V _{DS} = -16V,			-1	
DSS	Zero Gale voltage Drain Current	$V_{GS} = 0V$ $T_J = 55^{\circ}C$			-10	μA
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 12V, V_{DS} = 0V$			±10	μA
On Chara	cteristics					
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = -250 \mu A$	-0.4	-0.8	-1.5	V
$\Delta V_{GS(th)}$	Gate to Source Threshold Voltage		0.4		1.0	
ΔT_{J}	Temperature Coefficient	$I_D = -250\mu A$, referenced to $25^{\circ}C$		2.9		mV/°C
		$V_{GS} = -4.5V, I_{D} = -4.5A$		37	43	
r _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = -2.5V, I_D = -3.8A$		52	68	mΩ
()		$V_{GS} = -4.5V, I_D = -4.5A, T_J = 125^{\circ}C$		50	72	
I _{D(on)}	On-State Drain Current	$V_{GS} = -10V, V_{DS} = -4.5A$	-20			Α
9FS	Forward Transconductance	$V_{DS} = -10V$, $I_{D} = -4.5A$		18		S
Dynamic	Characteristics					
-	Input Capacitance			750	1000	pF
C _{iss}	Output Capacitance	V _{DS} = -10V, V _{GS} = 0V,		155	210	pF
C _{oss} C _{rss}	Reverse Transfer Capacitance	f = 1MHz		130	195	pF
				100	100	P1
Switching	Characteristics (Note 2)			1	1	
t _{d(on)}	Turn-On Delay Time			6	12	ns
t _r	Rise Time	−V _{DD} = –5V, I _D = –4.5A −V _{GS} = –4.5V, R _{GEN} = 6Ω		20	31	ns
t _{d(off)}	Turn-Off Delay Time	UGS 4.00, NGEN 032		48	77	ns
t _f	Fall Time			47	72	ns
Q _{g(TOT)}	Total Gate Charge	$V_{GS} = 0V \text{ to } -4.5V$ $V_{DD} = -5V$ $I_D = -4.5A$		8	12	nC
Q _{gs}	Gate to Source Gate Charge	I _D = -4.5A		2		nC
Q _{gd}	Gate to Drain "Miller" Charge			2		nC
Drain-Sou	urce Diode Characteristics					
I _S	Maximum Continuous Drain-Source Dic	de Forward Current			-1.3	Α
V _{SD}	Source to Drain Diode Forward Voltage			-0.8	-1.2	V
t _{rr}	Reverse Recovery Time			24	36	ns
Q _{rr}	Reverse Recovery Charge	−I _F = −4.5A, di/dt = 100A/μs		13	20	nC
	Im of the junction-to-case and case-to-ambient resistar y design while $R_{\theta CA}$ is determined by user's board desi a. 78°C/W when mounted on a 1 in ² pad of 2 oz copper on	gn.	hen mounted	ton a	of the drair	η pins.R _{θJC}

FDC638APZ Rev.B

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2: Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%.

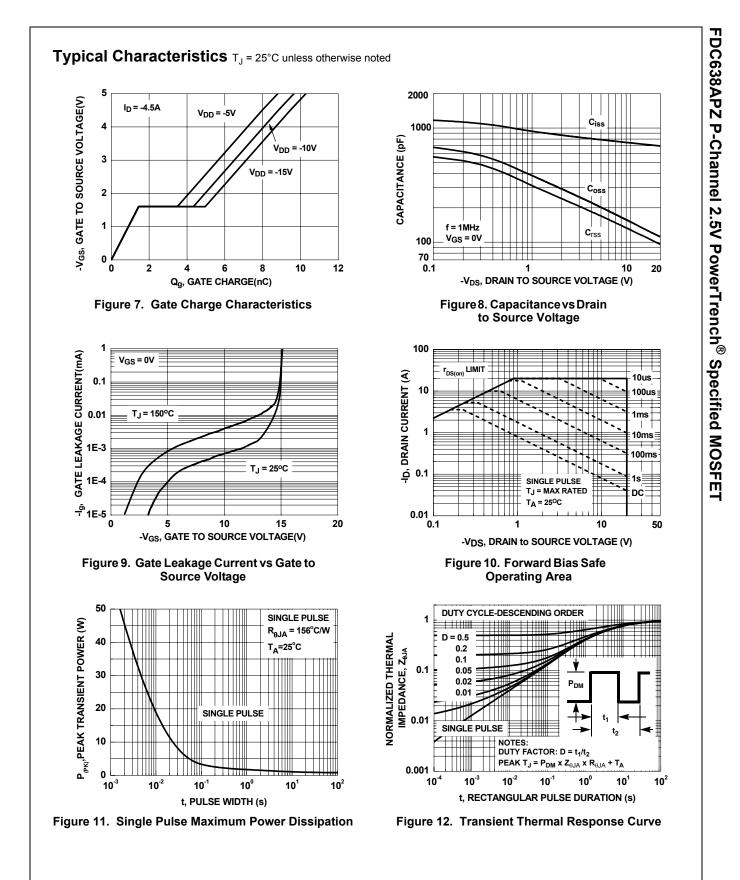
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FDC638ASPZ P-Channel 2.5V PowerTrench[®] Specified MOSFET

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