<u>**WAY**</u>

30V Dual P-Channel Enhancement Mode Power MOSFET

Description

WMS09DP03T1 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- V_{DS}= -30V, I_D = -9A
 R_{DS(on)} < 17mΩ @ V_{GS} = -10 V
 - $R_{DS(on)} < 25m\Omega @ V_{GS} = -4.5V$
- High Power and Current Handing Capability
- Low Gate Charge

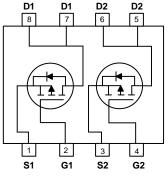
Applications

- Power Management Switches
- Battery Protection Applications

Absolute Maximum Ratings

D1 D2 D2 D2 D2 D2 D2 D2				
SOP-8L				





Parameter		Symbol	Value	Unit	
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		Vgs	±20	V	
	T _A =25°C		-9		
Continuous Drain Current@-10V ¹	T _A =70°C	- ID -	-6.5	A	
Pulsed Drain Current ²	Ідм	-35	А		
Single Pulse Avalanche Energy ³		EAS	80	mJ	
Avalanche Current		las	-40	А	
Total Power Dissipation ⁴ T _A =25°C		PD	2.7	W	
Operating Junction and Storage Temperature Range		Tj, Tstg	-55 to 150	°C	

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ¹	R _{0JA}	45	°C/W



Electrical Characteristics T_c = 25°C, unless otherwise noted

Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		V(BR)DSS	$V_{GS} = 0V, I_D = -250 \mu A$	-30	-	-	V
Gate-body Leakage current		lgss	$V_{DS} = 0V$, $V_{GS} = \pm 20V$	-	-	±100	nA
Zero Gate Voltage Drain Current	TJ=25℃	- Idss	$V_{DS} = -24V, V_{GS} = 0V$	-	-	-1	μA
	TJ=22℃			-	-	-5	
Gate-Threshold Voltage		V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250µA	-1.0	-1.7	-2.5	V
		_	V _{GS} = -10V, I _D = -4A	-	11	17	mΩ
Drain-Source On-Resistance ²		R _{DS(on)}	$V_{GS} = -4.5V, I_D = -2A$	-	16	25	
Forward Transconductance		g fs	V _{DS} = -5V, I _D = -5A	-	24	-	S
Dynamic Characteristics					•		•
Input Capacitance		Ciss		-	2710	-	
Output Capacitance Reverse Transfer Capacitance		Coss	V _{DS} = -15V, V _{GS} = 0V, f = 1MHz	-	331	-	pF
		Crss		-	279	-	
Switching Characteristic	S						
Gate Resistance		Rg	VDS= 0V , VGS= 0V , f= 1MHz	-	8.5	-	Ω
Total Gate Charge		Qg	V _{GS} = -4.5V, V _{DS} = -15V I _D = -2A	-	20	-	nC
Gate-Source Charge		Q _{gs}		-	5.1	-	
Gate-Drain Charge		Q _{gd}		-	7.3	-	
Turn-on Delay Time		t _{d(on)}	$V_{GS} = -10V, V_{DD} = -15V$ $R_G = 3.3\Omega, I_{D} = -1A$	-	33.8	-	- nS
Rise Time		tr		-	35.8	-	
Turn-off Delay Time Fall Time		t _{d(off)}		-	72.8	-	
		t _f	1	-	10.6	-	
Drain-Source Body Diode	e Characte	ristics					
Diode Forward Voltage ²		V _{SD}	$I_{S} = -1A, V_{GS} = 0V$	-	-	-1	V
Continuous Source Current ^{1,5}		Is	Vg=VD= 0V , Force Current	-	-	-9	А

Notes:

1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width \leq 300us , duty cycle $\leq 2\%$

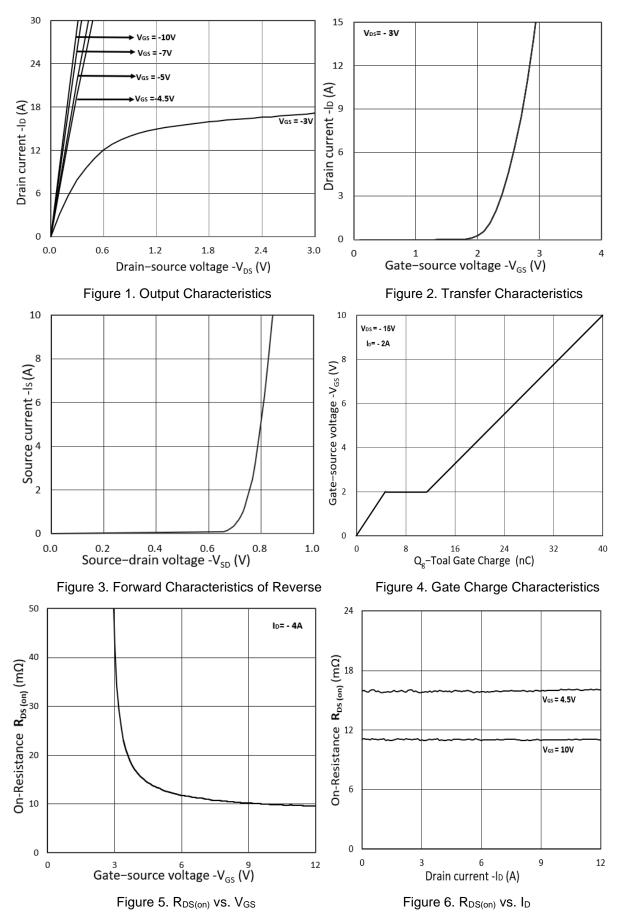
3. The EAS data shows Max. rating . The test condition is V_DD= -25V, V_GS= -10V, L=0.1mH, I_{AS}= -40A

4.The power dissipation is limited by 150°C $\,$ junction temperature

5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

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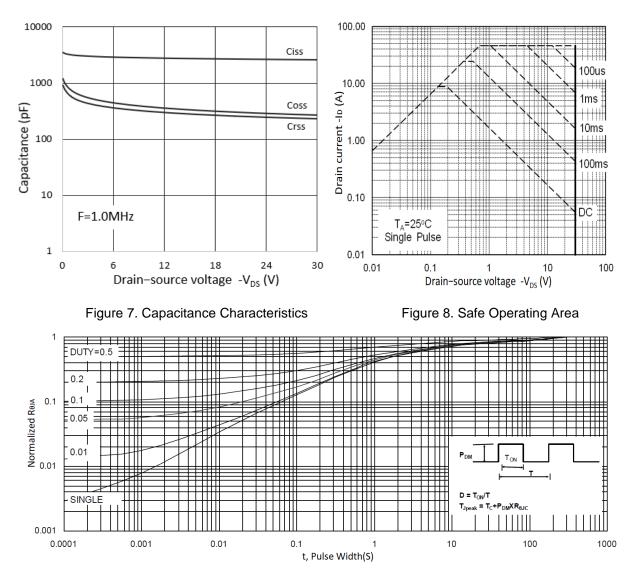
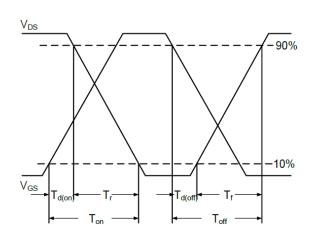
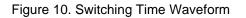
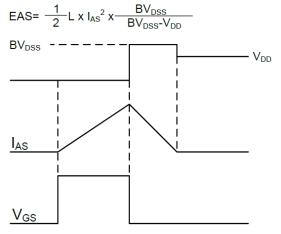
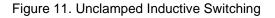


Figure 9. Normalized Maximum Transient Thermal Impedance



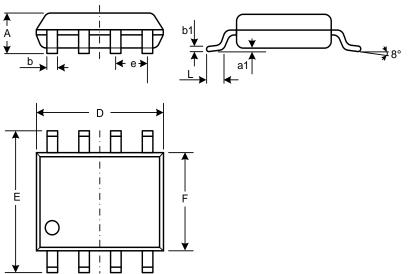






Waveform

Mechanical Dimensions for SOP-8L



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COMMON DIMENSIONS

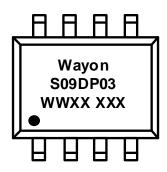
SYMBOL	ММ			
	MIN	MAX		
А	1.23	1.75		
a1	0.05	0.25		
b	0.31	0.51		
b1	0.16	0.25		
D	4.70	5.15		
E	5.75	6.25		
е	1.07	1.47		
F	3.70	4.10		
L	0.4	1.27		



Ordering Information

Part	art Package Marking		Packing method
WMS09DP03T1	SOP-8L	S09DP03	Tape and Reel

Marking Information



S09DP03 = Device code

WWXX XXX= Date code

Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207 Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com WAYON website: http://www.way-on.com For additional information, please contact your local Sales Representative.

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