

S2A - S2M

Voltage Range - 50 to 1000 V Forward Current - 2 Ampere

SURFACE MOUNT GLASS PASSIVATED RECTIFIER

FEATURES

- Plastic package has underwrites laboratory flammability Classification 94V-0
- For surface mounted applications
- Low profile package
- Built-in strain relief, ideal for automated placement
- Glass Passivated chip junction
- High temperature soldering guaranteed 250°C/10 second at terminals

MECHANICAL DATA

- Case: JEDED DO-214AA molded plastic over glass passivated chip
- Terminals: Solder plated, Solderable per MIL-STD-750, method
- 2026 Polarity: Color band denotes cathode end
- Weight: 0.002ounce, 0.064 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

■ Ratings at 25 °C ambient temperature unless otherwise specified

MAXIMUM RATINGS & THERMAL CHARACTERISTICS

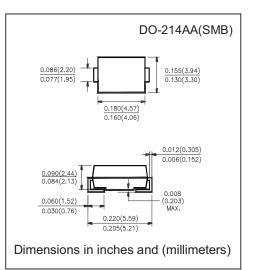
	SYMBOLS	S2A	S2B	S2D	S2G	S2J	S2K	S2M	UNIT
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	V _{DC}	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at T_L =100 °C	I _{F(AV)}	2.0							Amps
Peak Forward Surge Current 8.3ms single half sine wave superimposed on rated load (JEDEC method) $T_L=100^{\circ}C$	I _{FSM}	60							Amps
Typical Thermal Resistance (NOTE 1)	$R_{\theta JA}$	53							°C/W
	$R_{\theta JL}$	16							
Operating and Storage Temperature Range	T _J ,T _{STG}	-55 to +150							°C

ELECTRICAL CHARACTERISTICS

		SYMBOLS	S2A	S2B	S2D	S2G	S2J	S2K	S2M	UNIT
Maximum Instantaneous Forward Voltage at 2A		V _F	1.1							Volts
at rated DC Plasking Valtage	$T_A = 25$ °C	I _R	5.0							
	$T_A = 125 ^{\circ}\mathrm{C}$		125							
Typical Reverse Recovery Time at I_F =0.5A, I_R =1.0A, I_{RR} =0.25A,		T _{rr}				2.0				μs
Typical junction capacitance at 4.0V, 1MHz		CJ	30						pF	

Notes:

1. Thermal resistance from Junction to ambient and from junction to lead mounted on P.C.B.with 0.3×0.3 " (8.0×8.0 mm) copper pad areas.



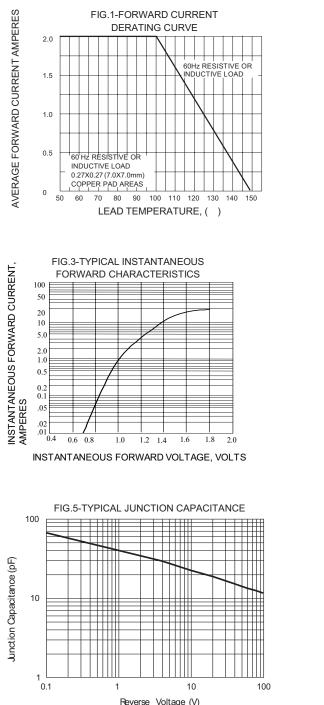


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RATINGS AND CHARACTERISTIC CURVES S2A - S2M



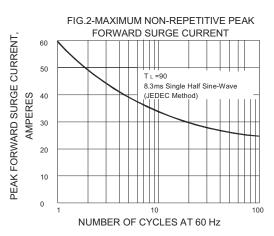
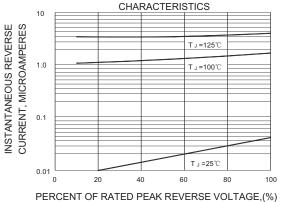


FIG.4-TYPICAL REVERSE



Reverse Voltage (V)

Disclaimer

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