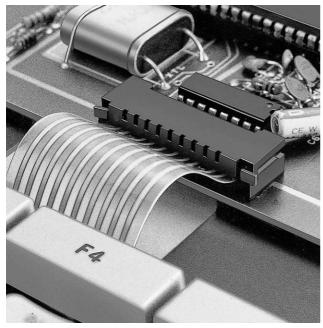


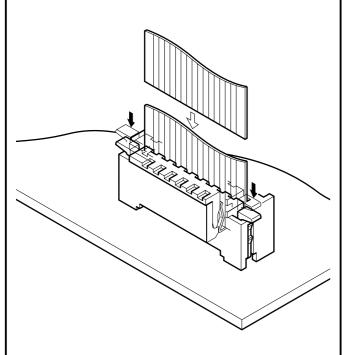
FDZ CONNECTOR



Connectors for FPC and membrane switch leads



The FDZ connector, a Zero Insertion Force (ZIF) connector, was developed to connect FPC and membrane switch leads. This combination of features allows greater freedom in internal wiring designs. No force is required to insert the leads. Once inserted, the leads are securely held and locked by moving the slide. The contact is double-sided to ensure greater reliability.



Features -

Double-sided contact

The contact is double-sided, thus providing redundant and highly reliable contact with FPC and membrane switch leads. This allows greater freedom in specifying the membrane switch lead conducting surfaces for use in keyboards.

Zero insertion force and locking mechanism

A ZIF mechanism increases wear resistance and extends the connector's mating life. The leads are securely connected by simply moving the slide. This greatly reduces the damage to the conducting surfaces of the leads. This mechanism is therefore ideal for silver-paste carbon-printed pattern leads which are susceptible to wear. The slide also has a hook to ensure secure contact and a tight hold.

• Temporary board retention feature

The contact solder tail has a board retention feature that allows the connector to be temporarily held on the printed circuit board while it is being soldered. As a result, the connector can be located accurately in the finished product.

Boss for polarity identification

A boss (projection) on the bottom of the housing prevents improper insertion in the printed circuit board and provides greater mounting stability.

• Flux is not a problem

The bottom of the connector housing is raised 2.0mm (.079") from the surface of the printed circuit board. This prevents flux being trapped under the connector. In addition, the central section of the contact has a special form that prevents flux from rising into the connector due to capillary action.

Caution: Flux level should be lower than the printed circuit board.

Specifications -

Current rating: Tin-plated copper foil lead/1.0A AC, DC

Silver paste carbon-printed lead/50mA AC, DC

• Voltage rating: 250V AC, DC

• Temperature range: -40°C to +85°C

(including temperature rise in applying

electrical current)

• Contact resistance: <Tin-plated copper foil lead>

Initial value/30m Ω max.

After environmental testing/40m Ω max.

<Silver paste carbon-printed lead>

Initial value/10 Ω max.

After environmental testing/15 Ω max.

• Insulation resistance: 1,000M Ω min.

Withstanding voltage: 1,200V AC/minute
 Applicable FDC and markets a suitable and markets.

Applicable FPC and membrane switch lead:

Lead pitch/2.54mm(.100") Lead width/1.3mm(.051")

Mating part thickness/125 micron(.005")

• Applicable PC board thickness: 1.6mm(.063")

<Note>FPC to be actually used should be checked for applicability.

* Contact JST if Lead-Free product is required.

* Refer to "General Instruction and Notice when using Terminals and Connectors" at the end of this catalog.

* Contact JST for details.

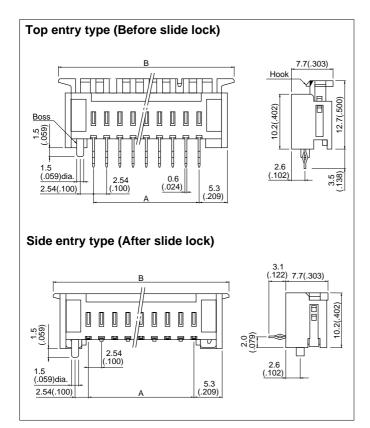
Standards -

Recognized E60389

⊕ Certified LR20812

FDZ CONNECTOR

Connector -



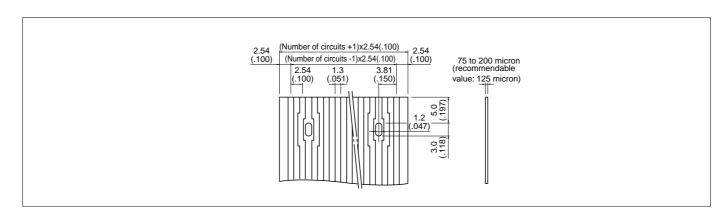
Circuits	Model No.		Dimensions mm(in.)		Q'ty /
	Top entry type	Side entry type	Α	В	bóx
5	05FDZ-BT(S)	05FDZ-ST(S)	10.16(.400)	23.56(.928)	100
6	06FDZ-BT(S)	06FDZ-ST(S)	12.70(.500)	26.10(1.028)	100
8	08FDZ-BT(S)	08FDZ-ST(S)	17.78(.700)	31.18(1.228)	100
9	09FDZ-BT(S)	09FDZ-ST(S)	20.32(.800)	33.72(1.328)	100
10	10FDZ-BT(S)	10FDZ-ST(S)	22.86(.900)	36.26(1.428)	100
11	11FDZ-BT(S)	11FDZ-ST(S)	25.40(1.000)	38.80(1.528)	50
12	12FDZ-BT(S)	12FDZ-ST(S)	27.94(1.100)	41.34(1.628)	50
13	13FDZ-BT(S)	13FDZ-ST(S)	30.48(1.200)	43.88(1.728)	50
17	17FDZ-BT(S)	17FDZ-ST(S)	40.64(1.600)	54.04(2.128)	50
19	19FDZ-BT(S)	_	45.72(1.800)	59.12(2.328)	50

Material and Finish

Contact: Phosphor bronze, copper-undercoated, tin/lead-plated Housing: Glass-filled PBT, UL94V-0, black

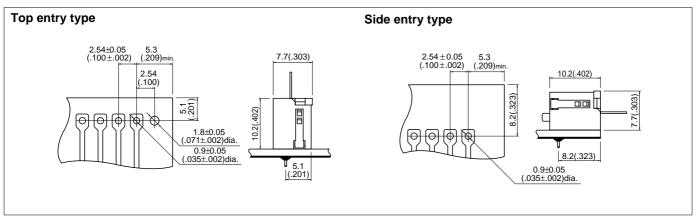
Note: 19FDZ-BT(S) is not UL/CSA approved.

Lead section dimensions of FPC



FDZ CONNECTOR

PC board layout (viewed from soldering side) and Assembly layout



Note:

- 1. Tolerances are non-cumulative: ±0.05mm(±.002") for all centers.
 2. Hole dimensions differ according to the kind of PC board and piercing method. The dimensions above should serve as a guideline. Contact JST for details.