

SANYO Semiconductors DATA SHEET

LA4629 — 2-channel AF Power Amplifier

Overview

The LA4629 is a 2-channel power amplifier developed for use in radio/cassette player products. The LA4629 reduces the number of required external components by 50% over earlier products (BS/NF capacitors and oscillation prevention RC components) and thus can contribute significantly to space saving in end products.

Features

- Provided in the SIP12H package.
- PO: $2.5W\times2$ (V_{CC} = 9V, R_L = 3 Ω), $4.5W\times2$ (V_{CC} = 12V, R_L = 3 Ω) : $2.0W\times2$ (V_{CC} = 9V, R_L = 4 Ω), $4.0W\times2$ (V_{CC} = 12V, R_L = 4 Ω)
- Standby function built in (supports direct microcontroller control).
- Built-in thermal protection circuit.

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max	No input	24	V
Allowable power dissipation	Pd max	With an arbitrarily large heat sink	25	W
Operating temperature	Topr		-25 to +75	°C
Storage temperature	Tstg		-40 to +150	°C

Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	VCC		12	V
Operating voltage range	V _{CC} op	The range over which the package Pd is not exceeded.	5 to 22	V
Recommended operating load resistance	R _L op		2.7 to 8	Ω

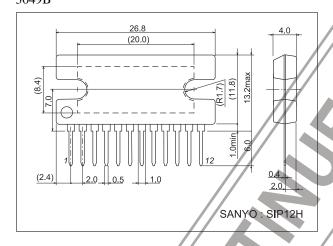
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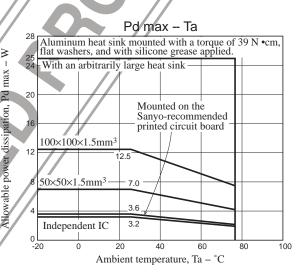
Electrical Characteristics at Ta = 25°C, $V_{CC} = 12V$, $R_L = 3\Omega$, f = 1kHz, $Rg = 600\Omega$

		0 155	Ratings				
Parameter	Symbol	Symbol Conditions		typ	max	Unit	
Quiescent current	Icco	Rg = 0	18	35	80	mA	
Standby current	I _{ST}			1.0	10	μА	
Voltage gain	V _G	V _O = 0dBm	43	45	47	dB	
Output power	P _O (1)	THD = 10%	3.0	4.5		W	
	P _O (2)	$V_{CC} = 12V, R_L = 4\Omega, THD = 10\%$		4.0		W	
	P _O (3)	V _{CC} = 9V, R _L = 3Ω, THD = 10%	2.0	2.5		W	
	P _O (4)	$V_{CC} = 9V, R_L = 4\Omega, THD = 10\%$		2.0		W	
Total harmonic distortion	THD	P _O = 1W		0.2	0.8	%	
Output noise voltage	V _{NO}	Rg = 0, DIN AUDIO		0.15	0.5	mV	
Ripple rejection ratio	SVRR	Rg = 0, f _R = 100Hz, Vr = 0dBm, DIN AUDIO	45	55		dB	
Channel separation	CHsep	Rg = 0, V _O = 0dBm, DIN AUDIO	43	50		dB	
Input resistance	Ri			30	40	kΩ	
Standby pin voltage	V _{ST}	The pin 6 voltage such that the amplifier is on	1.5	5.0		V	

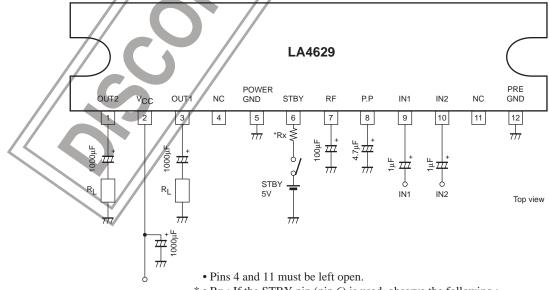
Package Dimensions

unit : mm (typ) 3049B



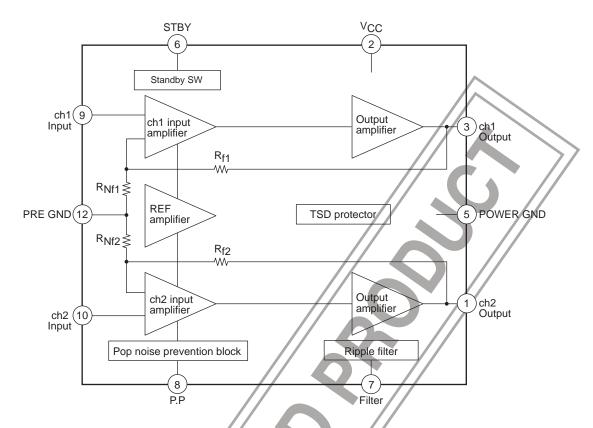


Pin Assignment and Application Circuit Example



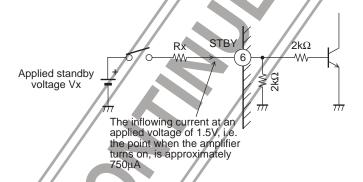
* • Rx : If the STBY pin (pin 6) is used, observe the following : Insert the resistor Rx in series to limit the inflowing current. (The amplifier will be on when a voltage is applied to pin 6.)

Block Diagram



Pin Functions

1. Standby switch function (pin 6)



STBY pin applied voltage: 5V

To hold the pin 6 inflow current to about 750 μ A insert a resistor (Rx) of 4.7k Ω

STBY pin applied voltage: 12V

To hold the pin 6 inflow current to about 750 μ A insert a resistor (Rx) of $14k\Omega$ ($12k\Omega$).

STBY pin applied voltage: Other value (Vx)

To hold the pin 6 inflow current to about $750\mu A$ insert a resistor (Rx) of (Vx - 1.5V)/750 μA .

- If a microcontroller output signal is applied directly, insert a resistor in series and adjust the current to a level optimal for the drive capability of the microcontroller.
- 2. Input pins (pins 9 and 10)

The input pin voltage is about 2V_{BE} (1.4V).

The input pin impedance is about $30k\Omega$.

• Although the recommended value for the input capacitor is 0.22µF, the starting time can be modified by changing the value of this capacitor. (The time from the point a voltage is applied to the standby pin to the point sound is emitted.)

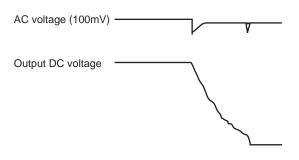
Input capacitor	1.0μF	2.2μF	3.3μF	4.7μF	10μF
Starting time (t _S)	0.2s	0.3s	0.5s	0.65s	1.5s

3. FILTER (decoupling) pin (pin 7)

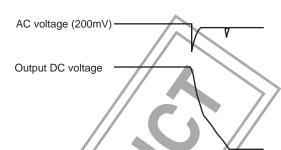
The pin voltage is about 1/2VCC.

The recommended value for the filter capacitor is $100\mu F$.

The pulse noise that occurs when the standby pin is set low (power off) will be degraded if a value under $100\mu F$ is used.



Filter capasitor = 100μ F

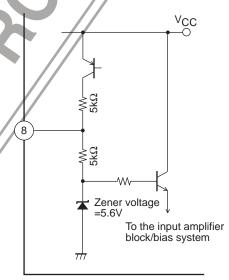


Filter capasitor = 47μ F

4. P.P (pulse noise) pin (pin 8)

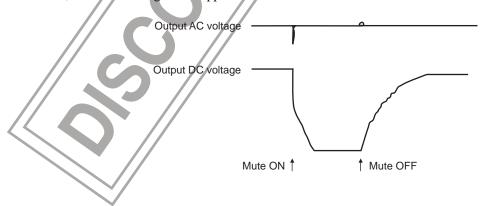
Pin 8 pin voltage
$$\approx \frac{V_{CC} - V_{CE} \text{ (about 0.3V)} - 5.6V}{2k\Omega} + 5.6V$$

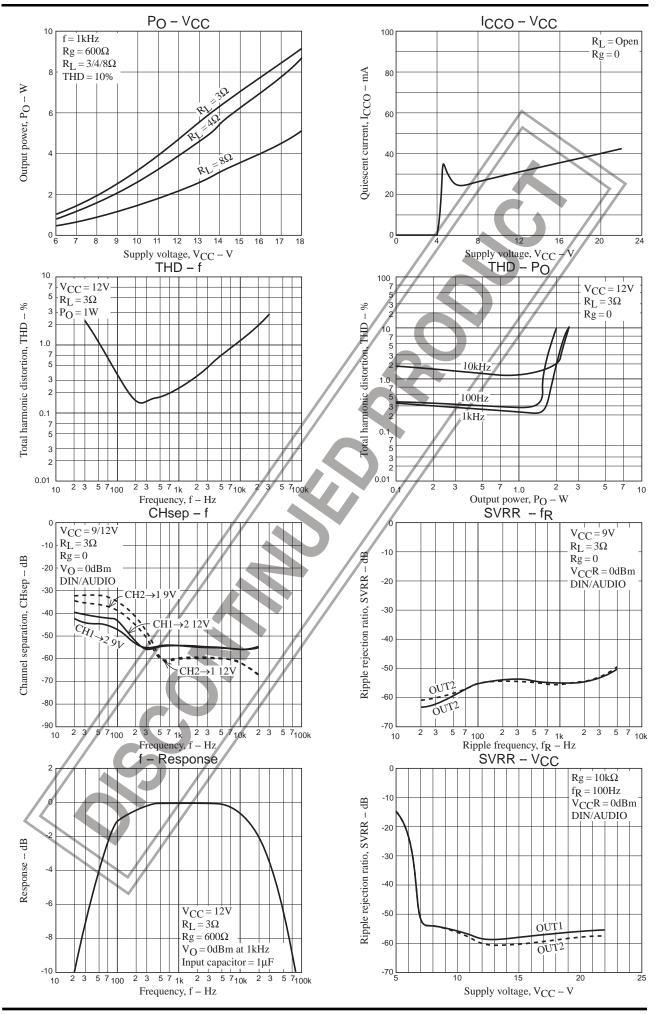
• The recommended value for the P.P capacitor is $4.7\mu F$. The pulse noise that occurs when the standby pin is set low (power off) will be degraded if a value under $2.2\mu F$ is used. Furthermore, if a value over $10\mu F$ is used, the signal may not be cut off and sound may remain audible when the standby pin is set low (power off).



5. MUTE (Muting)

The output signal can be controlled by shifting the pin 7 (FILTER) level towards ground with a 300 to 500Ω resistor. However, note that the degree of suppression is reduced if a value of 750Ω or more is used.







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