



STM3210B-PFSTICK

Hitex STM32-PerformanceStick complete tool package for STM32 evaluation and development

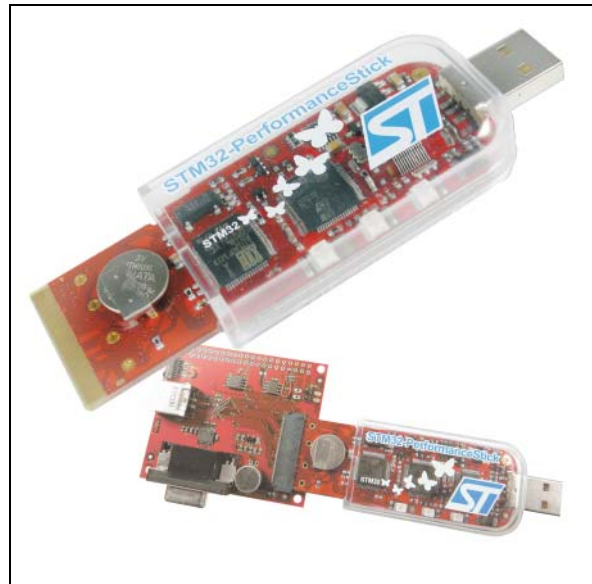
Data Brief

Features

- HiTOP5 integrated development environment with project manager, editor and high-level language debugging
- Tasking VX C/C++ compiler, no code size limitations
- STM32-PerformanceStick with STM32F103B and:
 - In-circuit debugging/programming via dedicated USB connection to the host PC
 - Connector for extension I/O boards
 - Auxiliary power supply from 3V button battery
- Optional extension I/O board with features for peripheral evaluation (USB, CAN, USART, IrDA, ...)
- DashBoard interface for modifying STM32 parameters and viewing performance indicators
- C source code for all sample applications and libraries including:
 - USB mouse
 - Vectored interrupt controller and timer
 - ADC and I/Os
 - CAN monitor and generator
 - STM32 library from STMicroelectronics

Description

The **Hitex STM32-PerformanceStick** is a complete, low-cost evaluation and development package that provides a fast and easy introduction to the capabilities of ST's ARM[®] Cortex[™]-M3 core-based STM32 family of microcontrollers. It is specifically designed to help application designers explore STM32 features and performance characteristics (low power modes, clock controls, wake up states, etc.), but can also be connected to extension boards with



hardware features for evaluation of device peripherals or development of an application.

The Hitex software toolset for STM32 includes everything developers need to modify and rebuild the sample applications (C sources included) and to develop their own applications using the STM32-PerformanceStick. The software includes **HiTOP5**, Hitex's integrated development environment that drives the hardware and offers a full range of project management, source code editing and debugging features from an intuitive graphical interface. For more information about HiTOP5 downloads and updates, refer to the Hitex website.

The software also includes the Altium **Tasking VX** C compiler for compiling application source files. When using the STM32-PerformanceStick, the compiler has no output code-size limitations.

The **DashBoard** graphical interface allows users to modify STM32 configuration and view performance information. This is possible thanks to the innovative hardware design of the **STM32-PerformanceStick**, which retrieves STM32 and peripheral performance information in real time.

The evaluation hardware includes the STM32-PerformanceStick with integrated debugging and programming capability via a dedicated USB interface. An extension I/O board connector provides access to signals on the STM32 pins and allows connection of extension boards with hardware features for peripheral evaluation, or the developer's own application board. Power supply is provided via a USB connection with the host PC.

An **extension I/O board** is available for connection to the STM32-PerformanceStick, and provides hardware features such as USB, CAN, USART connectors and IrDA sensor for evaluation of device peripherals. The extension I/O board is provided with the STM32-PerformanceStick in the Hitex STM32 starter kit (ST order code: STM3210B-SK/HIT).

Ordering information

The STM32-PerformanceStick is available from Hitex, or can be ordered from STMicroelectronics' sales offices and distributors using the order code:

STM3210B-PFSTICK – STM32-PerformanceStick with Hitex STM32 software tools (HiTOP5, Tasking VX compiler), DashBoard graphical interface and sample application code implementing a range of device peripherals.

Note: The STM32-PerformanceStick is also available as part of the Hitex starter kit for STM32. The kit is available from Hitex, or can be ordered from STMicroelectronics' sales offices and distributors (ST order code: STM3210B-SK/HIT). For further information, refer to the data brief "Hitex starter kits for ARM core-based microcontrollers."

For more information and complete documentation, please refer to the Hitex web site or the STMicroelectronics microcontroller support site, www.st.com.

Revision history

Table 1. Document revision history

Date	Revision	Changes
8-Oct-2007	1	Initial release.

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