

April 2012

## ES3A - ES3J

## **Fast Rectifiers**

## **Features**

- For surface mount applications.
- Glass passivated junction.
- · Low profile package.
- · Easy pick and place.
- Built-in strain relief.
- Superfast recovery times for high efficiency.



# **Absolute Maximum Ratings \*** $T_a = 25$ °C unless otherwise noted

Symbol	Parameter		Units			
		ES3A	ES3B	ES3C	ES3D	ES3J
V <sub>RRM</sub>	Maximum Repetitive Reverse Voltage	50 100 150 200 600				V
I <sub>F(AV)</sub>	Average Rectified Forward Current, .375" lead length @ T <sub>a</sub> = 75°C	3.0				Α
I <sub>FSM</sub>	Non-repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine-Wave	100		А		
T <sub>J,</sub> T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-50 to +150		°C		

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may by impaired.

## **Thermal Characteristics**

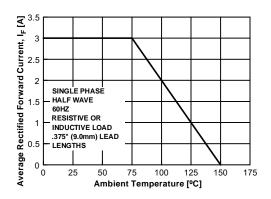
Symbol	Parameter	Value	Units
P <sub>D</sub>	Power Dissipation	1.66	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient *	47	°C/W
$R_{\theta JL}$	Thermal Resistance, Junction to Lead *	12	°C/W

<sup>\*</sup> Device mounted on FR-4 PCB 0.013 mm.

# **Electrical Characteristics** $T_a = 25$ °C unless otherwise noted

Symbol	Parameter		Value					Units
Symbol	Farameter			ES3B	ES3C	ES3D	ES3J	Units
V <sub>F</sub>	Maximum Forward Voltage @ I <sub>F</sub> = 3.0 A			0.95 1				V
т	Reverse Recovery Time $I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{RR} = 0.25 \text{ A}$		20 35				ns	
T <sub>rr</sub>			30 45					ns
	Maximum Reverse Current @ rated V <sub>R</sub>							
I <sub>R</sub>	$T_a = 25$ °C		10					μΑ
	T <sub>a</sub> = 100°C		500				μΑ	
C <sub>T</sub>	Total Capacitance $V_R = 4.0 \text{ V}, f = 1.0 \text{ MHz}$			45				

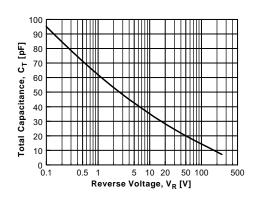
## **Typical Performance Characteristics**



100 Forward Current, I<sub>F</sub>[A] 10 0.1 T<sub>4</sub>=25 °C Pulse Width = 300us 2% Duty Cycle 0.01 ---0.6 0.8 1.2 1.6 1.0 Forward Voltage Drop,  $V_F[V]$ 

**Figure 1. Foward Current Deration Curve** 





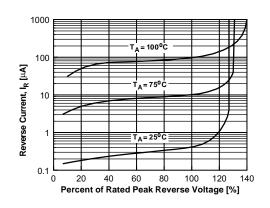
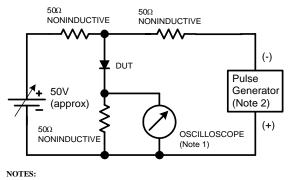
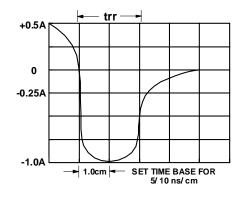


Figure 3. Total Capacitance

Figure 4. Reverse Current vs Reverse Voltage



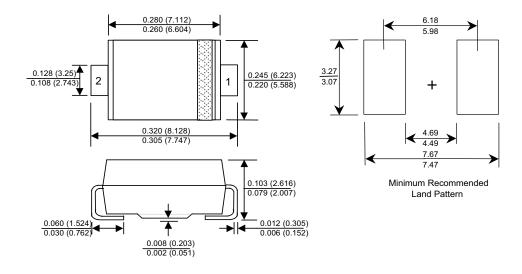


1. Rise time = 7.0 ns max; Input impedance = 1.0 megaohm 22 pf. 2. Rise time = 10 ns max; Source impedance = 50 ohms.

**Reverse Recovery Time Characterstic and Test Circuit Diagram** 

# **Package Dimensions**

# SMC / DO - 214AB



Dimensions in Inches(Millimeters)



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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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Rev. I61