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October 2035

### ISL9V3040D3S / ISL9V3040S3S / ISL9V3040P3 / ISL9V3040S3

### EcoSPARK<sup>a</sup> 300mJ, 400V, N-Channel Ignition IGBT

#### **General Description**

The ISL9V3040D3S, ISL9V3040S3S, ISL9V3040P3, and ISL9V3040S3 are the next generation ignition IGBTs that offer outstanding SCIS capability in the space saving D-Pak (TO-252), as well as the industry standard D<sup>2</sup>-Pak (TO-263), and TO-262 and TO-220 plastic packages. This device is intended for use in automotive ignition circuits, specifically as a coil driver. Internal diodes provide voltage clamping without the need for external components.

**EcoSPARK¤** devices can be custom made to specific clamp voltages. Contact your nearest Fairchild sales office for more information.

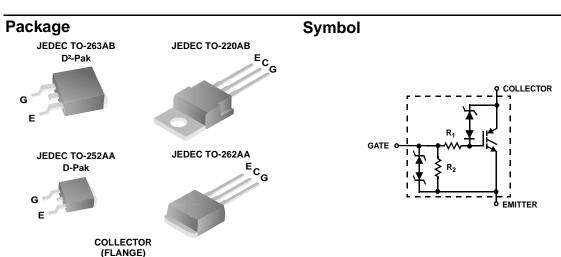
Formerly Developmental Type 49362

#### **Applications**

- Automotive Ignition Coil Driver CircuitsCoil- On Plug Applications

#### Features

- · Space saving D-Pak package availability
- SCIS Energy = 300mJ at T<sub>1</sub> =  $25^{\circ}$ C
- Logic Level Gate Drive



#### Device Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

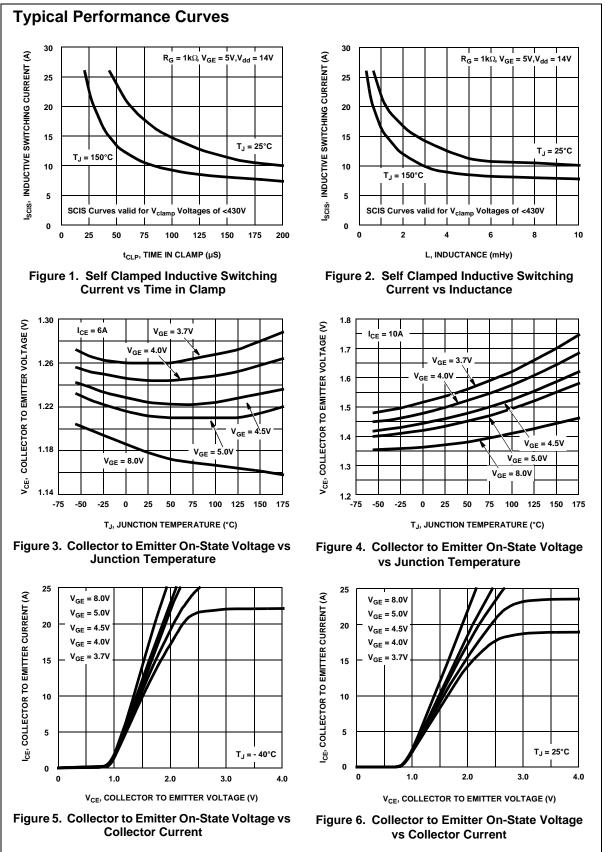
Symbol	Parameter	Ratings		
BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage (I <sub>C</sub> = 1 mA)	430	V	
BV <sub>ECS</sub>	Emitter to Collector Voltage - Reverse Battery Condition (I <sub>C</sub> = 10 mA)	24	V	
E <sub>SCIS25</sub>	At Starting $T_J$ = 25°C, $I_{SCIS}$ = 14.2A, L = 3.0 mHy	300	mJ	
E <sub>SCIS150</sub>	At Starting T <sub>J</sub> = 150°C, I <sub>SCIS</sub> = 10.6A, L = 3.0 mHy	170	mJ	
I <sub>C25</sub>	Collector Current Continuous, At T <sub>C</sub> = 25°C, See Fig 9	21	Α	
I <sub>C110</sub>	Collector Current Continuous, At T <sub>C</sub> = 110°C, See Fig 9	17	Α	
$V_{GEM}$	GEM Gate to Emitter Voltage Continuous		V	
PD	Power Dissipation Total T <sub>C</sub> = 25°C	150	W	
Power Dissipation Derating T <sub>C</sub> > 25°C		1.0	W/°C	
ТJ	Operating Junction Temperature Range	-40 to 175	°C	
T <sub>STG</sub> Storage Junction Temperature Range		-40 to 175	°C	
T <sub>L</sub> Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)		300	°C	
T <sub>pkg</sub> Max Lead Temp for Soldering (Package Body for 10s)		260	°C	
ESD	Electrostatic Discharge Voltage at 100pF, 1500 $\Omega$	4	kV	

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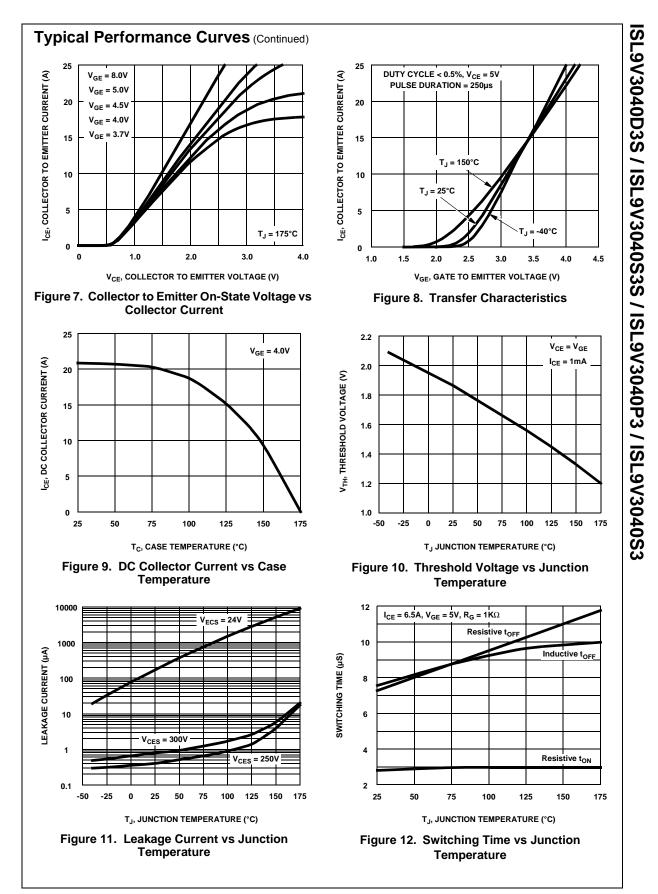
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Device Marking		Device		ackage	Reel Size	Tape Width		Quantity	
V3040	V3040D ISL9V3040D3ST TO		D-252AA	330mm	16mm		2500		
V3040S ISL9V3040S3ST		TC	D-263AB	330mm	24mm		800		
V3040P ISL9V3040P3			D-220AA	Tube	N/A		50		
			TO-262AA Tube		N/A		50		
V304		ISL9V3040D3S		D-252AA	Tube	N/A		75	
V304		ISL9V3040S3S		D-263AB	Tube		N/A		50
Symbol		Parameter	Cun	Test Con		Min	Тур	Max	Units
f State	Charact	eristics							
BV <sub>CER</sub>	1	ector to Emitter Breakdown Voltage		$I_C = 2mA$ , $V_{GE} = 0$ , $R_G = 1K\Omega$ , See Fig. 15 $T_1 = -40$ to 150°C		370	400	430	V
BV <sub>CES</sub>	Collector	r to Emitter Breakdown Voltage		$I_{C} = 10mA, V_{GE} = 0,$ $R_{G} = 0, See Fig. 15$ $T_{J} = -40 \text{ to } 150^{\circ}\text{C}$		390	420	450	V
BV <sub>ECS</sub>	Emitter t	o Collector Breakdown Vol	tage	$I_{C} = -75$ mA, $V_{GE} = 0$ V, $T_{C} = 25$ °C		30	-	-	V
$BV_{GES}$	Gate to I	Emitter Breakdown Voltage		I <sub>GES</sub> = ± 2mA		±12	±14	-	V
I <sub>CER</sub>	Collector	to Emitter Leakage Curre	nt	V <sub>CER</sub> = 250V,	T <sub>C</sub> = 25°C	-	-	25	μA
				R <sub>G</sub> = 1KΩ, See Fig. 11	T <sub>C</sub> = 150°C	-	-	1	mA
I <sub>ECS</sub>	Emitter t	o Collector Leakage Curre	nt	$V_{EC} = 24V$ , See		-	-	1	mA
				Fig. 11	T <sub>C</sub> = 150°C	-	-	40	mA
R <sub>1</sub>		Bate Resistance				-	70	-	Ω
R <sub>2</sub>		Emitter Resistance				10K	-	26K	Ω
n State (				•					
V <sub>CE(SAT)</sub>	Collector	llector to Emitter Saturation Voltage		I <sub>C</sub> = 6A, V <sub>GE</sub> = 4V	T <sub>C</sub> = 25°C, See Fig. 3	-	1.25	1.60	V
V <sub>CE(SAT)</sub>	Collector	ollector to Emitter Saturation Voltage		I <sub>C</sub> = 10A, V <sub>GE</sub> = 4.5V	T <sub>C</sub> = 150°C, See Fig. 4	-	1.58	1.80	V
V <sub>CE(SAT)</sub>	Collector	ector to Emitter Saturation Voltage		I <sub>C</sub> = 15A, V <sub>GE</sub> = 4.5V	T <sub>C</sub> = 150°C	-	1.90	2.20	V
/namic (	Charact	eristics							
Q <sub>G(ON)</sub>	Gate Ch	arge		I <sub>C</sub> = 10A, V <sub>CE</sub> = 12V, V <sub>GE</sub> = 5V, See Fig. 14		-	17	-	nC
V <sub>GE(TH)</sub>	Gate to	Emitter Threshold Voltage		-	T <sub>C</sub> = 25°C	1.3	-	2.2	V
				V <sub>CE</sub> = V <sub>GE,</sub> See Fig. 10	T <sub>C</sub> = 150°C	0.75	-	1.8	V
V <sub>GEP</sub>	Gate to	Emitter Plateau Voltage		I <sub>C</sub> = 10A, V <sub>CE</sub> :	= 12V	-	3.0	-	V
	Charao	cteristics							
t <sub>d(ON)R</sub>	Current	Turn-On Delay Time-Resis	tive	V <sub>CE</sub> = 14V, R <sub>L</sub> = 1Ω,		-	0.7	4	μs
t <sub>rR</sub>	Current	Rise Time-Resistive		$V_{GE} = 5V$ , $R_G = 1K\Omega$ $T_J = 25^{\circ}C$ , See Fig. 12		-	2.1	7	μs
t <sub>d(OFF)L</sub>		Turn-Off Delay Time-Induc	tive	V <sub>CE</sub> = 300V, L = 500µHy,		-	4.8	15	μs
t <sub>fL</sub>	Current	Fall Time-Inductive		V <sub>GE</sub> = 5V, R <sub>G</sub> = 1KΩ T <sub>J</sub> = 25°C, See Fig. 12		-	2.8	15	μs
SCIS	Self Clar	mped Inductive Switching		$T_J = 25^{\circ}C$ , L = 3.0 mHy, R <sub>G</sub> = 1K $\Omega$ , V <sub>GE</sub> = 5V, See Fig. 1 & 2		-	-	300	mJ
nermal C	haracte	eristics							
$R_{\theta JC}$	Thormol	Resistance Junction-Case	2	All packages		-	-	1.0	°C/W

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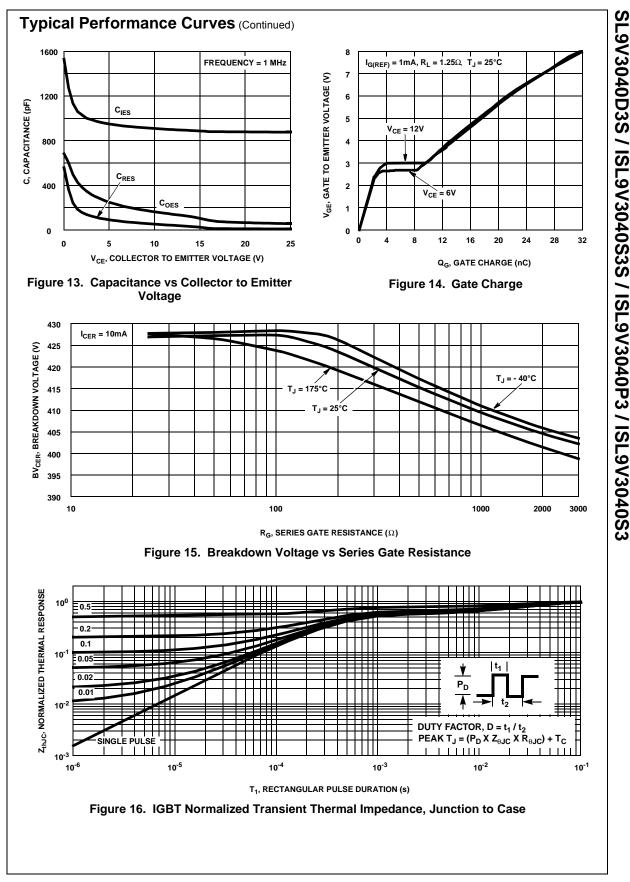


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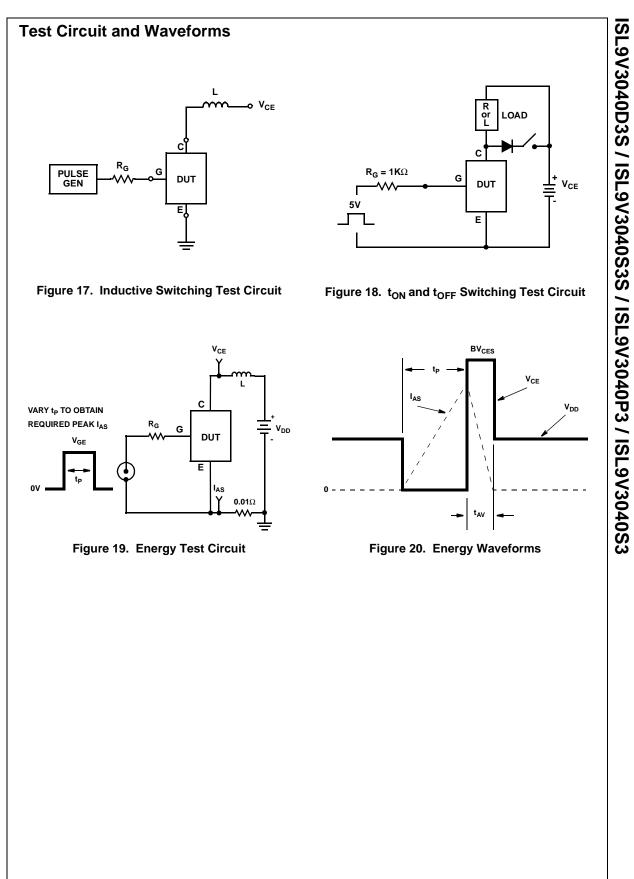
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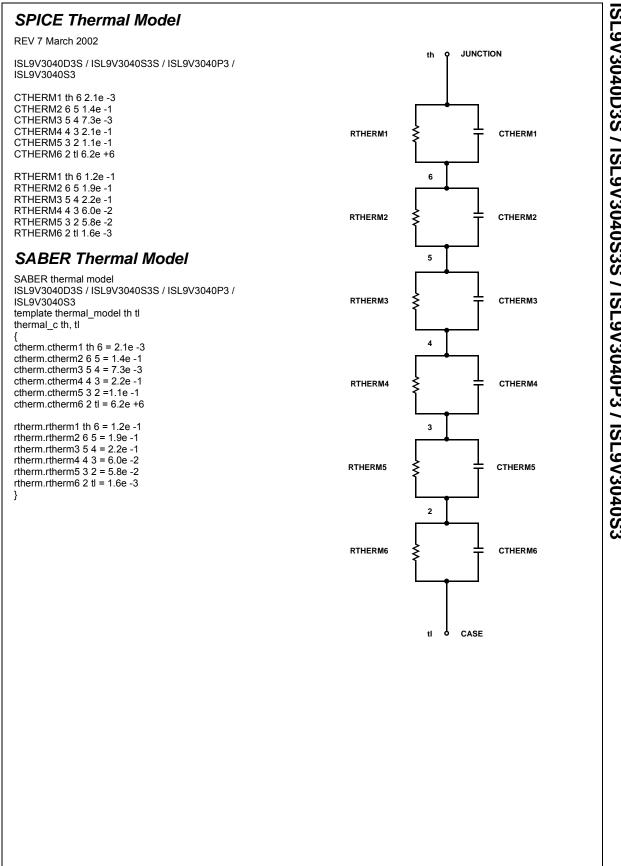
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