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AN5279

Mono channel SEPP audio power amplifier IC

■ Overview

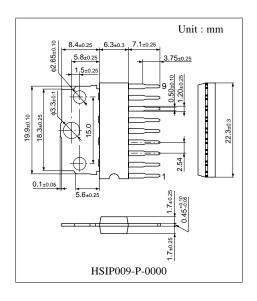
The AN5279 is a monolithic integrated circuit designed for $5.0~W~(19~V, 8~\Omega)$ output audio power amplifier. It is a mono channel SEPP IC suitable for TV application.

■ Features

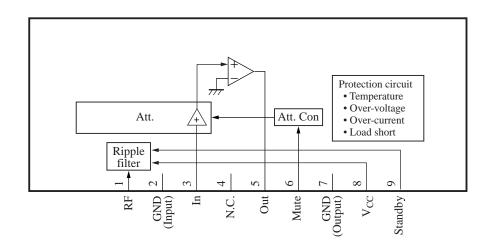
- Few external components :
 - No Boucherot cells(output C, R)
 - No Bootstrap Capacitors
 - No Negative Feeback Capacitors
- Built-in muting circuit
- Built-in stand-by circuit
- Built-in various protection circuits (Load-short, thermal, over-voltage and current)
- High ripple rejection(55 dB)
- Operating voltage range 10 V to 24 V(19 V typ.)

■ Applications

• TV



■ Block Diagram



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■ Pin Descriptions

Pin No.	Descriptions	Pin No.	Descriptions
1	Ripple filter	6	Mute
2	Input GND	7	Output GND
3	Input	8	V _{CC}
4	Not connected	9	Standby
5	Output		

■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage	V _{CC}	26.0	V
Supply current	I_{CC}	1.6	A
Power dissipation *2	P_{D}	6.2	W
Operating ambient temperature *1	T_{opr}	-25 to +75	°C
Storage temperature *1	T_{stg}	-55 to +150	°C

Note) *1: Except these items, all other measurements are taken at $T_a = 25$ °C.

■ Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V _{CC}	10.0 to 24.0	V

^{*2:} $T_a = 75$ °C with infinite heat sink.

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\blacksquare Electrical Characteristics at V_{CC} = 19 V, f = 1 kHz, R_L = 8 $\Omega,$ T_a = 25 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Quiescent current	I_{CQ}	$V_{IN} = 0 \text{ mV}$	_	25	50	mA
Output end noise voltage *1	V _{NO}	No input, $R_g = 10 \text{ k}\Omega$	_	0.22	0.4	mV
Voltage gain	G_{V}	$V_{IN} = 57 \text{ mV}$	32	34	36	dB
Total harmonic distortion	THD	$V_{IN} = 57 \text{ mV}$	_	0.2	0.4	%
Maximum Output Power	P _{O1}	V _{CC} = 19 V, THD = 10 %	4.0	5.0	_	W
Maximum Output power	P _{O2}	V _{CC} = 22 V, THD = 10 %	5.6	7.0	_	W
Ripple rejection ratio *1	RR	$V_r = 1 V_{rms}$	45	55	_	dB
		$f_r = 120 \text{ Hz}, R_g = 10 \text{ k}\Omega$				
Muting Ratio	MR	$V_{IN} = 57 \text{ mV}, V_{MUTE} > 3.0 \text{ V}$	70	80	_	dB
Muting control voltage	V _{MUTE}	$V_{IN} = 57 \text{ mV}, MR > 70 \text{ dB}$	3.0	_	_	V
Standby on voltage	V _{STD-ON}	No input, I _{CC} ≤ 0.1 mA	_	_	5.0	V
Standby off voltage	V _{STD-OFF}	No input, $I_{CC} \ge 9.5 \text{ mA}$	8.5	_	_	V

Note) *1: For this measurement, use the 20 Hz to 20 kHz (12 dB/OCT) filter.

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■ Terminal Equivalent Circuits

Pin No.	Equivalent circuit	Description	DC voltage (V)
1	30 kΩ 1 Cy 51 Cy 251 C	Ripple Filter This is the pin to connect the positive terminal of a ripple filter capacitor.	V _{CC} - 1.5V _{BE}
2	_	Input GND Input ground pin	0
3	3 200 Ω 400 Ω 30 kΩ	Input This is the amplifier input pin.	0
4	<u> </u>	Not connected	_
5	Pre amp. Driver Cct $\sqrt{5}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{7}$ $\sqrt{7}$ $\sqrt{8}$ $\sqrt{8}$ $\sqrt{9}$ \sqrt	Output Output pin	V _{CC} /2

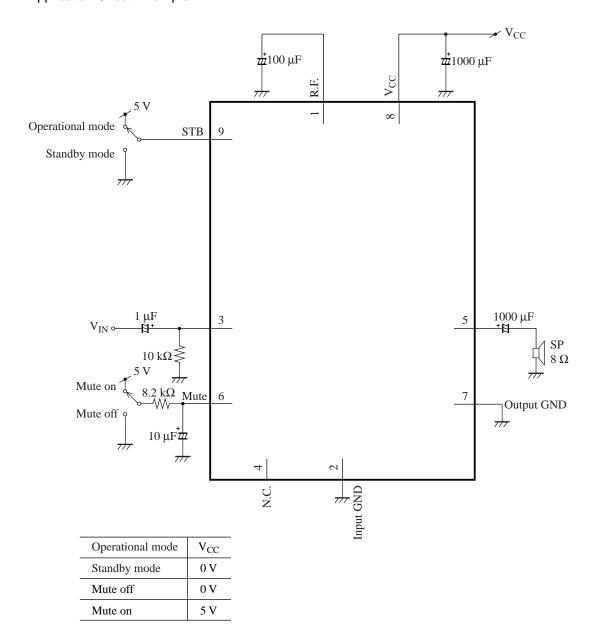
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■ Terminal Equivalent Circuits (continued)

Pin No.	Equivalent circuit	Description	DC voltage (V)
6	$\begin{array}{c} 3 \text{ k}\Omega \\ \hline \\ 3 \text{ k}\Omega \\ \hline \\ \end{array}$	Mute Mute input pin. Mute 'on' = 5 V Mute 'off' = 0 V	
7	_	Output GND Output ground.	0
8	_	$V_{\rm CC}$ This is the power supply pin.	19 V(typ.)
9	9 5 kΩ 5 kΩ 3 kΩ	$\begin{array}{l} Standby \\ Standby \ control \ pin. \\ Standby \ mode = 0 \ V \\ Operational \ mode = V_{CC} \end{array}$	

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■ Application Circuit Example



■ Usage notes

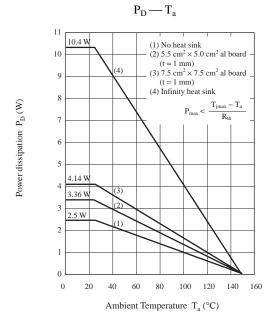
- 1) External heatsink is needed when used. External heatsink should be fixed to the chassis.
- 2) Fin of the IC can be connected to GND.
- 3) Please prevent "Output to V_{CC} short", "Output to GND short" and "Reverse Insertion" to avoid damaging the IC.
- 4) The temperature protection circuit will operate at T_j around 150 °C. However, if temperature decreases, the protection circuit would automatically be deactivated and resume normal operation.

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■ Technical Information

• $P_D - T_a$ curves of HSI P009-P-0000



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