# **74AUP1G14**

# Low-power Schmitt trigger inverter

Rev. 10 — 14 January 2022

**Product data sheet** 

## 1. General description

The 74AUP1G14 is a single inverter with Schmitt-trigger input. This device ensures very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- High noise immunity
- Overvoltage tolerant inputs to 3.6 V
- Low static power consumption; I<sub>CC</sub> = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8C (2.7 V to 3.6 V)
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 Class 3A exceeds 5000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 1000 V
  - MM: JESD22-A115-A exceeds 200 V
- · Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

# 3. Applications

- Wave and pulse shaper
- Astable multivibrator
- Monostable multivibrator



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# 4. Ordering information

**Table 1. Ordering information** 

Type number	Package	Package								
	Temperature range	Name	Description	Version						
74AUP1G14GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1						
74AUP1G14GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753						
74AUP1G14GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886						
74AUP1G14GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115						
74AUP1G14GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202						
74AUP1G14GX	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.32 mm	SOT1226-3						
74AUP1G14GX4	-40 °C to +125 °C	X2SON4	plastic thermal enhanced extremely thin small outline package; no leads; 4 terminals; body 0.6 × 0.6 × 0.32 mm	SOT1269-2						

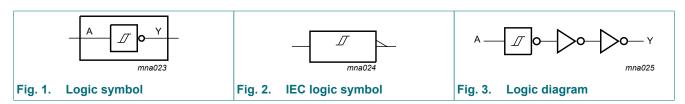
# 5. Marking

Table 2. Marking

Table 2. Marking						
Type number	Marking code[1]					
74AUP1G14GW	pF					
74AUP1G14GV	pF					
74AUP1G14GM	pF					
74AUP1G14GN	pF					
74AUP1G14GS	pF					
74AUP1G14GX	pF					
74AUP1G14GX4	pF					

<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

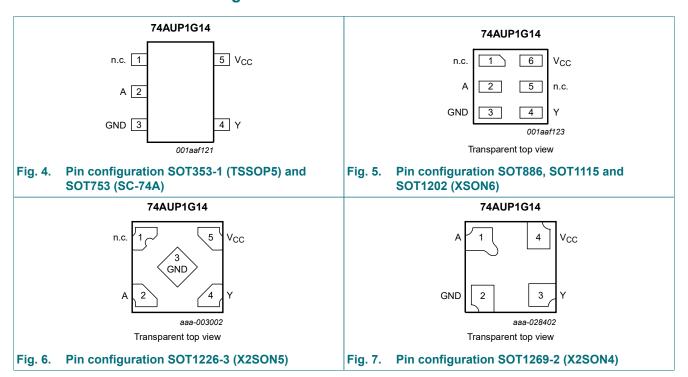
# 6. Functional diagram



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# 7. Pinning information

## 7.1. Pinning



## 7.2. Pin description

Table 3. Pin description

Symbol	Pin	Pin				
	TSSOP5, SC-74A and X2SON5	XSON6	X2SON4			
n.c.	1	1, 5	-	not connected		
A	2	2	1	data input		
GND	3	3	2	ground (0 V)		
Υ	4	4	3	data output		
V <sub>CC</sub>	5	6	4	supply voltage		

# 8. Functional description

#### **Table 4. Function table**

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$ 

Input	Output
A	Υ
L	Н
Н	L

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## 9. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V		-50	-	mA
Vo	output voltage	Active mode and Power-down mode	[1]	-0.5	+4.6	V
Io	output current	$V_O = 0 V \text{ to } V_{CC}$		-	±20	mA
I <sub>CC</sub>	supply current			-	+50	mA
$I_{GND}$	ground current			-50	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C				
		TSSOP5, SC-74A, XSON6 and X2SON5 package	[2]	-	250	mW
		X2SON4 package	[3]	-	150	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For SOT753 (SC-74A) package: Ptot derates linearly with 3.8 mW/K above 85 °C.

For SOT886 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package:  $P_{tot}$  derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

For SOT1226-3 (X2SON5) package:  $P_{tot}$  derates linearly with 3.0 mW/K above 67 °C.

# 10. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
V <sub>O</sub>	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C

<sup>[2]</sup> For SOT353-1 (TSSOP5) package: P<sub>tot</sub> derates linearly with 3.3 mW/K above 74 °C.

<sup>[3]</sup> For SOT1269-2 (X2SON4) package: P<sub>tot</sub> derates linearly with 1.7 mW/K above 57 °C.

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# 11. Static characteristics

#### **Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$T_{amb} = 2$	25 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		$I_{O}$ = -2.3 mA; $V_{CC}$ = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		$I_{O}$ = -2.7 mA; $V_{CC}$ = 3.0 V	2.72	-	-	V
		$I_{O}$ = -4.0 mA; $V_{CC}$ = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_O = 20 \mu A; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.1	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.2	μΑ
Δl <sub>OFF</sub>	additional power-off leakage current	$V_1$ or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.2	μΑ
I <sub>CC</sub>	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μΑ
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μΑ
Cı	input capacitance	$V_I$ = GND or $V_{CC}$ ; $V_{CC}$ = 0 V to 3.6 V	-	1.1	-	pF
Co	output capacitance	V <sub>O</sub> = GND; V <sub>CC</sub> = 0 V	-	1.7	-	pF
T <sub>amb</sub> = -	40 °C to +85 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		$I_O = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = 20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
I	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.5	μΑ
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.5	μΑ
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1$ or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.6	μΑ
I <sub>CC</sub>	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μΑ
$\Delta I_{CC}$	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 3.3 V	-	-	50	μΑ
T <sub>amb</sub> = -4	40 °C to +125 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
l <sub>l</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.75	μΑ
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.75	μΑ
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1$ or $V_0 = 0 V$ to 3.6 V; $V_{CC} = 0 V$ to 0.2 V	-	-	±0.75	μΑ
I <sub>CC</sub>	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μΑ
$\Delta I_{CC}$	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μΑ

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# 12. Dynamic characteristics

### **Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9.

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 5 p	C <sub>L</sub> = 5 pF									
t <sub>pd</sub>	propagation	A to Y; see <u>Fig. 8</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	19.9	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.7	5.9	11.0	2.4	11.1	2.4	11.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.6	4.3	6.6	2.4	7.1	2.4	7.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.1	3.7	5.4	2.0	6.0	2.0	6.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.0	3.0	4.1	1.7	4.5	1.7	4.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.9	2.8	3.6	1.5	3.9	1.5	4.0	ns
C <sub>L</sub> = 10	pF									
t <sub>pd</sub>	propagation	A to Y; see <u>Fig. 8</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	23.4	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.9	6.8	12.7	2.8	12.8	2.8	12.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.8	5.0	7.7	2.6	8.2	2.6	8.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.7	4.2	6.2	2.5	6.7	2.5	7.1	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.3	3.6	4.8	2.1	5.2	2.1	5.5	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.1	3.3	4.3	2.0	4.5	2.0	4.7	ns
C <sub>L</sub> = 15	pF									
t <sub>pd</sub>	propagation	A to Y; see <u>Fig. 8</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	26.9	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.3	7.6	14.3	3.0	14.5	3.0	14.7	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.3	5.5	8.6	2.9	9.4	2.9	9.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.8	4.7	7.0	2.8	7.7	2.8	8.1	ns
		$V_{CC}$ = 2.3 V to 2.7 V	2.7	4.0	5.5	2.4	5.9	2.4	6.2	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	2.6	3.8	4.8	2.2	5.2	2.2	5.4	ns
C <sub>L</sub> = 30	pF									
t <sub>pd</sub>	propagation	A to Y; see <u>Fig. 8</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	37.3	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.0	9.8	18.7	3.9	19.6	3.9	20.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.7	7.1	11.2	3.8	12.3	3.8	12.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.6	6.0	9.1	3.6	10.0	3.6	10.6	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	3.5	5.2	6.9	3.2	7.5	3.2	7.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	3.3	4.8	6.1	3.1	7.1	3.1	7.4	ns

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Symbol	Parameter	Conditions	25 °C		25 °C -40 °C to +85 °C		-40 °C to	+125 °C	Unit	
			Min	Typ[1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 5 p	F, 10 pF, 15 p	F and 30 pF								
C <sub>PD</sub>	power	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]								
	dissipation capacitance	V <sub>CC</sub> = 0.8 V	-	2.6	-	-	-	-	-	pF
	oapaoitarioc	V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.7	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	2.9	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	3.1	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	3.7	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.3	-	-	-	-	-	pF

- [1] All typical values are measured at nominal  $V_{CC}$ .
- [2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .
- [3]  $\dot{C}_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu W$ ).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:

f<sub>i</sub> = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

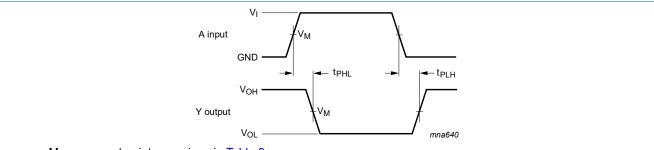
C<sub>L</sub> = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

### 12.1. Waveform and test circuit



Measurement points are given in Table 9.

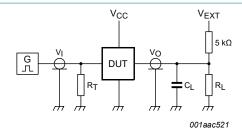
Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

Fig. 8. The data input (A) to output (Y) propagation delays

Table 9. Measurement points

Supply voltage	Output	Input					
V <sub>CC</sub>	V <sub>M</sub>	V <sub>M</sub>	VI	$t_r = t_f$			
0.8 V to 3.6 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns			

### Low-power Schmitt trigger inverter



Test data is given in Table 10.

Definitions for test circuit:

 $R_L$  = Load resistance;

C<sub>L</sub> = Load capacitance including jig and probe capacitance;

R<sub>T</sub> = Termination resistance should be equal to the output impedance Z<sub>o</sub> of the pulse generator;

V<sub>EXT</sub> = External voltage for measuring switching times.

#### Fig. 9. Test circuit for measuring switching times

#### Table 10. Test data

Supply voltage	Load	V <sub>EXT</sub>			
V <sub>CC</sub>	C <sub>L</sub> R <sub>L</sub> [1]		t <sub>PLH</sub> , t <sub>PHL</sub> t <sub>PZH</sub> , t <sub>PHZ</sub> t <sub>PZL</sub> , t <sub>PLZ</sub>		t <sub>PZL</sub> , t <sub>PLZ</sub>
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V <sub>CC</sub>

<sup>[1]</sup> For measuring enable and disable times  $R_L = 5 k\Omega$ .

For measuring propagation delays, setup and hold times and pulse width  $R_L$  = 1  $M\Omega$ .

### 12.2. Transfer characteristics

**Table 11. Transfer characteristics** 

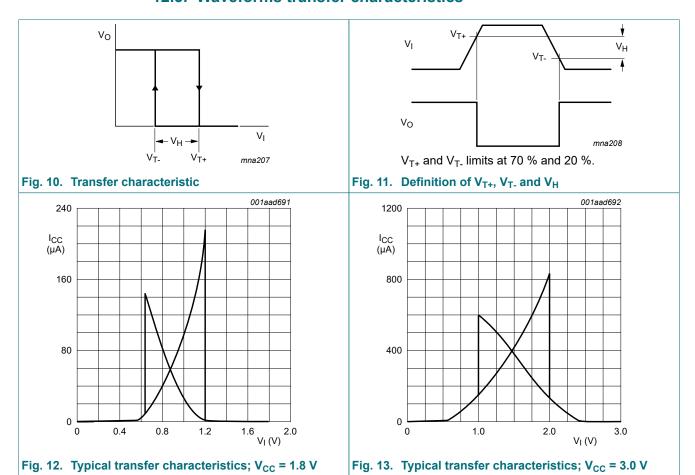
Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9.

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V <sub>T+</sub>	positive-going threshold voltage	see Fig. 10 and Fig. 11								
		V <sub>CC</sub> = 0.8 V	0.30	-	0.60	0.30	0.60	0.30	0.62	V
		V <sub>CC</sub> = 1.1 V	0.53	-	0.90	0.53	0.90	0.53	0.92	V
		V <sub>CC</sub> = 1.4 V	0.74	-	1.11	0.74	1.11	0.74	1.13	V
		V <sub>CC</sub> = 1.65 V	0.91	-	1.29	0.91	1.29	0.91	1.31	V
		V <sub>CC</sub> = 2.3 V	1.37	-	1.77	1.37	1.77	1.37	1.80	V
		V <sub>CC</sub> = 3.0 V	1.88	-	2.29	1.88	2.29	1.88	2.32	V
V <sub>T</sub> -	negative-going threshold voltage	see Fig. 10 and Fig. 11								
		V <sub>CC</sub> = 0.8 V	0.10	-	0.60	0.10	0.60	0.10	0.60	V
		V <sub>CC</sub> = 1.1 V	0.26	-	0.65	0.26	0.65	0.26	0.65	V
		V <sub>CC</sub> = 1.4 V	0.39	-	0.75	0.39	0.75	0.39	0.75	V
		V <sub>CC</sub> = 1.65 V	0.47	-	0.84	0.47	0.84	0.47	0.84	V
		V <sub>CC</sub> = 2.3 V	0.69	-	1.04	0.69	1.04	0.69	1.04	V
		V <sub>CC</sub> = 3.0 V	0.88	-	1.24	0.88	1.24	0.88	1.24	V

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Symbol	Parameter	Conditions	25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit	
			Min	Тур	Max	Min	Max	Min	Max	
1	hysteresis voltage	see <u>Fig. 10</u> , <u>Fig. 11</u> , <u>Fig. 12</u> and <u>Fig. 13</u>								
		V <sub>CC</sub> = 0.8 V	0.07	-	0.50	0.07	0.50	0.07	0.50	V
		V <sub>CC</sub> = 1.1 V	0.08	-	0.46	0.08	0.46	0.08	0.46	V
		V <sub>CC</sub> = 1.4 V	0.18	-	0.56	0.18	0.56	0.18	0.56	V
		V <sub>CC</sub> = 1.65 V	0.27	-	0.66	0.27	0.66	0.27	0.66	V
		V <sub>CC</sub> = 2.3 V	0.53	-	0.92	0.53	0.92	0.53	0.92	V
		V <sub>CC</sub> = 3.0 V	0.79	-	1.31	0.79	1.31	0.79	1.31	V

## 12.3. Waveforms transfer characteristics



#### Low-power Schmitt trigger inverter

# 13. Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{ad} = f_i \times (t_r \times I_{CC(AV)} + t_f \times I_{CC(AV)}) \times V_{CC}$  where:

- P<sub>ad</sub> = additional power dissipation (μW);
- f<sub>i</sub> = input frequency (MHz);
- t<sub>r</sub> = input rise time (ns); 10 % to 90 %;
- t<sub>f</sub> = input fall time (ns); 90 % to 10 %;
- I<sub>CC(AV)</sub> = average additional supply current (μA).

Average I<sub>CC</sub> differs with positive or negative input transitions, as shown in Fig. 14.

An example of a relaxation circuit using the 74AUP1G14 is shown in Fig. 15.

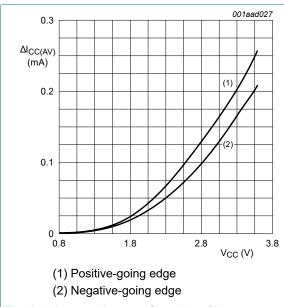


Fig. 14. Average  $I_{CC}$  as a function of  $V_{CC}$ 

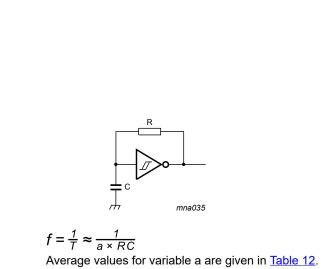


Fig. 15. Relaxation oscillator

#### Table 12. Variable values

Supply voltage	Variable a
1.1 V	1.28
1.5 V	1.22
1.8 V	1.24
2.8 V	1.34
3.3 V	1.45

Low-power Schmitt trigger inverter

# 14. Package outline

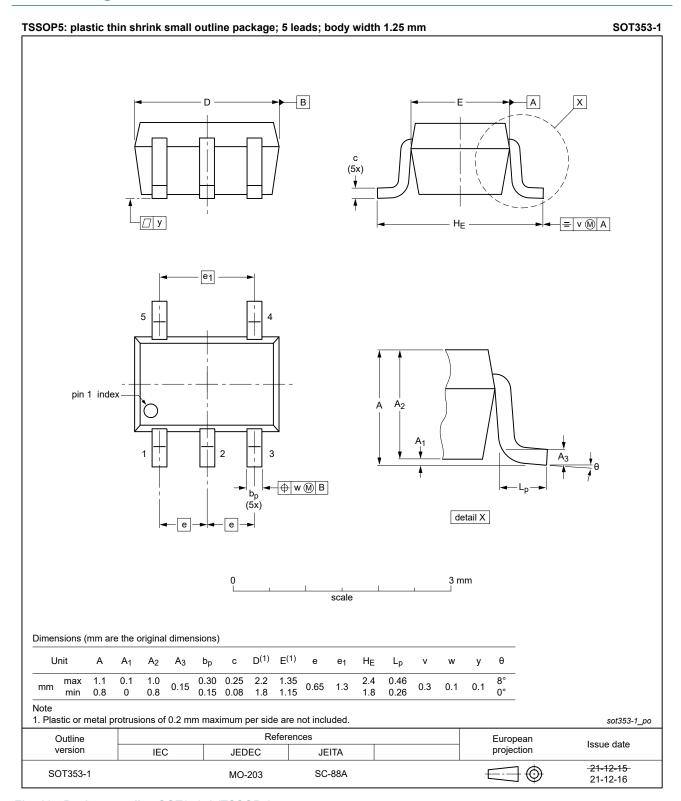


Fig. 16. Package outline SOT353-1 (TSSOP5)

## Low-power Schmitt trigger inverter

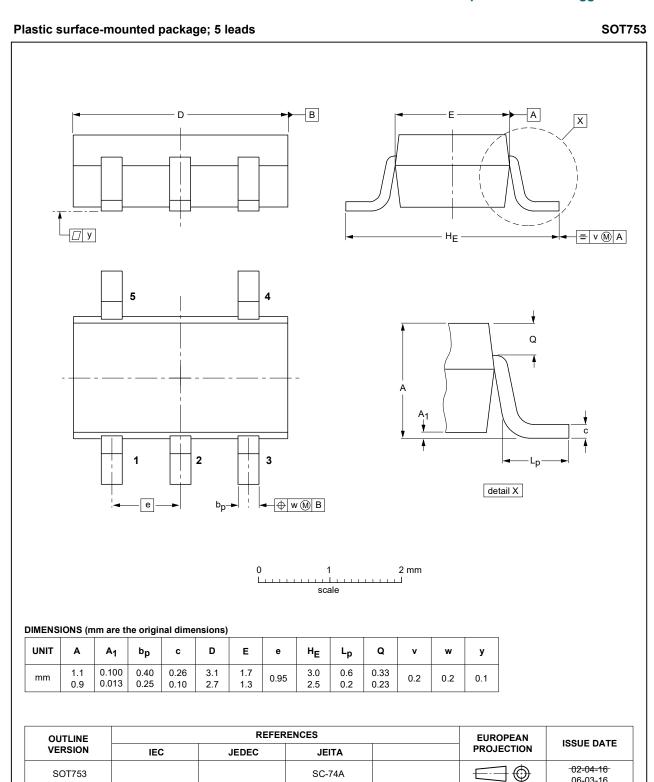


Fig. 17. Package outline SOT753 (SC-74A)

**Product data sheet** 

06-03-16

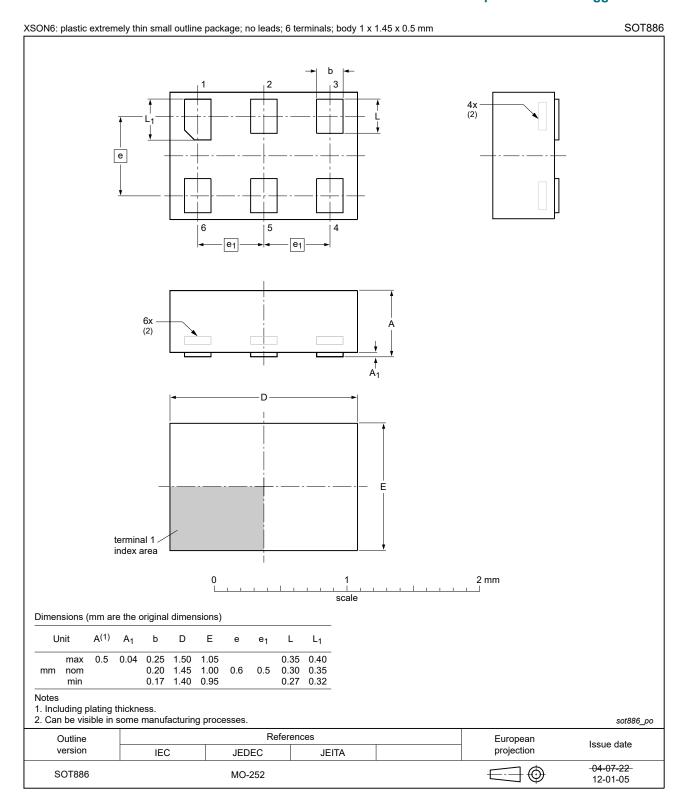


Fig. 18. Package outline SOT886 (XSON6)

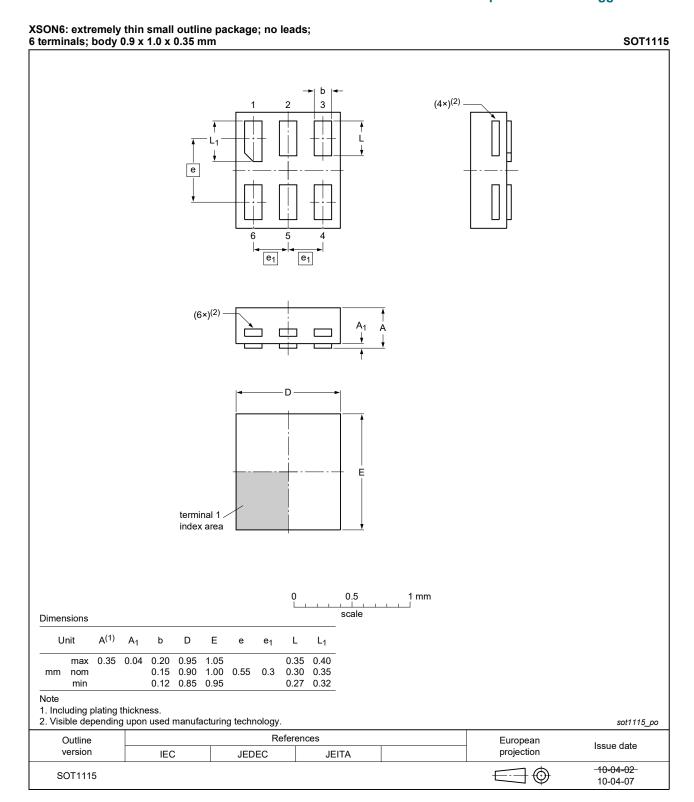


Fig. 19. Package outline SOT1115 (XSON6)

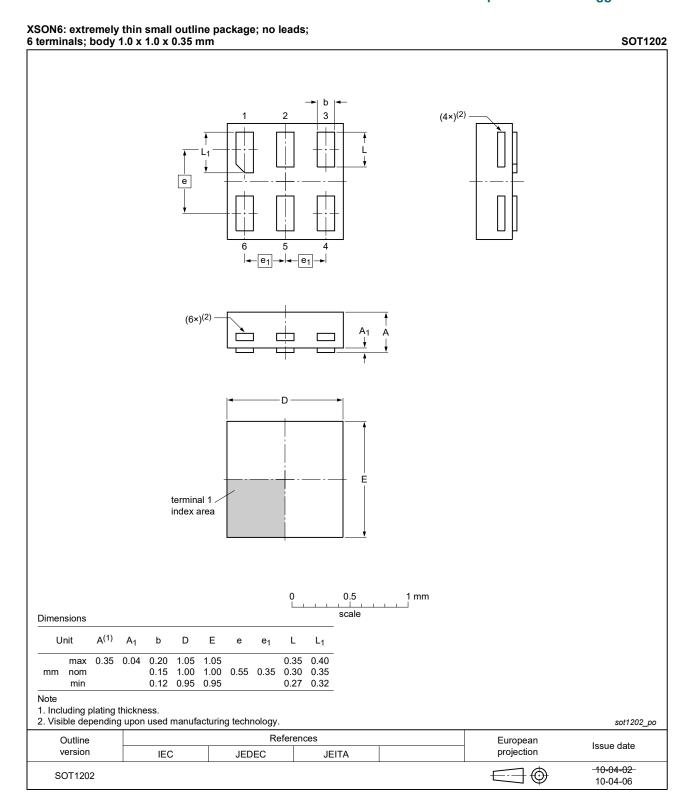


Fig. 20. Package outline SOT1202 (XSON6)

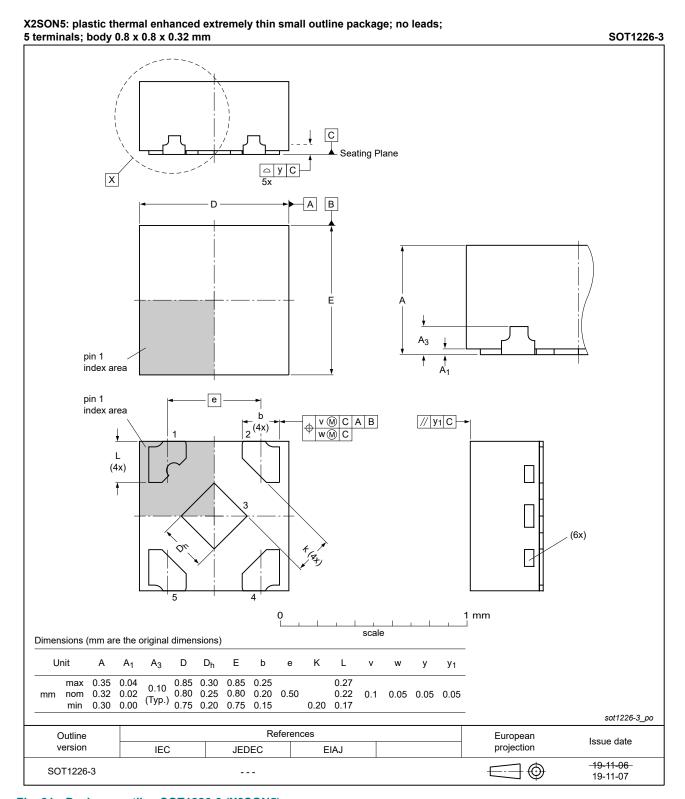


Fig. 21. Package outline SOT1226-3 (X2SON5)

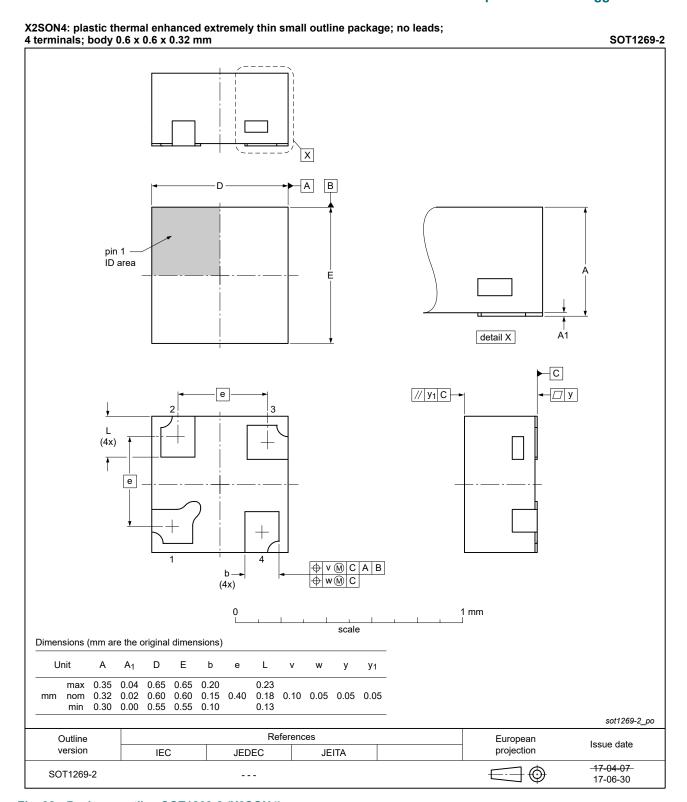


Fig. 22. Package outline SOT1269-2 (X2SON4)

# Low-power Schmitt trigger inverter

# 15. Abbreviations

#### **Table 13. Abbreviations**

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

# 16. Revision history

#### Table 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes				
74AUP1G14 v.10	20220114	Product data sheet	-	74AUP1G14 v.9				
Modifications:		<ul> <li><u>Section 2</u> updated.</li> <li><u>Fig. 16</u>: Package outline drawing for SOT353-1 (TSSOP5) has changed.</li> </ul>						
74AUP1G14 v.9	20210713	Product data sheet - 74AUP1G14 v						
Modifications:	Type numb	Type number 74AUP1G14GF (SOT891 / XSON6) removed.						
74AUP1G14 v.8	20180608	Product data sheet	-	74AUP1G14 v.7				
Modifications:	guidelines o Legal texts	of this data sheet has be of Nexperia. have been adapted to the number 74AUP1G14GX	e new company nar	,				
74AUP1G14 v.7	20161104	Product data sheet	-	74AUP1G14 v.6				
Modifications:	Added type	Added type number 74AUP1G14GV (SOT753)						
74AUP1G14 v.6	20120628	Product data sheet	-	74AUP1G14 v.5				
Modifications:	7.	<ul> <li>Added type number 74AUP1G14GX (SOT1226)</li> <li>Package outline drawing of SOT886 (Fig. 18) modified.</li> </ul>						
74AUP1G14 v.5	20111128	Product data sheet	-	74AUP1G14 v.4				
Modifications:	Legal page	Legal pages updated.						
74AUP1G14 v.4	20100713	Product data sheet	-	74AUP1G14 v.3				
74AUP1G14 v.3	20090708	Product data sheet	-	74AUP1G14 v.2				
7441104044 22	20060828	Product data sheet	-	74AUP1G14 v.1				
74AUP1G14 v.2								

## Low-power Schmitt trigger inverter

## 17. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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- 2] The term 'short data sheet' is explained in section "Definitions".
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