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Multiple Voltage Regulator



ADE-207-342 (Z)

Rev.0 Jun. 2001

General Description

The HA13164A is a compact multiple voltage regulator for car audio system. The outputs of this IC output consist of regulated 5.7 V output for a microcontroller, regulated 8 V output for CD driver, regulated 9.0 V output for audio control, regulated 10 V output for illuminations and regulated 5 V output, VCC-dependent output for external output and VCC-dependent output for remort-ANT.

Functions

General

- ACC power monitor circuit is built-in as to detect low voltage.
- Low saturation output (PNP output) used for audio output.
- Adjustable voltage for illumination output by changing an external resister.

Protections

- Output current limit circuit to avoid device destruction caused by shorted output, etc.
- High surge input protector against VCC and ACC.
- Built in a thermal shutdown circuit to prevent against the thermal destruction.

Pin Description and Equivalent Circuit

Pin				Function		
No.	Pin Name	Specification	Equivalent Circuit	Normal Operation	TSD	Surge Input
1	EXT OUT	VCC-1V/300mA min		Output voltage is VCC-1 V when M or H level applied to CTRL pin.	0V	0V
2	ANT OUT	VCC-1V/300mA min		Output voltage is VCC-1 V when M or H level to CTRL pin and H level to ANT-CTRL.	ΟV	OV
3	ACCIN	_	45kΩ ∽-₩- 15kΩ → 7/7	Connected to ACC.		_
4	VDD OUT	5.7V/100mA min	- Vcc - Vcc ↓ Vcc ↓ 175kΩ ↓ 50kΩ 777	Regular 5.7V.	5.7V	OV
5	SW5VOUT	5.0V/100mA min		Output voltage is 5V when M or H level applied to CTRL pin.	0V	0V
6	COMPOUT	5.0V/100mA min	⊂ ≶50kΩ 7/7	Output for ACC detector	0V	0V
7	ANT CTRL	_	51kΩ →₩→ 49kΩ 7/77	L: ANT output OFF H: ANT output ON	_	-

Pin				Function		
No.	Pin Name	Specification	Equivalent Circuit	Normal Operation	TSD	Surge Input
8	VCC	_		Connected to VCC		_
9	BATT DET	_	[−] VDD 250kΩ ≥ 10kΩ − ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	Low battery detect.	Detect	Not detect
10	AUDIOOUT	9.0V/500mA min		Output voltage is 9V when M or H level applied to CTRL pin.	OV	OV
11	CTRL	_	65kΩ → 35kΩ → → → → → → → → → → → → →	L: BIAS OFF M: BIAS ON H: CD ON	_	_
12	CD OUT	8.0V/1.3A min		Output voltage is 8V when H level applied to CTRL pin.	OV	0V
13	ILM AJ	_		Adjustment pin for ILM output voltage.	_	_
14	ILM OUT	9.85V/500mA min	33.4kΩ 5kΩ 7/7	Output voltage is 10V when M or H level applied to CTRL pin	OV	OV
15	GND	_		Connected to GND		_

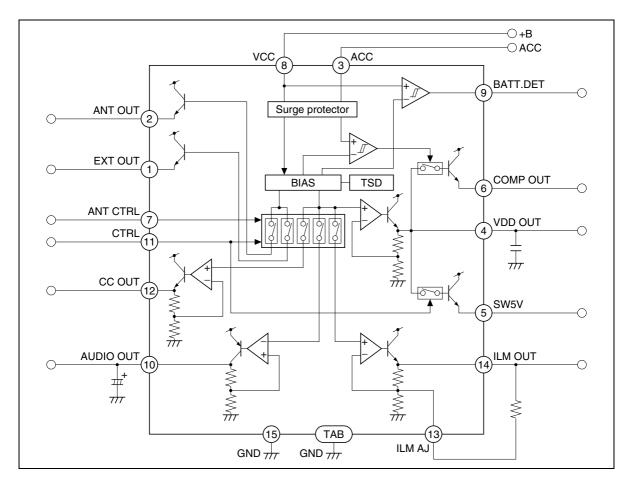
Pin Description and Equivalent Circuit (cont)



Timing Chart

vcc			8.5V	9.25V
CTRL				
AUDIO				
CD				
ILM				
EXT			1	
SW5V			1	
ANT				
ACC	2.8V			2.5V
COMP				
B.DET current				

Block Diagram





Absolute Maximum Ratings

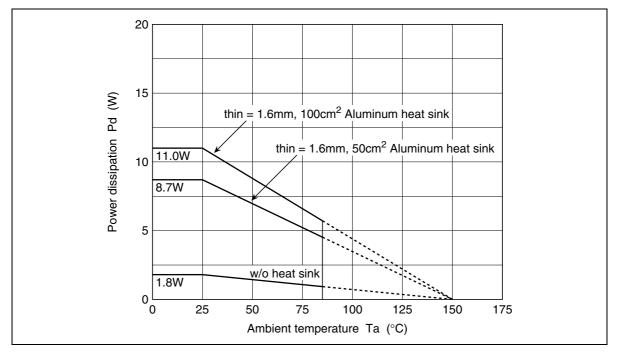
 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit	Note
Operating power supply voltage	Vcc	18	V	
DC supply voltage	Vcc(DC)	26	V	1
Peak voltage	Vcc(PEAK)	50	V	2
Power dissipation	Pd	36	W	3
Junction temperature	Tj	150	°C	
Operating temperature	Topr	-40 to +85	°C	
Storage temperature	Tstg	-55 to +125	°C	

Notes: Recommended power supply voltage range 10V to 16V.

1. Applied time is less than 30 sec.

- 2. Surge pulse as input.
- 3. Ta=25°C. :Permissible power dissipation when using a heat sink of infinite area. Refer to the derating curves below.



Electrical Characteristics

(unless otherwise noted, Vcc = 13.2 V, $Ta = 25^{\circ}C$)

Item		Symbol	Min	Тур	Max	Unit	Test Condition
Standby current		IST	_	460	700	μA	ACC = 0V, CTRL = 0V
CTRL L level (STBY mode)		VCL	0	_	1.0	V	
CTRL M level (CD OFF mode)		VCM	2.0	_	3.0	V	
CTRL H	l level (CD ON mode)	VCH	4.0	—	—	V	
ANT CT	RL L level (ANTOFF mode)	VACL	0	_	2.0	V	
ANT CT	RL H level (ANT ON mode)	VACH	3.0	_	_	V	
VDD	Output voltage	Vo1	5.4	5.7	6.0	V	lo1 = 80mA
OUT	Voltage regulation	Δ Vo11	_	10	50	mV	Vcc = 10 to 16V, Io1 = 80mA
	Load regulation	Δ Vo12	_	50	100	mV	lo1 = 0 to 80mA
	Minimum I/O voltage differential	Δ Vo13	_	1.0	1.5	V	lo1 = 80mA
	Output current capacity	lo1	100	250	_	mA	$Vo1 \ge 5.4V$
	Ripple rejection ratio	SVR1	50	60		dB	f = 100Hz, lo1 = 80mA
CD	Output voltage 2	Vo2	7.6	8.0	8.4	V	lo2 = 1.0A
OUT	Voltage regulation	Δ Vo21	_	40	100	mV	Vcc = 10 to 16V, lo2 = 1.0A
	Load regulation	Δ Vo22	_	70	150	mV	lo2 = 10m to 1.0A
	Minimum I/O voltage differential	Δ Vo23	_	1.0	1.5	V	lo2 = 1.0A
	Output current capacity	lo2	1.3	2.0	—	А	Vo2 ≥ 7.6V
	Ripple rejection ratio	SVR2	40	45		dB	f = 100Hz, lo2 = 1.0A
AUDIO	Output voltage 3	Vo3	8.5	9.0	9.5	V	lo3 = 400mA
OUT	Voltage regulation	Δ Vo31	_	30	90	mV	Vcc = 10 to 16V, Io3 = 400mA
	Load regulation	Δ Vo32	_	100	200	mV	lo3 = 10 to 400mA
	Minimum I/O voltage differential	Δ Vo33	_	0.4	0.9	V	lo3 = 400mA
	Output current capacity	lo3	500	850	_	mA	$Vo3 \ge 8.5V$
	Ripple rejection ratio	SVR3	45	50	_	dB	f = 100Hz, lo3 = 400mA
ILM	Output voltage 4	Vo4	9.35	9.85	10.35	V	lo4 = 400mA
OUT	Voltage regulation	Δ Vo41	_	40	100	mV	Vcc = 12.5 to 16V, Io4 = 400mA
	Load regulation	Δ Vo42	_	50	100	mV	lo4 = 10 to 400mA
	Minimum I/O voltage differential	Δ Vo43	_	1.0	1.5	V	lo4 = 400mA
	Output current capacity	lo4	500	900	_	mA	$Vo4 \ge 9.35V$
	Ripple rejection ratio	SVR4	35	40	_	dB	f = 100Hz, lo4 = 400mA
EXT12	Differential I/O voltage	$\Delta Vo51$	_	1.0	1.5	V	lo5 = 300mA
OUT	Load regulation	Δ Vo52	_	350	600	mV	lo5 = 10 to 300mA
	Output current capacity	lo5	300	500	_	mA	Vo5 ≥ 11.7V

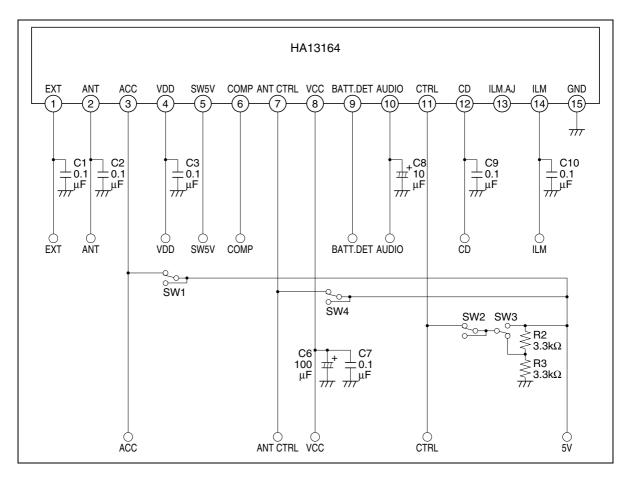
Electrical Characteristics (cont)

(unless otherwise noted, Vcc = 13.2 V, $Ta = 25^{\circ}C$)

Item		Symbol	Min	Тур	Max	Unit	Test Condition
ANT	Differential I/O voltage	$\Delta Vo61$	—	1.0	1.5	V	lo6 = 300mA
OUT	Load regulation	$\Delta Vo62$	—	350	600	mV	lo6 = 10 to 300mA
	Output current capacity	lo6	300	500	_	mA	Vo6 ≥ 11.7V
SW5V	Output voltage	Vo7	4.6	5.0	5.4	V	lo7 = 80mA, VDD = no load
OUT	Output current capacity	lo7	100	300	_	mA	$Vo7 \ge 4.6V$
ACC	Output voltage	Vo8	4.6	5.0	5.4	V	lo8 = 40mA, VDD = no load
OUT	Output current capacity	lo8	100	300	_	mA	$Vo8 \ge 4.6V$
	Rise threshold voltage	VTHH8	2.6	2.8	3.0	V	
	Hysteresis range	$\Delta VTH8$	0.2	0.3	0.4	V	
BATT.	Threshold voltage	VTHH9	8.1	8.5	8.9	V	
DET	Hysteresis range	$\Delta VTH9$	0.55	0.75	0.95	V	
	Output current capacity	lo9	200	_	_	μA	Vo = 0.3V

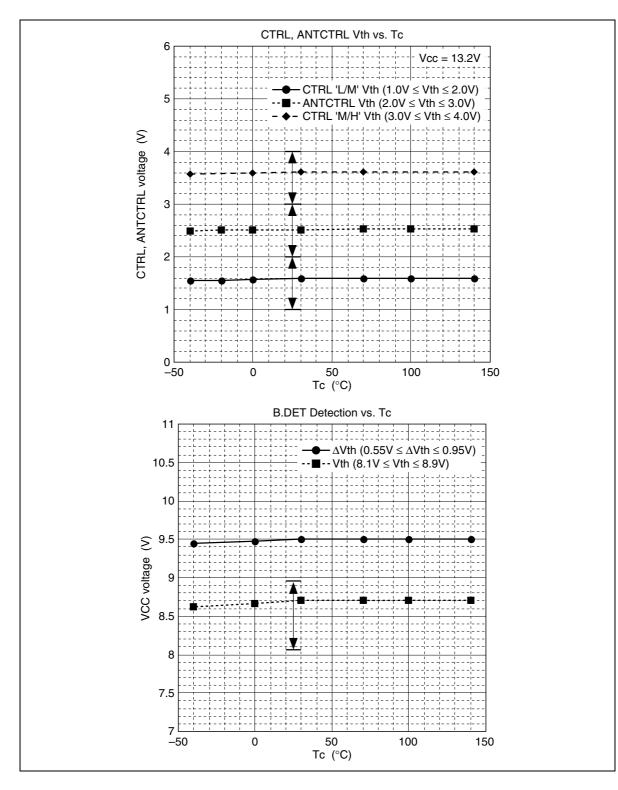


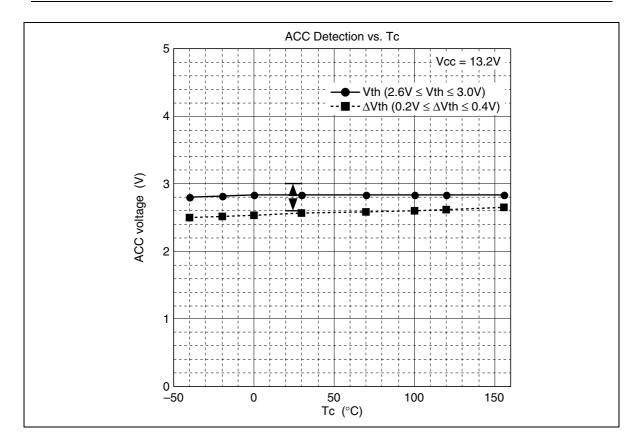
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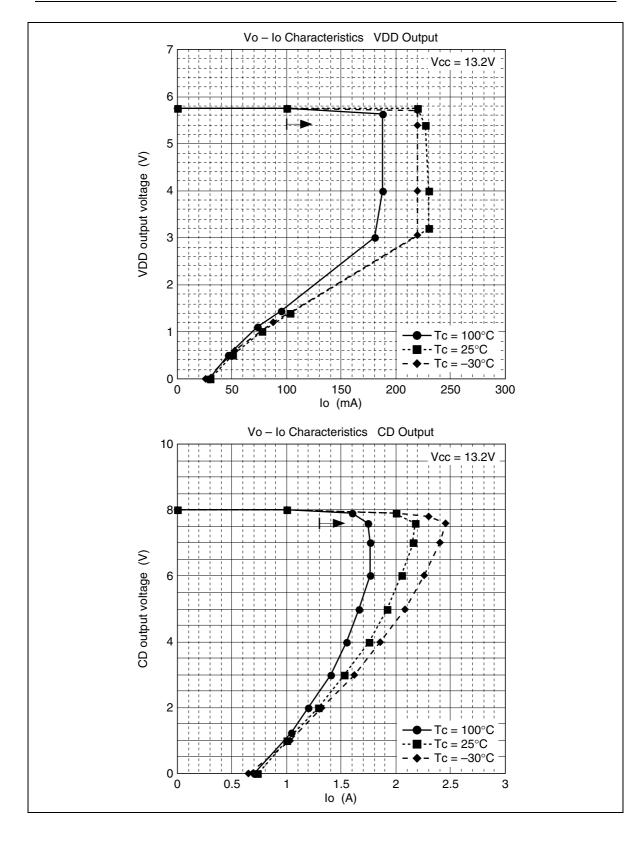


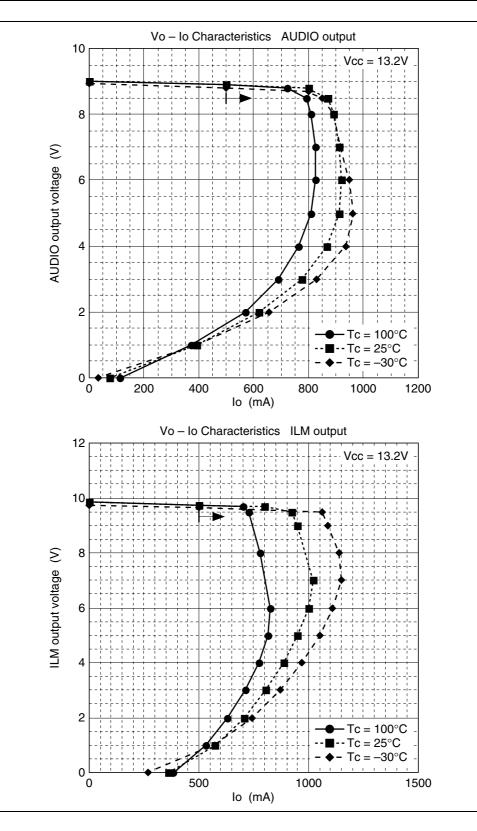


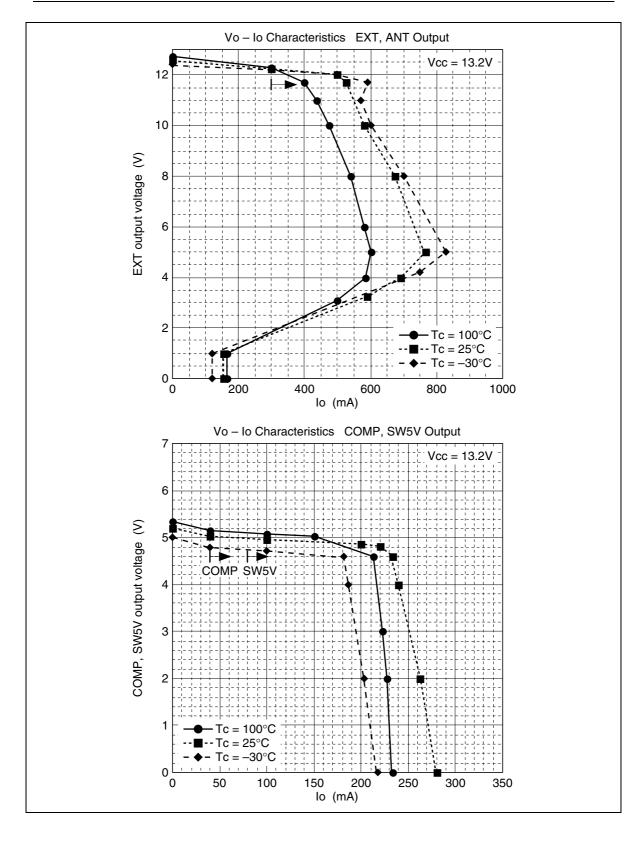
Main Characteristic

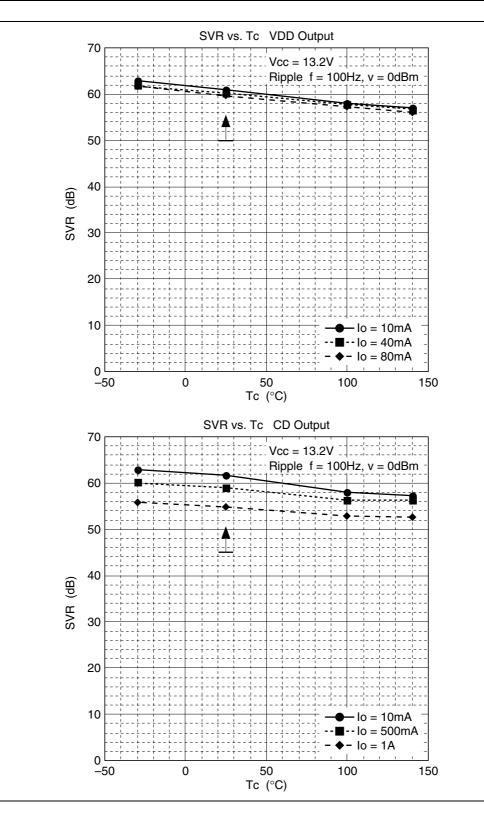


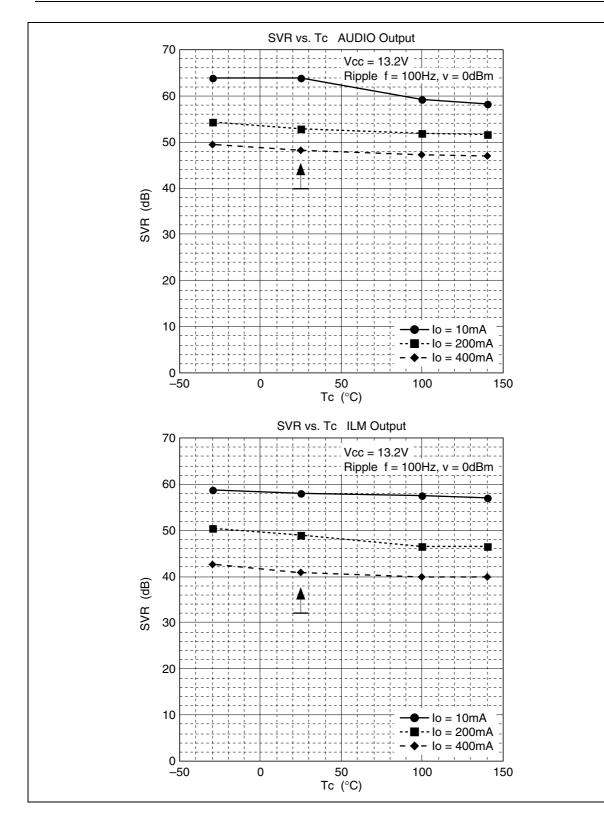




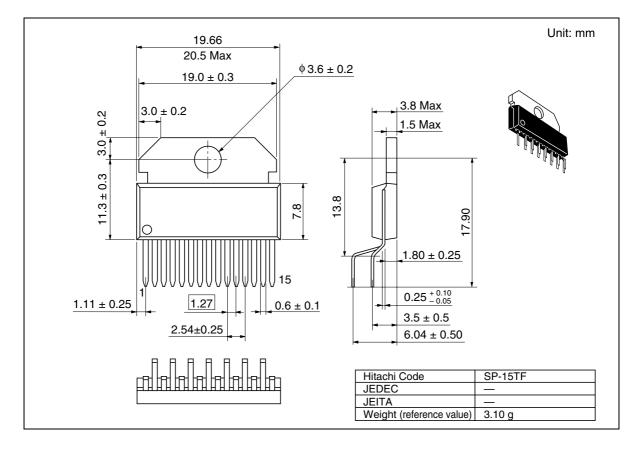








Package Dimensions





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