Low-power dual buffer/line driver; 3-state Rev. 13 — 10 March 2022

**Product data sheet** 

### 1. General description

The 74AUP2G125 is a dual buffer/line driver with 3-state outputs controlled by the output enable inputs ( $n\overline{OE}$ ). Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device ensures very low static and dynamic power consumption across the entire V<sub>CC</sub> range from 0.8 V to 3.6 V.

This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- CMOS low power dissipation
- Low static power consumption; I<sub>CC</sub> = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD78B Class II
- Overvoltage tolerant inputs to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- Input-disable feature allows floating input conditions
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.2 V to 1.95 V)
  - JESD8-5 (1.8 V to 2.7 V)
  - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
  - HBM JESD22-A114F Class 3A exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

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## 3. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AUP2G125DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1
74AUP2G125GT	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 × 1.95 × 0.5 mm	SOT833-1
74AUP2G125GF	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1 × 0.5 mm	SOT1089
74AUP2G125GN	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 × 1.0 × 0.35 mm	SOT1116
74AUP2G125GS	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1.0 × 0.35 mm	SOT1203
74AUP2G125GX	-40 °C to +125 °C	X2SON8	plastic thermal enhanced extremely thin small outline package; no leads; 8 terminals; body 1.35 × 0.8 × 0.35 mm	SOT1233

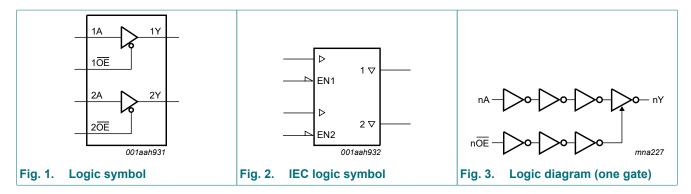
### 4. Marking

#### Table 2. Marking codes

Type number	Marking code[1]
74AUP2G125DC	p25
74AUP2G125GT	p25
74AUP2G125GF	aM
74AUP2G125GN	aM
74AUP2G125GS	aM
74AUP2G125GX	aM

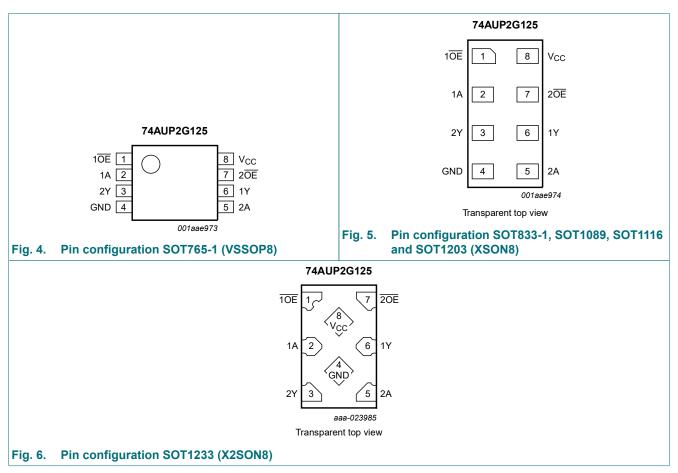
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

### 5. Functional diagram



74AUP2G125

### 6. Pinning information



### 6.1. Pinning

### 6.2. Pin description

Table 3. Pin description		
Symbol	Pin	Description
10E, 20E	1, 7	output enable input (active LOW)
1A, 2A	2, 5	data input
GND	4	ground (0 V)
1Y, 2Y	6, 3	data output
Vcc	8	supply voltage

### 7. Functional description

#### Table 4. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

Input nOE	Output	
nOE	nA	nY
L	L	L
L	Н	Н
Н	X	Z

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+4.6	V
VI	input voltage		[1]	-0.5	+4.6	V
Vo	output voltage	Active mode and Power-down mode	[1]	-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>1</sub> < 0 V		-50	-	mA
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V		-50	-	mA
Ι <sub>Ο</sub>	output current	$V_{O} = 0 V \text{ to } V_{CC}$		-	±20	mA
I <sub>CC</sub>	supply current			-	+50	mA
I <sub>GND</sub>	ground current			-50	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C				
		All packages except SOT1233	[2]	-	250	mW
		SOT1233 package	[3]	-	300	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT765-1 (VSSOP8) package: P<sub>tot</sub> derates linearly with 4.9 mW/K above 99 °C.

For SOT833-1 (XSON8) package:  $P_{tot}$  derates linearly with 3.1 mW/K above 68 °C.

For SOT1089 (XSON8) package: P<sub>tot</sub> derates linearly with 4.0 mW/K above 88 °C.

For SOT1116 (XSON8) package: Ptot derates linearly with 4.2 mW/K above 90 °C.

For SOT1203 (XSON8) package: Ptot derates linearly with 3.6 mW/K above 81 °C.

[3] For SOT1233 (X2SON8) package: Ptot derates linearly with 7.7 mW/K above 118 °C.

### 9. Recommended operating conditions

#### Table 6. Operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
V <sub>O</sub>	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	-	200	ns/V

# **10. Static characteristics**

#### Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
T <sub>amb</sub> = 2	5 °C					
VIH	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		$I_0$ = -2.3 mA; $V_{CC}$ = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		$I_0$ = -4.0 mA; $V_{CC}$ = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
		$I_0$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
I	input leakage current	$V_I$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.1	μA
l <sub>oz</sub>	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.1	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.2	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μA
l <sub>cc</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
∆I <sub>CC</sub>	additional supply current	data input; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; [1] V <sub>CC</sub> = 3.3 V	-	-	40	μA
		$\overline{\text{NOE}}$ input; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; [1] V <sub>CC</sub> = 3.3 V	-	-	110	μA
		all inputs; $V_1$ = GND to 3.6 V; [2] n $\overline{OE}$ = GND; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1	μA

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
CI	input capacitance	$V_{CC}$ = 0 V to 3.6 V; V <sub>I</sub> = GND or V <sub>CC</sub>	-	0.8	-	pF
Co	output capacitance	output enabled; $V_0$ = GND; $V_{CC}$ = 0 V	-	1.4	-	pF
		output disabled; $V_O = GND$ or $V_{CC} = 0$ V; $V_{CC} = 0$ V to 3.6 V	-	1.3	-	pF
T <sub>amb</sub> = -4	0 °C to +85 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub> HIGH-level output volta		$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		$I_0$ = -2.3 mA; $V_{CC}$ = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		$I_0$ = -4.0 mA; $V_{CC}$ = 3.0 V	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_0 = 20 \ \mu\text{A}; \ V_{CC} = 0.8 \ \text{V} \text{ to } 3.6 \ \text{V}$	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μA
I <sub>OZ</sub>	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.5	μA
I <sub>OFF</sub>	power-off leakage current	$V_{1} \text{ or } V_{0} = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.5	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μA
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μA
ΔI <sub>CC</sub>	additional supply current	data input; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; [1] V <sub>CC</sub> = 3.3 V	-	-	50	μA
		$\overline{\text{NOE}}$ input; V <sub>1</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; [1] V <sub>CC</sub> = 3.3 V	-	-	120	μA
		all inputs; $V_I$ = GND to 3.6 V; [2] nOE = GND; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1	μA

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
T <sub>amb</sub> = -	40 °C to +125 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.75 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.70 × V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.25 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.30 × V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH}$ or $V_{IL}$				
		$I_0 = 20 \ \mu\text{A}; V_{CC} = 0.8 \ \text{V} \text{ to } 3.6 \ \text{V}$	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		$I_0 = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.50	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	V
		$I_0 = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.50	V
I	input leakage current	$V_{I} = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μA
I <sub>OZ</sub>	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μA
I <sub>OFF</sub>	power-off leakage current	$V_{1}$ or $V_{0}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.75	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA
Δl <sub>CC</sub>	additional supply current	data input; V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; [1] V <sub>CC</sub> = 3.3 V	-	-	75	μA
		$\overline{\text{NOE}}$ input; V <sub>1</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; [1] V <sub>CC</sub> = 3.3 V	-	-	180	μA
		all inputs; $V_I$ = GND to 3.6 V; [2] n $\overline{OE}$ = GND; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1	μA

[1] [2]

One input at  $V_{CC}$  - 0.6 V, other input at  $V_{CC}$  or GND. To show  $I_{CC}$  remains very low when the input-disable feature is enabled.

# **11. Dynamic characteristics**

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9.

Symbol	Parameter	Conditions		25 °C		T <sub>an</sub> -40 °C te	<sub>nb</sub> = o +85 °C	T <sub>amb</sub> = -40 °C to +125 °C		Unit
			Min	Typ[1]	Мах	Min	Max	Min	Max	
C <sub>L</sub> = 5 p	F									
	propagation	nA to nY; see <u>Fig. 7</u> . [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	20.6	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.8	5.5	10.5	2.5	11.7	2.5	12.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.2	3.9	6.1	2.0	7.3	2.0	8.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.9	3.2	4.8	1.7	6.1	1.7	6.7	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	2.6	3.6	1.4	4.3	1.4	4.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.4	2.4	3.1	1.2	3.9	1.2	4.4	ns
t <sub>en</sub>	enable time	nOE to nY; see Fig. 8. [3]								
		V <sub>CC</sub> = 0.8 V	-	69.9	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.1	6.1	11.8	2.9	13.9	2.9	15.4	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.5	4.2	6.6	2.3	7.7	2.3	8.3	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.1	3.4	5.1	2.0	6.2	2.0	6.8	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.8	2.6	3.7	1.7	4.5	1.7	5.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	2.4	3.1	1.7	3.5	1.7	3.9	ns
t <sub>dis</sub>	disable time	nOE to nY; see Fig. 8. [4]								
		V <sub>CC</sub> = 0.8 V	-	14.3	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.7	4.3	6.5	2.7	7.3	2.7	8.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.1	3.2	4.4	2.1	5.1	2.1	5.7	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.0	3.0	4.3	2.0	5.0	2.0	5.7	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.4	2.2	2.9	1.4	3.3	1.4	4.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	2.5	3.2	1.7	3.4	1.7	3.9	ns

#### Low-power dual buffer/line driver; 3-state

Symbol	Parameter	Conditions	25 °C			T <sub>amb</sub> = -40 °C to +85 °C		T <sub>amb</sub> = -40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Мах	Min	Max	
C <sub>L</sub> = 10	pF			-						
t <sub>pd</sub>	propagation	nA to nY; see <u>Fig. 7</u> . [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	24.0	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.2	6.4	12.3	3.0	13.8	3.0	15.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.1	4.5	7.3	1.9	8.5	1.9	9.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.9	3.8	5.5	1.7	6.8	1.7	7.6	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.1	3.2	4.2	1.6	5.3	1.6	5.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.8	3.0	3.8	1.6	4.6	1.6	5.2	ns
t <sub>en</sub>	enable time	nOE to nY; see Fig. 8. [3]								
		V <sub>CC</sub> = 0.8 V	-	73.7	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.6	6.9	13.5	3.4	15.8	3.4	17.5	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.3	4.8	7.7	2.2	8.6	2.2	9.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.0	3.9	5.8	1.9	6.8	1.9	7.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.8	3.2	4.3	1.7	5.3	1.7	5.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.7	3.0	3.9	1.7	4.3	1.7	4.8	ns
t <sub>dis</sub>	disable time	nOE to nY; see Fig. 8. [4]								
		V <sub>CC</sub> = 0.8 V	-	32.7	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.4	5.4	7.9	3.4	8.8	3.4	9.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.2	4.1	5.5	2.2	6.2	2.2	7.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.2	4.2	5.6	1.9	6.3	1.9	7.1	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	3.0	3.8	1.7	4.5	1.7	5.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.1	3.8	4.8	1.7	5.0	1.7	5.6	ns

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#### Low-power dual buffer/line driver; 3-state

Symbol	Parameter	Conditions	25 °C			T <sub>amb</sub> = -40 °C to +85 °C		T <sub>amb</sub> = -40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 15	pF									
	propagation	nA to nY; see <u>Fig. 7</u> . [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	27.4	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.6	7.2	14.1	3.3	15.8	3.3	17.5	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.0	5.1	8.1	2.5	9.8	2.5	10.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.2	4.3	6.3	2.0	7.9	2.0	8.8	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.0	3.7	4.9	1.8	6.0	1.8	6.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	3.5	4.4	1.8	5.4	1.8	6.1	ns
t <sub>en</sub>	enable time	nOE to nY; see Fig. 8. [3]								
		V <sub>CC</sub> = 0.8 V	-	77.5	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.0	7.7	15.2	3.7	17.6	3.7	19.6	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.0	5.3	8.4	2.5	9.8	2.5	10.7	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.3	4.4	6.5	2.1	7.7	2.1	8.5	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.1	3.6	5.0	2.0	6.1	2.0	6.8	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	3.5	4.4	1.9	4.9	1.9	5.5	ns
t <sub>dis</sub>	disable time	nOE to nY; see Fig. 8. [4]								
		V <sub>CC</sub> = 0.8 V	-	60.8	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.3	6.5	9.2	3.7	10.3	3.7	11.6	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.0	5.0	6.5	2.5	7.4	2.5	8.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.0	5.3	7.0	2.1	7.4	2.1	8.9	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.1	3.8	4.9	2.0	5.1	2.0	6.4	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.9	5.0	6.2	1.9	6.6	1.9	7.4	ns

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#### Low-power dual buffer/line driver; 3-state

Symbol	Parameter	Conditions		25 °C		T <sub>an</sub> -40 °C t	<sub>nb</sub> = o +85 °C	T <sub>ar</sub> -40 °C to	<sub>nb</sub> = o +125 °C	Unit
			Min	Typ[1]	Max	Min	Мах	Min	Мах	
C <sub>L</sub> = 30	pF			-					-	
t <sub>pd</sub>	propagation	nA to nY; see Fig. 7. [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	37.4	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.8	9.5	19.0	4.4	21.6	4.4	24.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	4.0	6.7	10.8	3.0	13.0	3.0	14.5	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.9	5.6	8.4	2.6	10.3	2.6	11.5	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.7	4.8	6.3	2.5	7.8	2.5	8.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.7	4.6	5.8	2.5	7.5	2.5	8.3	ns
t <sub>en</sub>	enable time	nOE to nY; see Fig. 8. [3]								
		V <sub>CC</sub> = 0.8 V	-	88.9	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	5.2	9.9	19.8	4.8	22.8	4.8	25.3	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	4.0	6.8	10.8	3.1	12.6	3.1	14.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.0	5.6	8.5	2.8	10.2	2.8	11.3	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.7	4.8	6.5	2.6	7.8	2.6	8.8	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.7	4.6	6.0	2.6	6.9	2.6	7.7	ns
t <sub>dis</sub>	disable time	nOE to nY; see Fig. 8. [4]								
		V <sub>CC</sub> = 0.8 V	-	49.9	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	6.0	9.9	13.3	4.8	14.8	4.8	16.5	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	4.4	7.7	9.6	3.1	10.8	3.1	12.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	5.1	8.7	11.1	2.8	12.4	2.8	13.8	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	3.6	6.2	7.6	2.6	8.6	2.6	9.6	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	5.2	8.7	10.5	2.6	10.8	2.6	13.1	ns
C <sub>L</sub> = 5 p	F, 10 pF, 15 p	F and 30 pF								
C <sub>PD</sub>	power dissipation	output enabled; $f_i = 1 \text{ MHz}$ ; [5] $V_1 = \text{GND}$ to $V_{CC}$								
	capacitance	V <sub>CC</sub> = 0.8 V	-	2.7	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.8	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	2.9	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	3.0	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	3.6	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.2	-	-	-	-	-	pF

[1] All typical values are measured at nominal  $V_{CC}$ .

 $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ . [2]

[3]  $\dot{t}_{en}$  is the same as  $t_{PZH}$  and  $t_{PZL}$ .

[4]  $t_{dis}$  is the same as  $t_{PHZ}$  and  $t_{PLZ}$ . [5]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu$ W).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

 $f_o$  = output frequency in MHz;

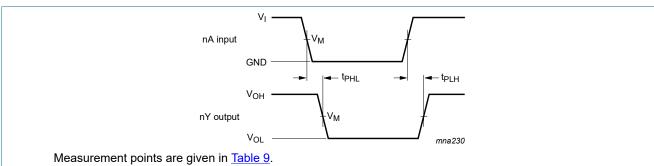
C<sub>L</sub> = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o) = \text{sum of outputs.}$ 



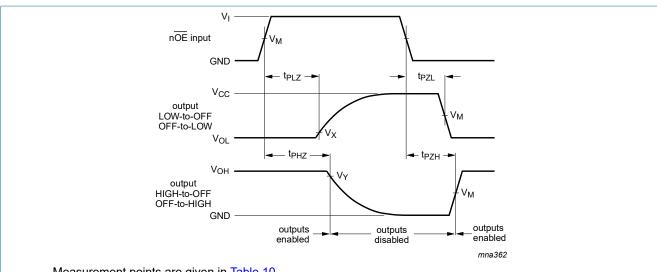


Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

#### Fig. 7. The data input (nA) to output (nY) propagation delays

#### **Table 9. Measurement points**

Supply voltage	Input			Output
V <sub>cc</sub>	V <sub>M</sub>	VI	t <sub>r</sub> = t <sub>f</sub>	V <sub>M</sub>
0.8 V to 3.6 V	$0.5 \times V_{CC}$	V <sub>CC</sub>	≤ 3.0 ns	0.5 × V <sub>CC</sub>



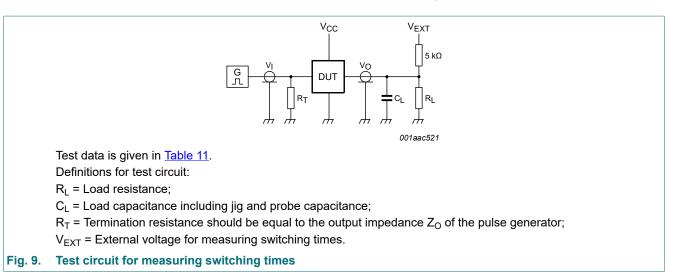
Measurement points are given in Table 10.

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

#### Enable and disable times Fig. 8.

#### **Table 10. Measurement points** Supply voltage Input Output Vcc VM Vм Vx Vy 0.8 V to 1.6 V $0.5 \times V_{CC}$ V<sub>OL</sub> + 0.1 V $0.5 \times V_{CC}$ V<sub>OH</sub> - 0.1 V 1.65 V to 2.7 V V<sub>OL</sub> + 0.15 V V<sub>OH</sub> - 0.15 V 0.5 × V<sub>CC</sub> $0.5 \times V_{CC}$ 3.0 V to 3.6 V $0.5 \times V_{CC}$ $0.5 \times V_{CC}$ V<sub>OL</sub> + 0.3 V V<sub>OH</sub> - 0.3 V

#### Low-power dual buffer/line driver; 3-state



#### Table 11. Test data

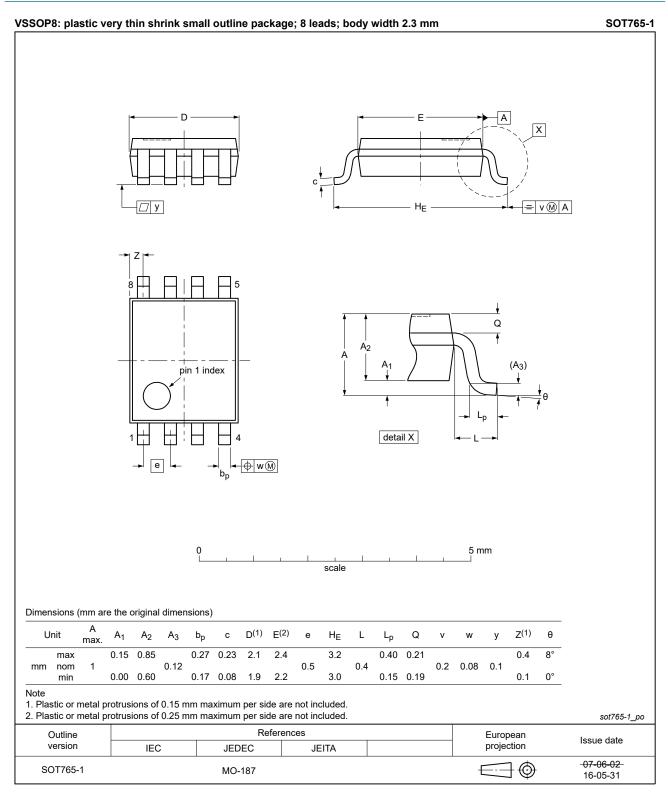
Supply voltage	Load	V <sub>EXT</sub>			
V <sub>cc</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	$2 \times V_{CC}$

[1] For measuring enable and disable times  $R_L = 5 k\Omega$ .

For measuring propagation delays, setup and hold times and pulse width  $R_L$  = 1 M $\Omega$ .

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### 12. Package outline



#### Fig. 10. Package outline SOT765-1 (VSSOP8)

#### Low-power dual buffer/line driver; 3-state

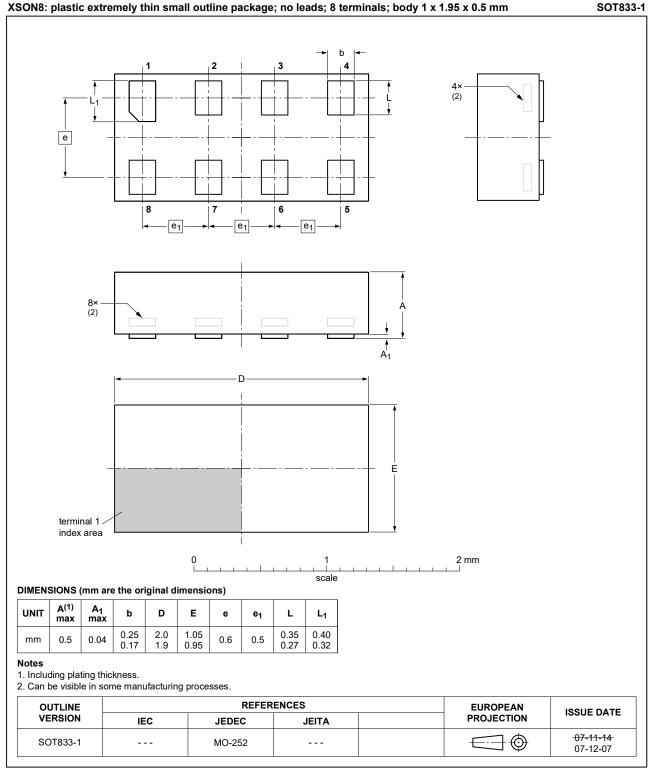
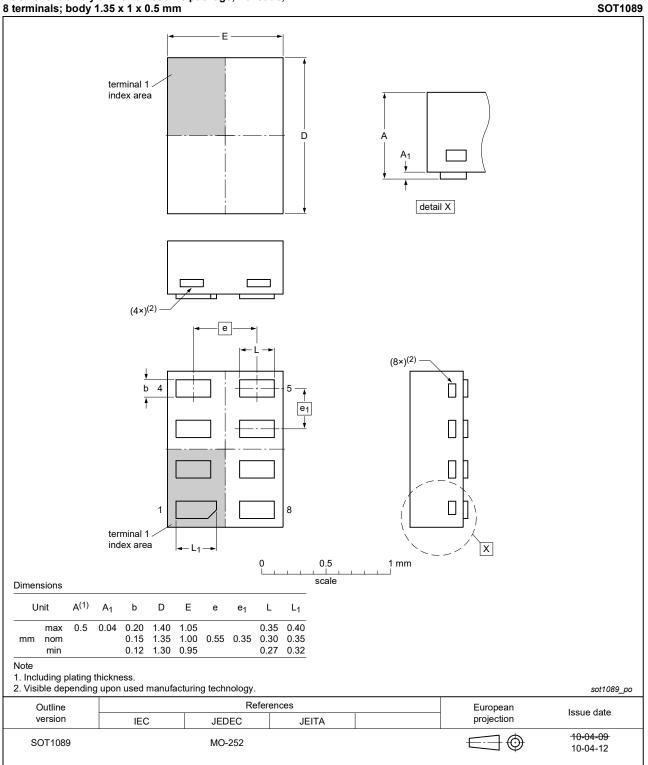


Fig. 11. Package outline SOT833-1 (XSON8)

#### Low-power dual buffer/line driver; 3-state

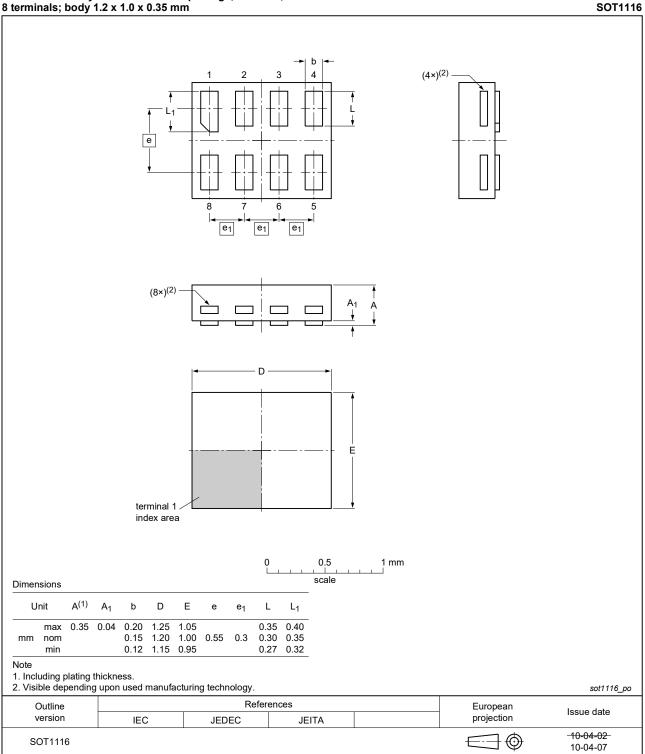


#### XSON8: extremely thin small outline package; no leads; 8 terminals; body 1.35 x 1 x 0.5 mm

Fig. 12. Package outline SOT1089 (XSON8)

#### Low-power dual buffer/line driver; 3-state

#### XSON8: extremely thin small outline package; no leads; 8 terminals; body 1.2 x 1.0 x 0.35 mm





#### Low-power dual buffer/line driver; 3-state

#### XSON8: extremely thin small outline package; no leads; 8 terminals; body 1.35 x 1.0 x 0.35 mm SOT1203 b (4×)<sup>(2)</sup> 4 2 3 е 8 6 e<sub>1</sub> e<sub>1</sub> e<sub>1</sub> (8×)<sup>(2)</sup> А С С ٦ D E terminal 1 index area 0.5 1 mm 0 1 1 1 scale Dimensions Unit A<sup>(1)</sup> A<sub>1</sub> b D Е L е e<sub>1</sub> $L_1$ 0.35 0.04 0.20 1.40 1.05 0.35 0.40 max 0.15 1.00 0.55 0.35 0.30 0.35mm nom 1.35 min 0.12 1.30 0.95 0.27 0.32 Note 1. Including plating thickness. 2. Visible depending upon used manufacturing technology. sot1203\_po References Outline European Issue date version projection IEC JEDEC JEITA 10-04-02 SOT1203 $\blacksquare$ 10-04-06

Fig. 14. Package outline SOT1203 (XSON8)

#### Low-power dual buffer/line driver; 3-state

# X2SON8: plastic thermal enhanced extremely thin small outline package; no leads; 8 terminals; body 1.35 x 0.8 x 0.35 mm



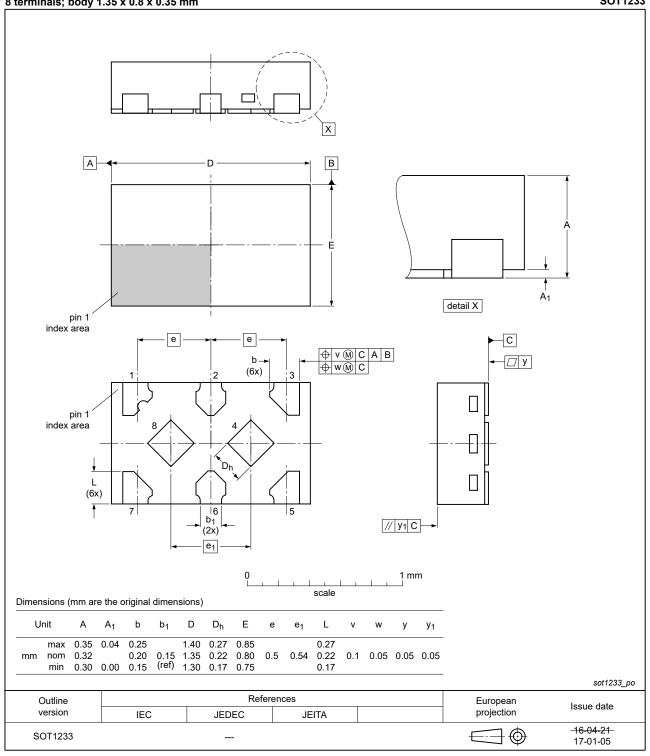


Fig. 15. Package outline SOT1233 (X2SON8)

### 13. Abbreviations

Table 12. Abbreviat	ions
Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

### 14. Revision history

#### Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP2G125 v.13	20220310	Product data sheet	-	74AUP2G125 v.12
Modifications:	<ul> <li><u>Section 1</u> and</li> </ul>	r 74AUP2G125GM (SO d <u>Section 2</u> updated. ating values for P <sub>tot</sub> total		
74AUP2G125 v.12	20170703	Product data sheet	-	74AUP2G125 v.11
Modifications:	of Nexperia. • Legal texts h • <u>Fig. 6</u> and <u>Fi</u>	f this data sheet has be ave been adapted to the <u>g. 15</u> (drawings SOT123 r 74AUP2G125GD remo	e new company nam 33/X2SON8) updated	
74AUP2G125 v.11	20161028	Product data sheet	-	74AUP2G125 v.10
Modifications:	Added type r	number 74AUP2G125G	X (SOT1233/X2SON	18)
74AUP2G125 v.10	20130208	Product data sheet	-	74AUP2G125 v.9
Modifications:	For type num	ber 74AUP2G125GD X	SON8U has change	ed to XSON8.
74AUP2G125 v.9	20120607	Product data sheet	-	74AUP2G125 v.8
74AUP2G125 v.8	20111202	Product data sheet	-	74AUP2G125 v.7
74AUP2G125 v.7	20100921	Product data sheet	-	74AUP2G125 v.6
74AUP2G125 v.6	20091127	Product data sheet	-	74AUP2G125 v.5
74AUP2G125 v.5	20090202	Product data sheet	-	74AUP2G125 v.4
74AUP2G125 v.4	20090122	Product data sheet	-	74AUP2G125 v.3
74AUP2G125 v.3	20080409	Product data sheet	-	74AUP2G125 v.2
74AUP2G125 v.2	20070419	Product data sheet	-	74AUP2G125 v.1
74AUP2G125 v.1	20061017	Product data sheet	-	-

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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