





2N7002-7-02

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C			
60V	7.5Ω @ V _{GS} = 5V	210mA			

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor control
- Power Management Functions

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
 Small Surface Mount Decker
- Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Notes 3)
 Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating).
- Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)

SOT23

Ordering Information (Note 4)

Part Number	Case	Packaging
2N7002-7-02	SOT23	3,000/Tape & Reel

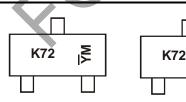
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



K72 = Product Type Marking Code YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) \overline{YM} = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or \overline{Y} = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Chengdu A/T Site

Date Code K	ley															
Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	Ν	Р	R	S	Т	U	V	W	Х	Y	Z	А	В	С	D	E
Month	Jan	F	eb	Mar	Apr	Μ	lay	Jun	Jul	Α	ug	Sep	Oct	N	ov	Dec
Code	1		2	3	4		5	6	7		8	9	0		N	D

ΥM

Shanghai A/T Site



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage			V _{DSS}	60	V
Drain-Gate Voltage $R_{GS} \le 1.0M\Omega$			V _{DGR}	60	V
Gate-Source Voltage		Continuous Pulsed	V _{GSS}	±20 ±40	V
Continuous Drain Current (Note 5)	Steady State	T _A = +25°C T _A = +85°C T _A = +100°C	ID	170 120 105	mA
Continuous Drain Current (Note 6)	Steady State	T _A = +25°C T _A = +85°C T _A = +100°C	ID	210 150 135	mA
Maximum Body Diode Forward Current (Note 6)	•	Continuous	Is	0.5	А

Thermal Characteristics

Characteristic	Symbol Value	Units
Total Power Dissipation	(Note 5) (Note 6) PD 520	mW
Thermal Resistance, Junction to Ambient	(Note 5) (Note 6) ReJA 243	°C/W
Thermal Resistance, Junction to Case	(Note 6) R _{0JC} 89	
Operating and Storage Temperature Range	T _J , T _{STG} -55 to +150	°C

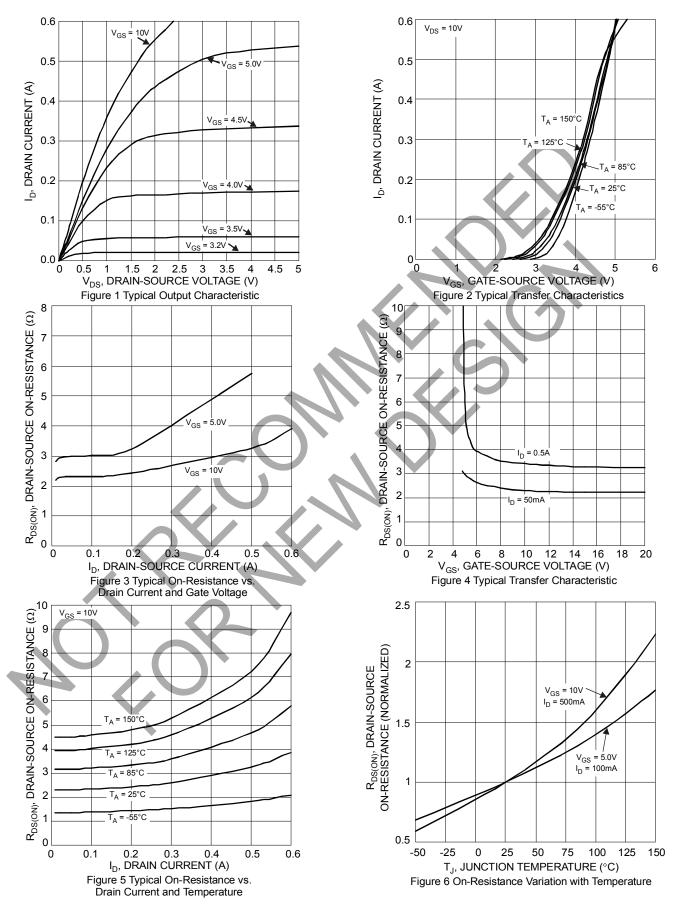
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage		BV _{DSS}	60	_	_	V	V _{GS} = 0V, I _D = 10µA
Zero Gate Voltage Drain Current	@T _C = +25°C @T _C = +125°C	I _{DSS}	_	_	1.0 500	μA	V _{DS} = 60V, V _{GS} = 0V
Gate-Body Leakage		IGSS		_	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	@T _J = +25°C C~ +100°C (Note 8)	V _{GS(th)}	2.1 1.8		2.55 2.65	V	$V_{DS} = V_{GS}, I_D = 250\mu A$ $V_{DS} = V_{GS}, I_D = 250\mu A$
Static Drain-Source On-Resistance	@T _J = +25°C @T _J = +125°C	R _{DS (ON)}		_	7.5 13.5	Ω	V_{GS} = 5.0V, I_D = 0.05A V_{GS} = 10V, I_D = 0.5A
On-State Drain Current		I _{D(ON)}	0.5	_		Α	V _{GS} = 10V, V _{DS} = 7.5V
Forward Transconductance		g fs	80	_	_	mS	V _{DS} = 10V, I _D = 0.2A
Diode Forward Voltage		V _{SD}	_	0.78	1.5	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						-	
Input Capacitance		Ciss		_	50	pF	
Output Capacitance				_	25	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance		Crss		—	5.0	pF	
Total Gate Charge (V _{GS} = 4.5V)		Qg	_	0.34	_		
Gate-Source Charge		Q _{gs}	_	0.17	_	рС	V _{DS} = 15V, I _D = 0.2A
Gate-Drain Charge	Q _{qd}	_	0.13	_			
SWITCHING CHARACTERISTICS (Note 8)						-	
Turn-On Delay Time		t _{D(on)}		3.2			
Turn-On Rise Time	tr	_	2.4	_		$V_{DD} = 30V, I_D = 0.2A,$ $R_L = 150\Omega, V_{GEN} = 10V,$ $P_{ADD} = 25\Omega$	
Turn-Off Delay Time	t _{D(off)}	_	7.5	_	ns		
Turn-Off Fall Time		t _f	_	3.8	_		R _{GEN} = 25Ω

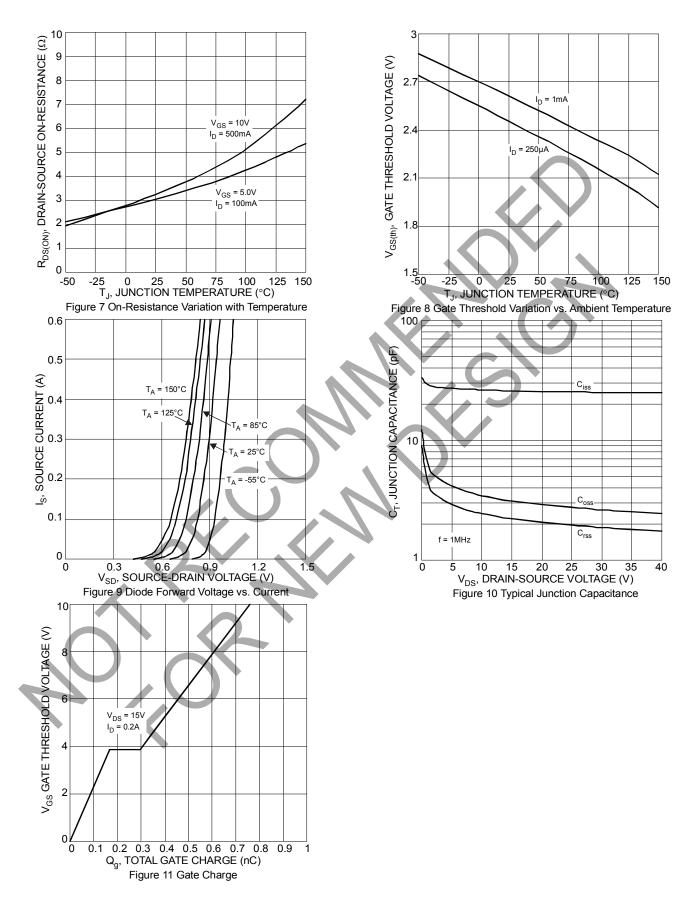
Notes:

Device mounted on FR-4 PCB, with minimum recommended pad layout
 Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





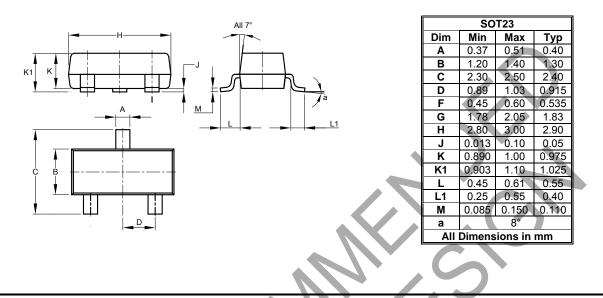






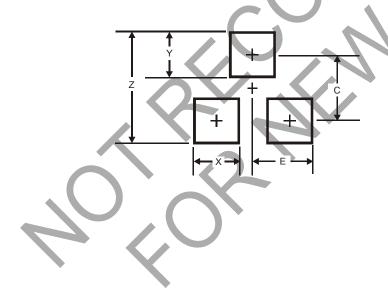
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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