Quad 2-input NAND Schmitt trigger Rev. 7 — 3 July 2020

1. General description

The 74AHC132; 74AHCT132 is a quad 2-input NAND gate with Schmitt-trigger inputs. Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

2. Features and benefits

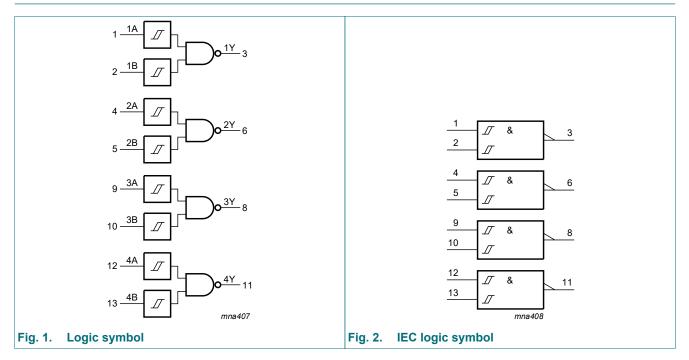
- Wide supply voltage range from 2.0 V to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- CMOS low power dissipation
- Input levels:
 - For 74AHC132: CMOS level
 - For 74AHCT132: TTL level
- ESD protection:
 - HBM JESD22-A114E exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101C exceeds 1000 V
- · Latch-up performance exceeds 100 mA per JESD 78 Class II Level A
- Multiple package options
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

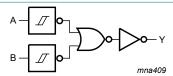


3. Ordering information

Type number	Package	e								
	Temperature range	Name	Description	Version						
74AHC132D	-40 °C to +125 °C	SO14	plastic small outline package; 14 leads;	SOT108-1						
74AHCT132D			body width 3.9 mm							
74AHC132PW	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads;	SOT402-1						
74AHCT132PW			body width 4.4 mm							
74AHC132BQ	-40 °C to +125 °C	DHVQFN14	plastic dual in-line compatible thermal	SOT762-1						
74AHCT132BQ			enhanced very thin quad flat package; no leads; 14 terminals; body 2.5 × 3 × 0.85 mm							

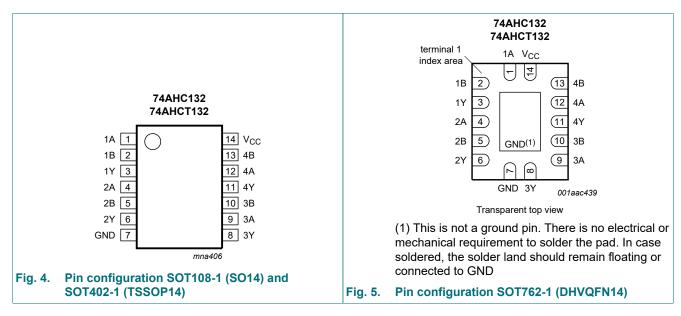
4. Functional diagram







5. Pinning information



5.1. Pinning

5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A, 4A	1, 4, 9, 12	data input A
1B, 2B, 3B, 4B	2, 5, 10, 13	data input B
1Y, 2Y, 3Y, 4Y	3, 6, 8, 11	data output Y
GND	7	ground (0 V)
V _{CC}	14	supply voltage

6. Functional description

Table 3. Function table

H = *HIGH* voltage level; *L* = *LOW* voltage level.

Input	Output	
nA	nB	nY
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
VI	input voltage			-0.5	+7.0	V
I _{IK}	input clamping current	V _I < -0.5 V	[1]	-20	-	mA
I _{OK}	output clamping current	$V_{\rm O}$ < -0.5 V or $V_{\rm O}$ > $V_{\rm CC}$ + 0.5 V	[1]	-20	+20	mA
I _O	output current	$V_{\rm O}$ = -0.5 V to (V _{CC} + 0.5 V)		-25	+25	mA
I _{CC}	supply current			-	+75	mA
I _{GND}	ground current			-75	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[2]	-	500	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT108-1 (SO14) package: P_{tot} derates linearly with 10.1 mW/K above 100 °C.

For SOT402-1 (TSSOP14) package: P_{tot} derates linearly with 7.3 mW/K above 81 °C.

For SOT762-1 (DHVQFN14) package: Ptot derates linearly with 9.6 mW/K above 98 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	74AHC132		7.	Unit			
			Min	Тур	Мах	Min	Тур	Max]
V _{CC}	supply voltage		2.0	5.0	5.5	4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	0	-	5.5	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and	V _{CC} = 3.3 V ± 0.3 V	-	-	100	-	-	-	ns/V
	fall rate	V _{CC} = 5.0 V ± 0.5 V	-	-	20	-	-	20	ns/V

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Мах	Min	Max	1
74AHC1	32	1	1			1			1	
V _{OH}	HIGH-level	$V_{I} = V_{T+}$ or V_{T-}								
	output voltage	I _O = -50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	2.2	1.9	-	V
		I _O = -50 μA; V _{CC} = 3.0 V	2.9	3.0	-	2.9	3.15	2.9	-	V
		I _O = -50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	3.85	4.4	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I _O = -8.0 mA; V _{CC} = 4.5 V	3.94	-	-	3.80	-	3.70	-	V
V _{OL}	LOW-level	$V_{I} = V_{T+}$ or V_{T-}								
	output voltage	I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I _O = 8.0 mA; V _{CC} = 4.5 V	-	-	0.36	-	0.44	-	0.55	V
I	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	2.0	-	20	-	40	μA
Cı	input capacitance	V _I = V _{CC} or GND	-	3	10	-	10	-	10	pF
Co	output capacitance		-	4	-	-	-	-	-	pF
74AHCT	132	1							1	-
V _{OH}	HIGH-level	$V_{I} = V_{T+}$ or V_{T-} ; $V_{CC} = 4.5 V$								
	output voltage	I _O = -50 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -8.0 mA	3.94	-	-	3.80	-	3.70	-	V
V _{OL}	LOW-level	$V_{I} = V_{T+} \text{ or } V_{T-}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I _O = 8.0 mA	-	-	0.36	-	0.44	-	0.55	V
I	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{CC}	supply current	$V_1 = V_{CC}$ or GND; $I_0 = 0$ A; $V_{CC} = 5.5$ V	-	-	2.0	-	20	-	40	μA
ΔI _{CC}	additional supply current	per input pin; $V_I = V_{CC} - 2.1 V$; other pins at V_{CC} or GND; $I_O = 0 A$; $V_{CC} = 4.5 V$ to 5.5 V	-	-	1.35	-	1.5	-	1.5	mA
CI	input capacitance	V _I = V _{CC} or GND	-	3	10	-	10	-	10	pF
Co	output capacitance		-	4	-	-	-	-	-	pF

10. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 7.

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to	o +125 °C	Unit	
				Min	Typ[1]	Max	Min	Max	Min	Мах	1
74AHC1	32				1			1	I		
t _{pd}	propagation	nA, nB to nY; see <u>Fig. 6</u>	[2]								
	delay	V _{CC} = 3.0 V to 3.6 V									
		C _L = 15 pF		-	4.4	11.9	1.0	14.0	1.0	15.0	ns
		C _L = 50 pF		-	6.2	15.4	1.0	17.5	1.0	19.5	ns
		V _{CC} = 4.5 V to 5.5 V									
		C _L = 15 pF		-	3.3	7.7	1.0	9.0	1.0	10.0	ns
		C _L = 50 pF		-	4.7	9.7	1.0	11.0	1.0	12.5	ns
C _{PD}	power dissipation capacitance	$f_i = 1 \text{ MHz}; V_i = \text{GND to } V_{\text{CC}}$	[3]	-	11	-	-	-	-	-	pF
74AHCT	132; V _{CC} = 4.	5 V to 5.5 V				1					
t _{pd}		nA, nB to nY; see <u>Fig. 6</u>	[2]								
	delay	C _L = 15 pF		-	3.5	7.0	1.0	8.0	1.0	9.0	ns
		C _L = 50 pF		-	5.0	8.0	1.0	9.0	1.0	10.0	ns
C _{PD}	power dissipation capacitance	$f_i = 1 \text{ MHz}; V_i = \text{GND to } V_{\text{CC}}$	[3]	-	14	-	-	-	-	-	pF

Typical values are measured at nominal supply voltage (V_{CC} = 3.3 V and V_{CC} = 5.0 V). [1]

[2]

 t_{pd} is the same as t_{PLH} and t_{PHL} . C_{PD} is used to determine the dynamic power dissipation (P_D in μ W). [3]

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

 $f_o = output$ frequency in MHz;

C_L = output load capacitance in pF;

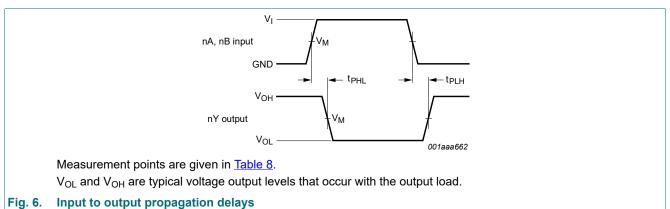
V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

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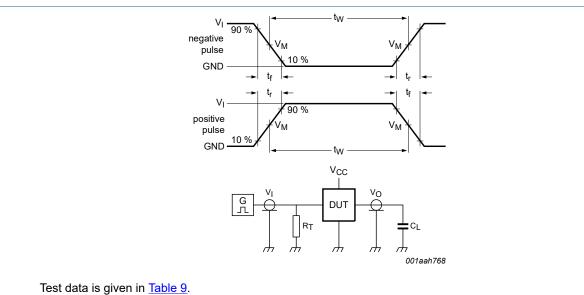
10.1. Waveform and test circuit



rig. 6. Input to output propagation de

Table 8. Measurement points

Туре	Input	Output
	V _M	V _M
74AHC132	0.5 x V _{CC}	0.5 x V _{CC}
74AHCT132	1.5 V	0.5 x V _{CC}



Definitions test circuit:

 R_T = termination resistance should be equal to output impedance Z_o of the pulse generator.

C_L = load capacitance including jig and probe capacitance.

Fig. 7. Test circuit for measuring switching times

Table	9. '	Test	data	

Туре	Input Loa		Load	Test
	VI	t _r , t _f	CL	
74AHC132	V _{CC}	≤ 3.0 ns	50 pF, 15 pF	t _{PLH} , t _{PHL}
74AHCT132	3.0 V	≤ 3.0 ns	50 pF, 15 pF	t _{PLH} , t _{PHL}

74AHC_AHCT132

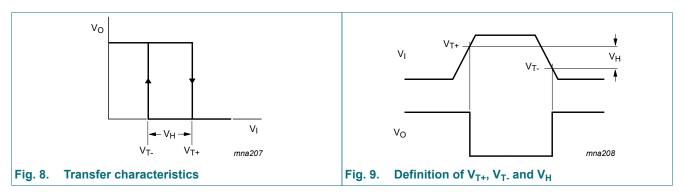
11. Transfer characteristics

Table 10. Transfer characteristics

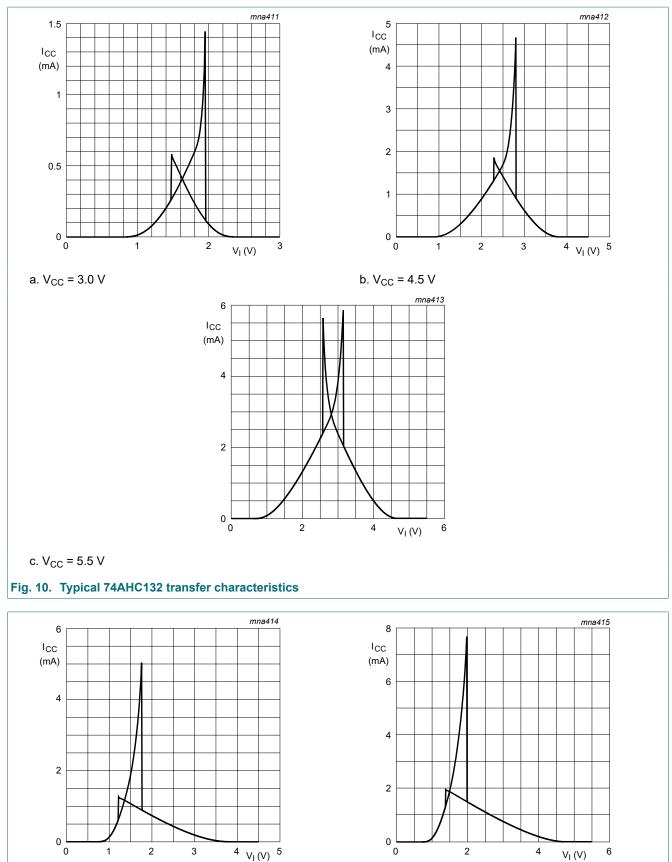
At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Мах	Min	Max	
74AHC1	32									
V _{T+}	positive-going threshold	V _{CC} = 3.0 V	-	-	2.2	-	2.2	-	2.2	V
	voltage	V _{CC} = 4.5 V	-	-	3.15	-	3.15	-	3.15	V
		V _{CC} = 5.5 V	-	-	3.85	-	3.85	-	3.85	V
V _{T-}	negative-going threshold	V _{CC} = 3.0 V	0.9	-	-	0.9	-	0.9	-	V
	voltage	V _{CC} = 4.5 V	1.35	-	-	1.35	-	1.35	-	V
		V _{CC} = 5.5 V	1.65	-	-	1.65	-	1.65	-	V
V _H	hysteresis voltage	V _{CC} = 3.0 V	0.3	-	1.2	0.3	1.2	0.25	1.2	V
		V _{CC} = 4.5 V	0.4	-	1.4	0.4	1.4	0.35	1.4	V
		V _{CC} = 5.5 V	0.5	-	1.6	0.5	1.6	0.45	1.6	V
74АНСТ	132									
V _{T+}	positive-going threshold	V _{CC} = 4.5 V	-	-	1.9	-	1.9	-	1.9	V
	voltage	V _{CC} = 5.5 V	-	-	2.1	-	2.1	-	2.1	V
V _{T-}	negative-going threshold	V _{CC} = 4.5 V	0.5	-	-	0.5	-	0.5	-	V
	voltage	V _{CC} = 5.5 V	0.6	-	-	0.6	-	0.6	-	V
V _H	hysteresis voltage	V _{CC} = 4.5 V	0.3	-	1.4	0.3	1.4	0.3	1.4	V
		V _{CC} = 5.5 V	0.3	-	1.5	0.3	1.5	0.3	1.5	V

11.1. Transfer characteristics waveforms

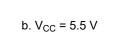


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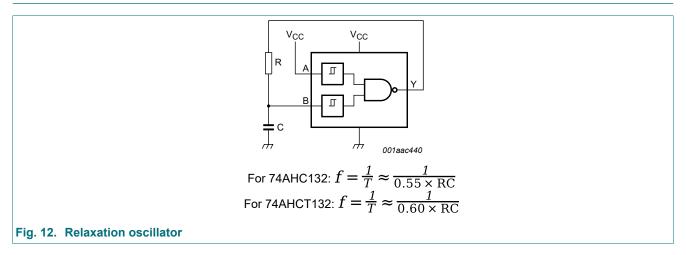
a. V_{CC} = 4.5 V

Fig. 11. Typical 74AHCT132 transfer characteristics



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12. Application information



13. Package outline

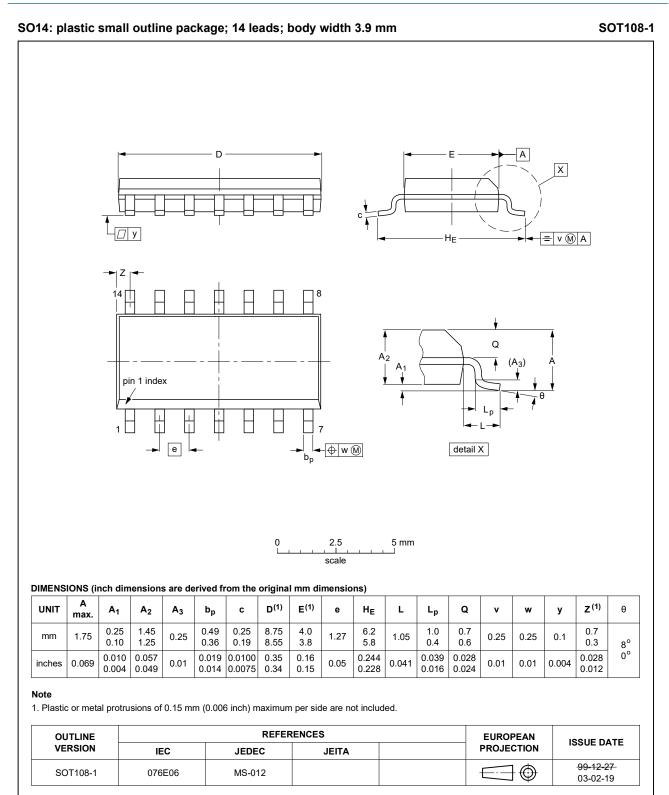


Fig. 13. Package outline SOT108-1 (SO14)

Quad 2-input NAND Schmitt trigger

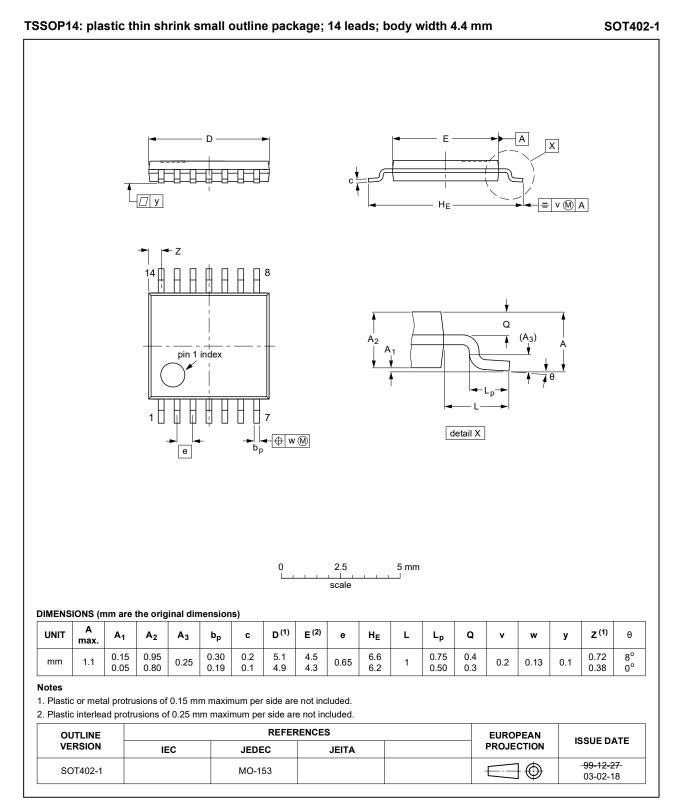


Fig. 14. Package outline SOT402-1 (TSSOP14)

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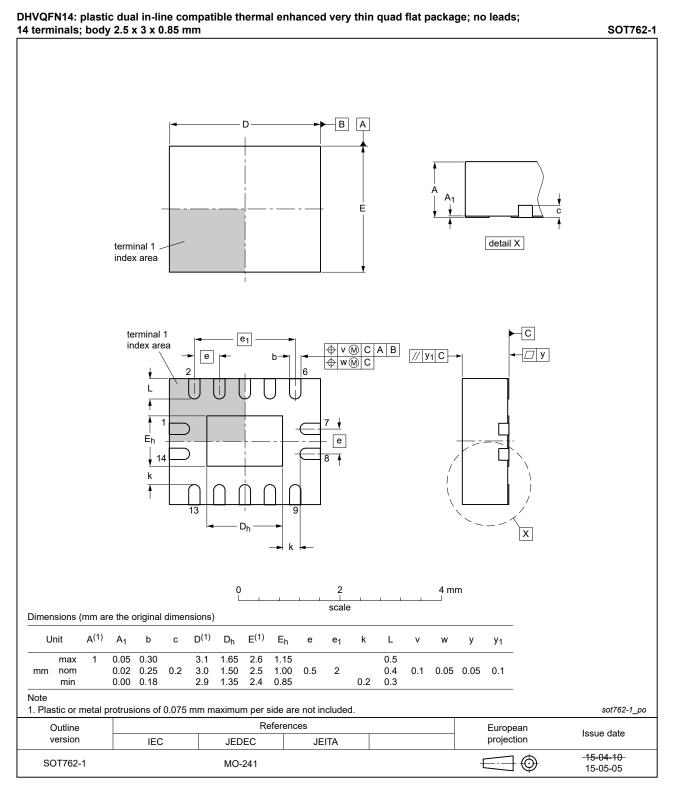


Fig. 15. Package outline SOT762-1 (DHVQFN14)

14. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

15. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AHC_AHCT132 v.7	20200703	Product data sheet	-	74AHC_AHCT132 v.6	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. <u>Section 1</u> and <u>Section 2</u> updated. <u>Table 4</u>: Derating values for P_{tot} total power dissipation updated. Package outline drawing of SOT762-1 (<u>Fig. 15</u>) updated. 				
74AHC_AHCT132 v.6	20090504	Product data sheet	-	74AHC_AHCT132 v.5	
Modifications:	• <u>Table 6</u> : the conditions for HIGH-level output voltage and LOW-level output voltage have been changed.				
74AHC_AHCT132 v.5	20080509	Product data sheet	-	74AHC_AHCT132 v.4	
74AHC_AHCT132 v.4	20050207	Product data sheet	-	74AHC_AHCT132 v.3	
74AHC_AHCT132 v.3	20040415	Product specification	-	74AHC_AHCT132 v.2	
74AHC_AHCT132 v.2	19990924	Product specification	-	74AHC_AHCT132 v.1	
74AHC_AHCT132 v.1	19990531	Product specification	-	-	

16. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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