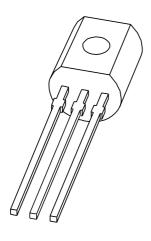
DISCRETE SEMICONDUCTORS

DATA SHEET



BC369 PNP medium power transistor; 20 V, 1 A

Product data sheet Supersedes data of 2003 Nov 20



PNP medium power transistor; 20 V, 1 A

BC369

FEATURES

- High current
- Two current gain selections.

APPLICATIONS

- · Linear voltage regulators
- · High side switches
- Supply line switches
- MOSFET drivers
- · Audio pre-amplifiers.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V_{CEO}	collector-emitter voltage	_	-20	>
I _C	collector current (DC)	_	-1	Α
I _{CM}	peak collector current	_	-2	Α
h _{FE}	DC current gain			
	BC369	85	375	
	BC369-16	100	250	
	BC369-25	160	375	

DESCRIPTION

PNP medium power transistor (see "Simplified outline, symbol and pinning") for package details.

PRODUCT OVERVIEW

TYPE NUMBER	PAC	KAGE	MARKING CODE
ITPE NUMBER	PHILIPS EIAJ		WARKING CODE
BC369	SOT54	SC-43A	C369
BC369-16	SOT54	SC-43A	C36916
BC369-25	SOT54	SC-43A	C36925

SIMPLIFIED OUTLINE, SYMBOL AND PINNING

TYPE NUMBER	CIMPLIFIED OUTLINE AND CVMPOL	PINNING		
TYPE NUMBER	SIMPLIFIED OUTLINE AND SYMBOL	PIN	DESCRIPTION	
BC369	1 2	1	base	
	23 ====================================	2	collector	
		3	emitter	
	MAM285			

ORDERING INFORMATION

TYPE NUMBER	PACKAGE				
ITPE NUMBER	NAME DESCRIPTION		VERSION		
BC369	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54		
BC369-16					
BC369-25					

PNP medium power transistor; 20 V, 1 A

BC369

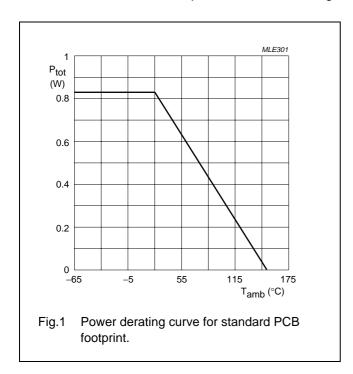
LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-32	V
V _{CEO}	collector-emitter voltage	open base	_	-20	V
V _{EBO}	emitter-base voltage	open collector	_	-5	V
I _C	collector current (DC)		_	-1	Α
I _{CM}	peak collector current		-	-2	Α
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; notes 1 and 2	-	830	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	ambient temperature		-65	+150	°C

Notes

- 1. Refer to SOT54 (SC-43A) standard mounting conditions.
- 2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint for SOT54.



PNP medium power transistor; 20 V, 1 A

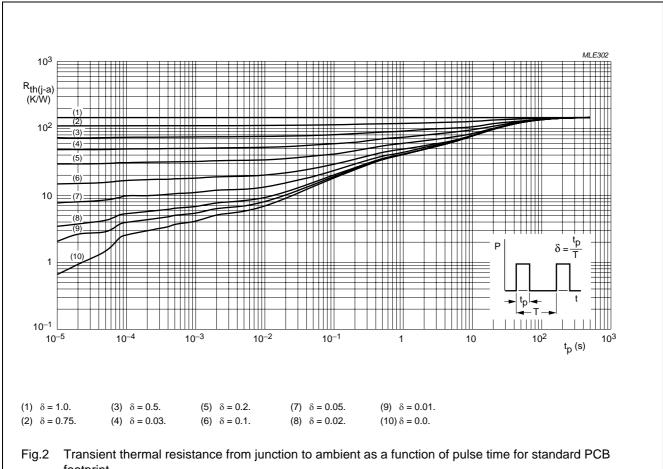
BC369

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	T _{amb} ≤ 25 °C; notes 1 and 2	150	K/W

Notes

- 1. Refer to SOT54 (SC-43A) standard mounting conditions.
- 2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint for SOT54.



footprint.

PNP medium power transistor; 20 V, 1 A

BC369

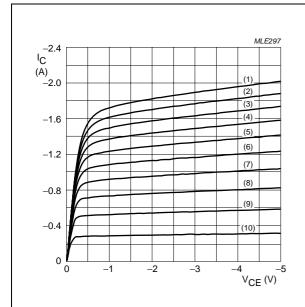
CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	$V_{CB} = -25 \text{ V}; I_E = 0 \text{ A}$	_	_	-100	nA
		$V_{CB} = -25 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$	_	_	-10	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}$	_	_	-100	nA
h _{FE}	DC current gain					
	BC369	$V_{CE} = -10 \text{ V}; I_{C} = -5 \text{ mA}$	50	_	_	
		$V_{CE} = -1 \text{ V; } I_{C} = -500 \text{ mA}$	85	_	375	
		$V_{CE} = -1 \ V; \ I_{C} = -1 \ A$	60	_	_	
	BC369-16	$V_{CE} = -1 \text{ V; } I_{C} = -500 \text{ mA}$	100	_	250	
	BC369-25	$V_{CE} = -1 \text{ V; } I_{C} = -500 \text{ mA}$	160	_	375	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -1 A$; $I_B = -100 \text{ mA}$	_	_	-500	mV
V_{BE}	base-emitter voltage	$V_{CE} = -10 \text{ V}; I_{C} = -5 \text{ mA}$	_	_	-700	mV
		$V_{CE} = -1 \text{ V; } I_{C} = -1 \text{ A}$	_	_	-1	V
C _c	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$	_	28	_	pF
f _T	transition frequency	$V_{CE} = -5 \text{ V}; I_{C} = -50 \text{ mA}; f = 100 \text{ MHz}$	40	140	_	MHz

PNP medium power transistor; 20 V, 1 A

BC369



BC369-16.

 T_{amb} = 25 °C.

(1) $I_B = -18 \text{ mA}.$

(6) $I_B = -9.0 \text{ mA}.$

(2) $I_B = -16.2 \text{ mA}.$

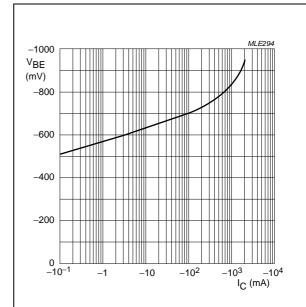
(7) $I_B = -7.2 \text{ mA}.$

(3) $I_B = -14.4 \text{ mA}.$ (4) $I_B = -12.6 \text{ mA}.$ (8) $I_B = -5.4 \text{ mA}$. (9) $I_B = -3.6 \text{ mA}$.

(5) $I_B = -10.8 \text{ mA}.$

(10) $I_B = -1.8 \text{ mA}$.

Fig.3 Collector current as a function of collector-emitter voltage; typical values.



BC369-16.

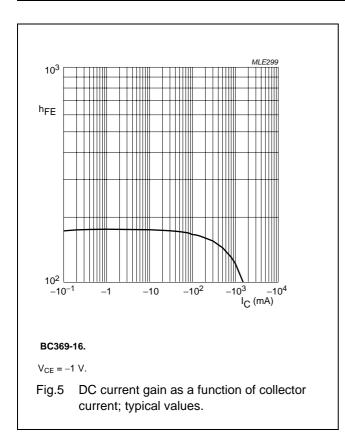
 $V_{CE} = -1 V$.

6

Fig.4 Base-emitter voltage as a function of collector current; typical values.

PNP medium power transistor; 20 V, 1 A

BC369



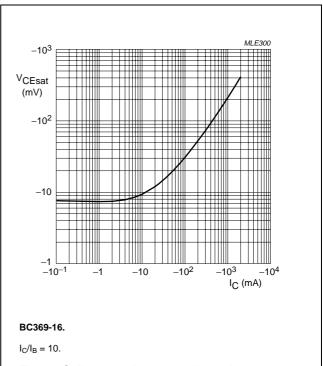


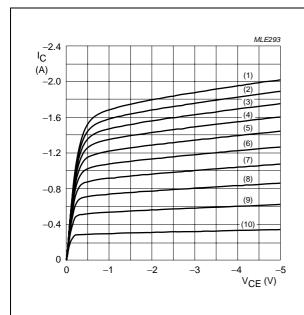
Fig.6 Collector-emitter saturation voltage as a function of collector current; typical values.

2004 Nov 05

7

PNP medium power transistor; 20 V, 1 A

BC369



BC369-25.

 $T_{amb} = 25 \, ^{\circ}C.$

(1) $I_B = -12 \text{ mA}.$

(6) $I_B = -6.0 \text{ mA}.$ (7) $I_B = -4.8 \text{ mA}.$

(2) $I_B = -10.8 \text{ mA}.$ (3) $I_B = -9.6 \text{ mA}.$

(7) $I_B = -4.8 \text{ mA}.$ (8) $I_B = -3.6 \text{ mA}.$

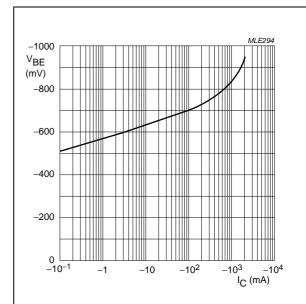
(4) $I_B = -8.4 \text{ mA}.$

(9) $I_B = -2.4 \text{ mA}.$

(5) $I_B = -7.2 \text{ mA}.$

(10) $I_B = -1.2 \text{ mA}.$

Fig.7 Collector current as a function of collector-emitter voltage; typical values.



BC369-25.

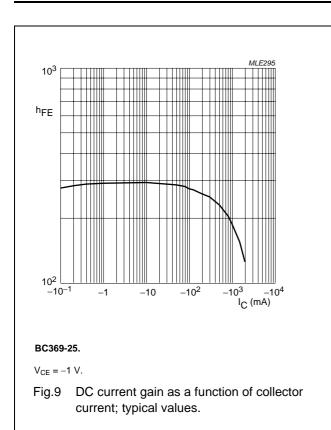
 $V_{CE} = -1 V$.

8

Fig.8 Base-emitter voltage as a function of collector current; typical values.

PNP medium power transistor; 20 V, 1 A

BC369



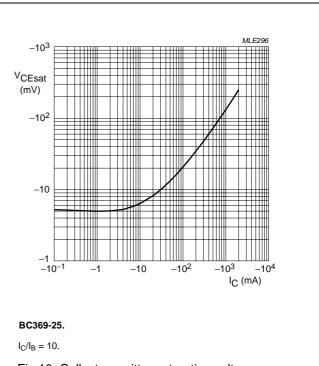


Fig.10 Collector-emitter saturation voltage as a function of collector current; typical values.

2004 Nov 05

9

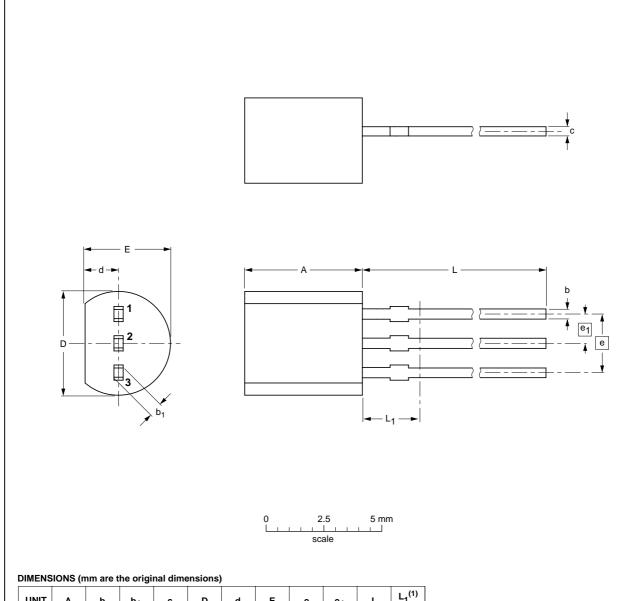
PNP medium power transistor; 20 V, 1 A

BC369

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



UNIT	Α	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾ max.
mm	5.2 5.0	0.48 0.40	0.66 0.55	0.45 0.38	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT54		TO-92	SC-43A			-04-06-28- 04-11-16

PNP medium power transistor; 20 V, 1 A

BC369

DATA SHEET STATUS

DOCUMENT STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

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