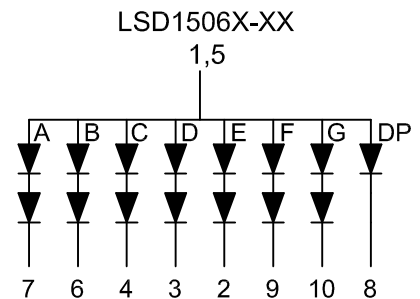
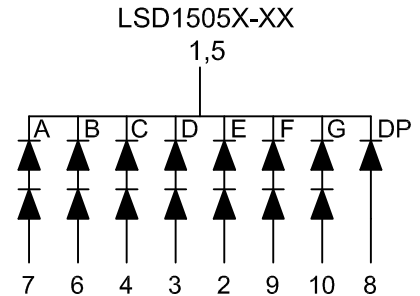
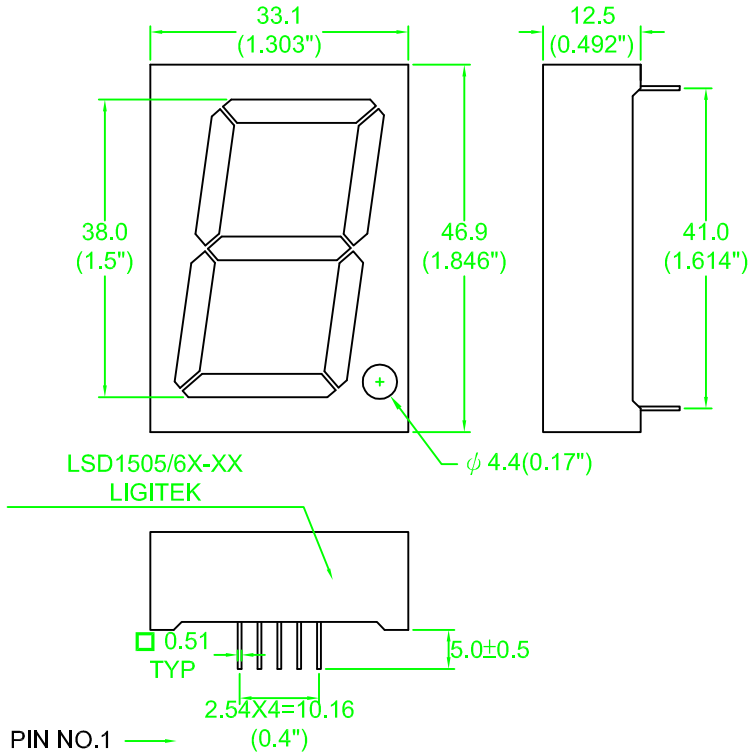




PACKAGE DIMENSION

INTERNAL CIRCUIT DIAGRAM



NOTE:1.All Dimension Are In Millimeters And (Inch)
Tolerance Is $\pm 0.25(0.01)$ " unless Otherwise Noted
2.Specifications are subject to change without notice.

▪ Connection To Electrical Schematic

Electrical connection

PIN NO.	LSD1505X-XX	PIN NO.	LSD1506X-XX
1	Common Cathode	1	Common Anode
2	Anode E	2	Cathode E
3	Anode D	3	Cathode D
4	Anode C	4	Cathode C
5	Common Cathode	5	Common Anode
6	Anode B	6	Cathode B
7	Anode A	7	Cathode A
8	Anode DP	8	Cathode DP
9	Anode F	9	Cathode F
10	Anode G	10	Cathode G

• Part Selection And Application Information(Ratings At 25°C Ambient)

PART NO	CHIP		common cathode or anode	λ_p (nm)	$\Delta\lambda$ (nm)	Electrial					IV-M
	material	emitted				Vf(v)			Iv(mcd)		
						Min	Typ	Max	Min	Typ	
LSD15055-XX	GaAlAs	Red	Common Cathode	660	20	3.0	3.4	4.8	6.1	9.8	2:1
LSD15051-XX	GaP	Red		697	90	3.4	4.2	5.6	1.75	2.5	2:1
LSD15052-XX	GaP	Green		565	30	3.4	4.2	5.6	5.0	7.5	2:1
LSD15053-XX	GaAsP/GaP	Yellow		585	35	3.4	4.0	5.6	4.0	7.0	2:1
LSD15054-XX	GaAsP/GaP	Orange		635	45	3.4	4.0	5.6	5.0	7.5	2:1
LSD15065-XX	GaAlAs	Red	Common Anode	660	20	3.0	3.4	4.8	6.1	9.8	2:1
LSD15061-XX	GaP	Red		697	90	3.4	4.2	5.6	1.75	2.5	2:1
LSD15062-XX	GaP	Green		565	30	3.4	4.2	5.6	5.0	7.5	2:1
LSD15063-XX	GaAsP/GaP	Yellow		585	35	3.4	4.0	5.6	4.0	7.0	2:1
LSD15064-XX	GaAsP/GaP	Orange		635	45	3.4	4.0	5.6	5.0	7.5	2:1

• Absoult Maximum Rating (Ta=25°C)

Parameter	Red		Green		Yellow		Orange		Unit	Remark
	SR	H	G	Y	E					
Forward Current Per Chip	40	15	30	20	30			mA		
Peak Current Per Chip (Duty 1/10,0.1mS Pulse Width)	200	60	120	80	120			mA		
Power Dissipation Per Chip	110	45	100	85	100			mW		
Derating Linear From 25°C Per Chip	0.45	0.25	0.45	0.45	0.45			mA/°C		
Reverse Current Per Any Chip	10		10	10	10			μA		
Operating Temperature	-25°C TO +85°C									
Storage Temperature	-25°C TO +85°C									

Solder Temperature 1-16 Inch Below Seating Plane For 3 Seconds At 260 °C

• Test Condition For Each Parameter

Parameter	Symbol	Unit	Test Condition
Forward Voltage Per Chip	Vf	volt	If=20mA
Luminous Intensity Per Chip	Iv	mcd	If=10mA
Peak Emission Wavelength	λ_p	nm	If=20mA
Spectral Line Half-Width	$\Delta\lambda$	nm	If=20mA
Reverse Current Any Chip	Ir	μA	Vr=5V
Luminous Intensity Matching Ratio	IV-M		