3-phase motor driver

BA6444FP

The BA6444FP is a 3-phase, full-wave, pseudo-linear motor driver suited for VCR capstan motors. The IC has a torque ripple cancellation circuit to reduce wow and flutter, and a forced brake circuit that allows abrupt change of operational mode. The output transistor saturation prevention circuit provides superb torque control over a wide range of current. FG and hysteresis amplifiers are also built in.

●Applications

VCR capstan motors, DAT capstan motors

●Features

1) 3-phase, full-wave, pseudo-linear drive system.
2) Torque ripple cancellation circuit.
3) Forced brake circuit.
4) Output transistor (high- and low-sides) saturation prevention circuit.
5) FG and hysteresis amplifiers.
6) Thermal shutdown circuit.

●Absolute maximum ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit
Power supply voltage	V_{CC}	7	V
Power supply voltage	V_M	36	V
Power dissipation	Pd	1700*1	mW
Operating temperature	Topr	−20~75	℃
Storage temperature	Tstg	−40~150	℃
Allowable output current	$I_{O\,peak}$	1500*2	mA

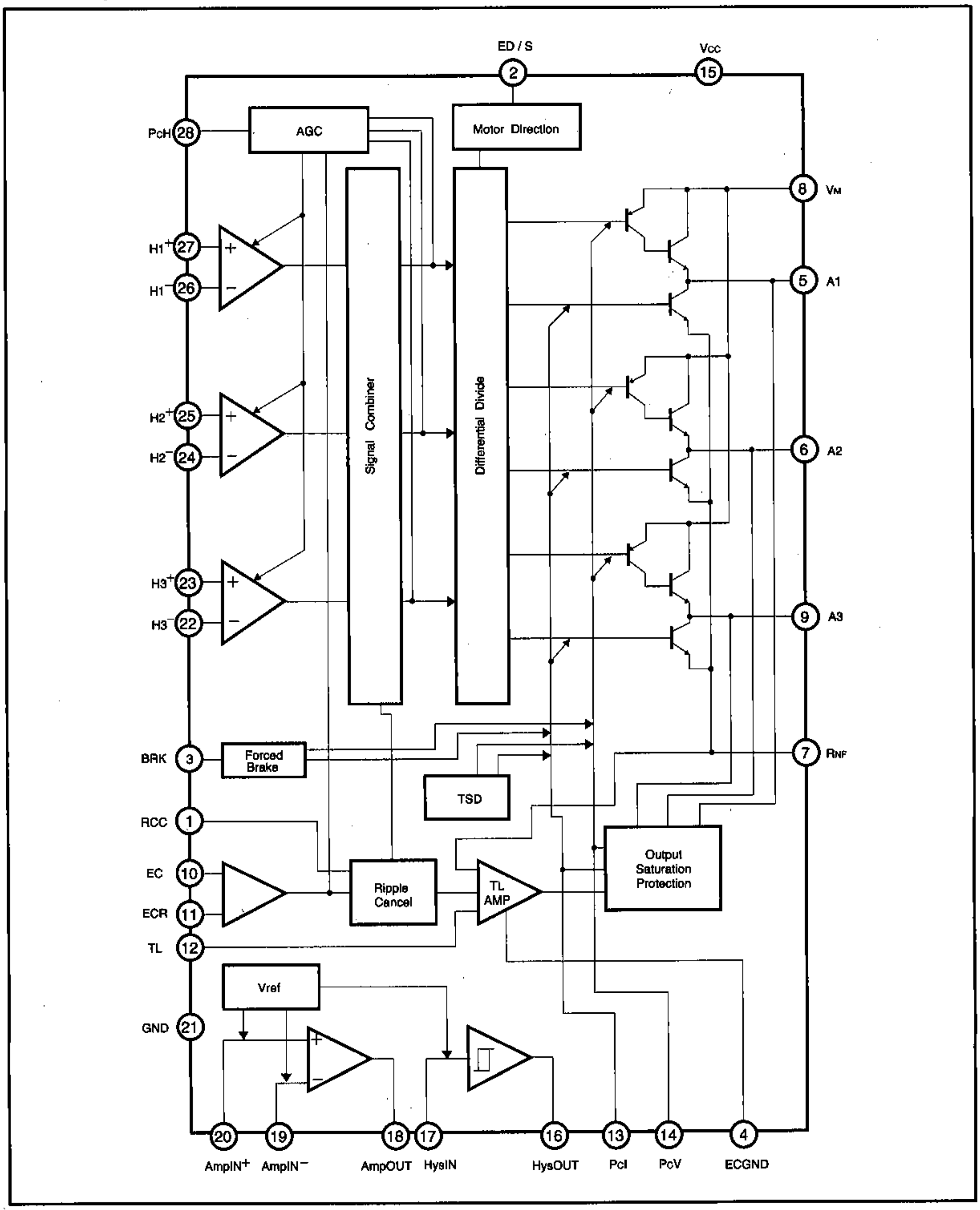
*1 Mounted on a glass epoxy PCB (70X 70 X 1.6 mm).
Reduce power by 13.6 mW for each degree above 25 ℃.
*2 Should not exceed Pd- or ASO-value.

●Recommended operating conditions

Parameter	Symbol	Range	Unit
Operating power supply voltage	V_{CC}	4~6	V
	V_M	3~32*3	V
Hall signal input voltage		1.5~ (V_{CC}−1.5)	V

*3 Should not exceed ASO-value.

● Block diagram



●Pin descriptions

Pin No.	Pin name	Function
1	R_{CC}	Resistor connection pin for changing the ripple cancellation ratio
2	ED / S	Forward when LOW; stop when MEDIUM; reverse when HIGH
3	BRK	Forced brake pin; brake mode when LOW
4	ECGND	Torque amplifier ground pin
5	A1	Motor output pin
6	A2	Motor output pin
7	R_{NF}	Motor ground pin; connect a resistor (0.5 Ω recommended) for current sensing
8	V_M	Motor power supply pin
9	A3	Motor output pin
10	E_C	Torque control voltage input pin
11	E_{CR}	Torque control reference voltage input pin
12	TL	Torque limit pin
13	PcI	Capacitor connection pin for phase compensation of the low-side saturation prevention circuit
14	PcV	Capacitor connection pin for phase compensation of the high-side saturation prevention circuit
15	V_{CC}	Power supply pin
16	Hys OUT	Schmitt trigger amplifier output pin
17	Hys IN	Schmitt trigger amplifier input pin
18	Amp OUT	Amplifier output pin
19	Amp IN^-	Amplifier input pin, inverted
20	Amp IN^+	Amplifier input pin, non-inverted
21	GND	Ground pin
22	H_3^-	Hall signal input pin
23	H_3^+	Hall signal input pin
24	H_2^-	Hall signal input pin
25	H_2^+	Hall signal input pin
26	H_1^-	Hall signal input pin
27	H_1^+	Hall signal input pin
28	PcH	Capacitor connection pin for Hall amplifier AGC circuit phase compensation

●Input/output circuits

1. I/O circuit interface

Resistances, in Ω, are typical values. Note that the resistance values can vary ±30%.

(1) ED/S pin (2 pin)

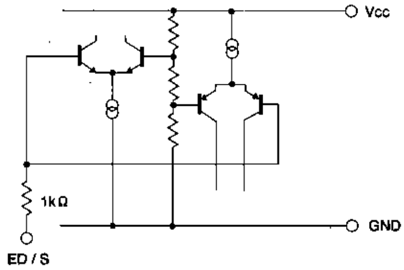


Fig.1

(2) BRK pin (3 pin)

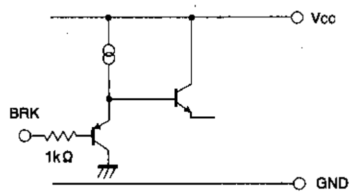


Fig.2

(3) Motor output (A1, 5 pin ; A2, 6 pin ; A3, 9 pin)

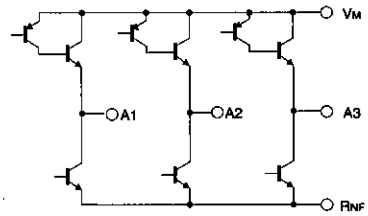


Fig.3

(4) Ec and ECR pins (10 pin, 11 pin)

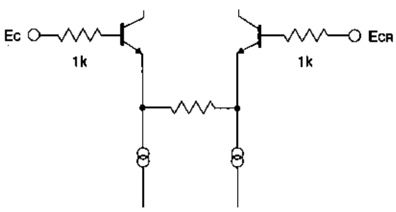


Fig.4

(5) TL pin (12 pin)

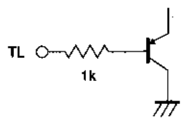


Fig.5

(6) Hall signal input pins

(H_1^+ : 27 pin, H_1^- : 26 pin, H_2^+ : 25 pin, H_2^- : 24 pin, H_3^+ : 23 pin, H_3^+ : 22 pin)

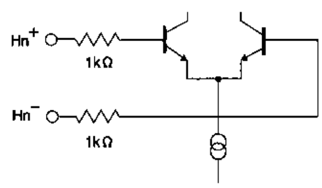


Fig.6

(7) Schmitt trigger amplifier I/O pins (17 pin, 16 pin)

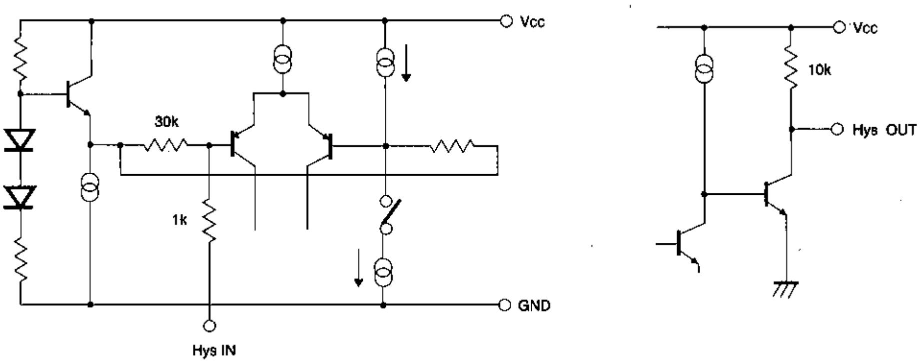


Fig.7

(8) Amplifier I/O pins (20 pin, 19 pin, 18 pin)

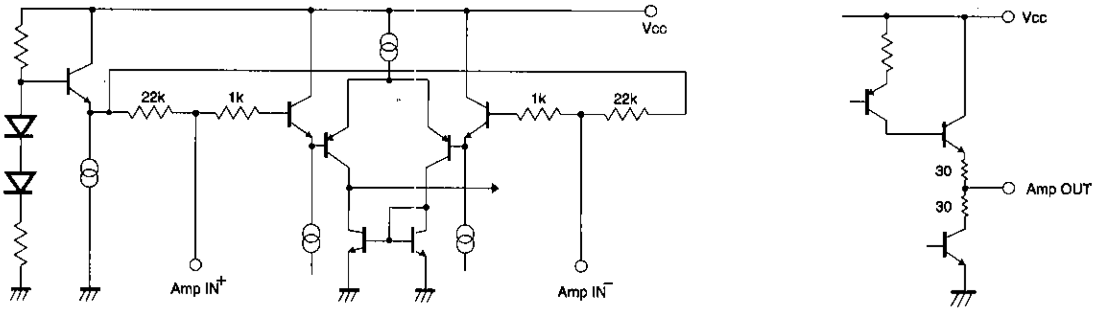


Fig.8

●Electrical characteristics (Unless otherwise noted, Ta=25°C, V_{CC}=5V, V_M=12V)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Circuit current	I_{CC}	−	10	15	mA	E_C= E_{CR} - 0.1, ED/S = M, input = (L, L, H)
Hall device input conversion offset	H_{E}ofs	−6	0	6	mV	
Hall device input conversion offset differential	ΔH_{E}ofs	0	−	8	mV	
Torque control offset	Ecofs	−100	−	100	mV	
Output idle voltage	Ecidle	−	0	10	mV	
Torque control input gain	Gio	0.52	0.58	0.64	A / V	E_C= 2.7→2.8, input = (L, L, H), R_{NF} = 0.5Ω
Brake ON voltage	BR_{ON}	−	−	0.7	V	
Brake OFF voltage	BR_{OFF}	2.0	−	−	V	
Forward ON voltage	ED / F	−	−	0.9	V	
Stop ON voltage	ED / S	1.3	−	3.0	V	
Reverse ON voltage	ED / R	3.5	−	−	V	
TL-R_{NF} offset	TL–R_{NF}ofs	38	60	88	mV	TL=0.35V
Ripple cancellation ratio	V_{RCC}	3.0	3.9	4.8	%	R_{CC}= 10 kΩ, input = (L, L, H)→(L, M, H)
HIGH level output voltage	V_{OH}	0.8	1.2	1.55	V	I_O=0.8A
LOW level output voltage	V_{OL}	1.15	1.6	2.05	V	I_O=0.8A
Output current capacity	$I_{O Max.}$	1.4	−	−	A	V_{CC}= 4.5 V, input = (H, L, M)
[FGAMP]						
Input impedance	R_{BA}	15.4	22	28.6	kΩ	
Open gain 1	GA 1	65	70	−	dB	f=500Hz
Open gain 2	GA 2	33	38	−	dB	f=20kHz
DC bias voltage	VBA	2.25	2.5	2.75	V	
HIGH level output voltage	VOH A	3.6	4	−	V	I_{OA}=0.5mA
LOW level output voltage	VOL A	−	0.9	1.3	V	I_{OA}=0.5mA
Input voltage	VAB	1.5	−	3.8	V	
[Schmitt trigger amplifier]						
Hysteresis width	Vhys	±115	±155	±195	mV	
DC bias voltage	Vbhys	2.25	2.5	2.75	V	
LOW level output voltage	V_{OL}hys	−	100	320	mV	I_{OLhys}=2mA

◎Not designed for radiation resistance

● Circuit operation

(1) Pseudo-linear output and torque ripple cancellation

The IC generates a trapezoidal (pseudo-linear) output current, whose waveform phase is 30 degrees ahead of that of the Hall input voltage (Fig. 9).

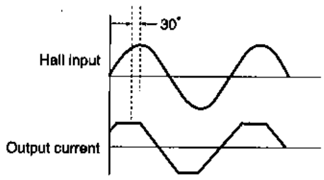


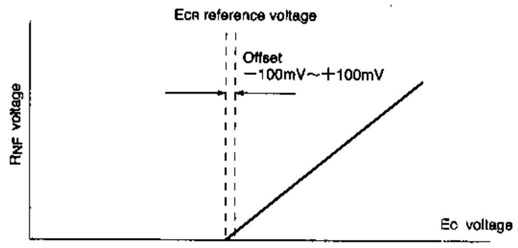
Fig. 9

Fig.10 Torque ripple cancellation

The trapezoidal waveform of output current would create intermittence in the magnetic field generated by the 3-phase motor, and would result in an irregular rotation of the motor. To prevent this, the output waveform is obtained by superimposing a triangular wave on the trapezoidal wave (Fig. 10). This process is called torque ripple cancellation.

(2) Torque control

The output current can be controlled by adjusting the voltage applied to the torque control pins.



The pins are the inputs to a differential amplifier. A reference voltage between ±2.3-3.0 V (2.5 V recommended) is applied to pin 11.

Fig.11

A brake is applied to the motor when the brake pin (3 pin) is put to LOW. The brake mode is activated when the brake pin voltage is 0.7V or less and deactivated when the voltage is 2.0V or more.

(3) Output current sensing and torque limitation

The R_{NF} pin (7 pin) is the ground pin for the output stage. To sense the output current, a resistor (0.5 Ω recommended) is connected between pin 7 and the ground. The output current is sensed by applying the voltage developed across this resistor to the TL amplifier input as a feedback.

The output current can be limited by adjusting the voltage applied to pin 12. The current is limited when pin 12 reaches the same potential as pin 7. The output current ($I_{MAX.}$) under this condition is given by :

$$I_{MAX.} = \frac{V_{TL} - (TL - R_{NF}\ \text{offset})}{R_{RNF}}$$

where R_{RNF} is the value of the resistor connected between the R_{NF} and ground pins and V_{TL} is the voltage applied to the TL pin.

(4) Motor direction control (ED/S pin)

The motor mode is :

Forward when the ED/S-pin voltage is less than 0.9V,
Stop when the voltage is between 1.3~3.0V,
Reverse when the voltage is above 3.5V.

In the stop mode, high- and low-side output transistors are turned off, resulting in a high impedance state.

(5) Output transistor saturation prevention circuit

This circuit monitors the output voltage and maintain the operation of the output transistors below their saturation levels. Operating the transistors in the linear characteristic range provides good control over a wide range of current and good torque characteristics even during overloading.

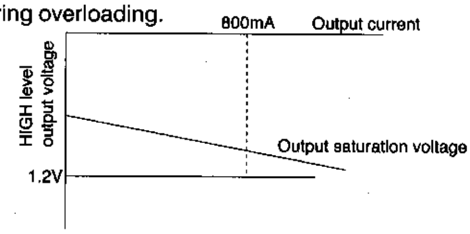


Fig.12 Transistor HIGH level output voltage

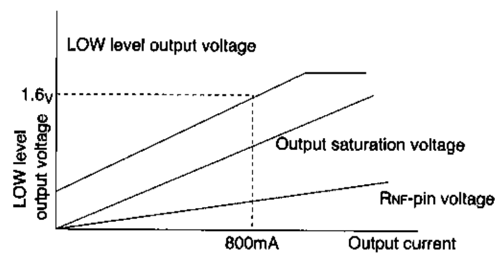


Fig.13 Transistor LOW level output voltage

(6) Ripple cancellation circuit

The cancellation ratio of the torque ripple cancellation circuit (Fig. 10) can be adjusted by an external resistor connected to pin 1. Select a suitable value by taking wow and flutter into consideration.

The ripple cancellation ratio can be obtained in the following manner. With E_C=2.7V, the R_{NF} value for the Hall input of ($H1^+$, $H2^+$, $H3^+$) = (L, L, H) is denoted as V1, and the R_{NF} value for the Hall input of ($H1^+$, $H2^+$, $H3^+$) = (L, M, H) is denoted as V2. The ripple cancellation ratio is then given by :

$$R_{CC}=\frac{V_2-V_1}{(V_1+V_2)/2}\times 100\ (\%)$$

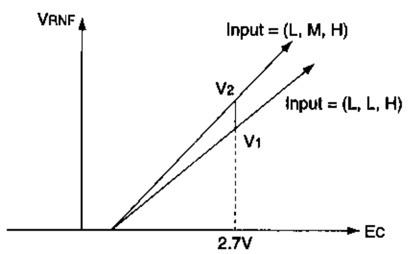


Fig.14

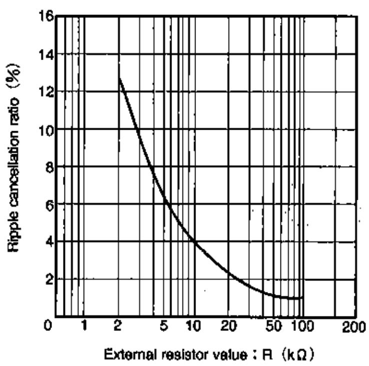


Fig.15 Ripple cancellation ratio vs. external resistor value (reference curve)

(7) Brake pin

The brake pin threshold depends on the chip temperature as shown in Fig. 16. Make sure that your application will work properly when using the IC at low or high temperatures.

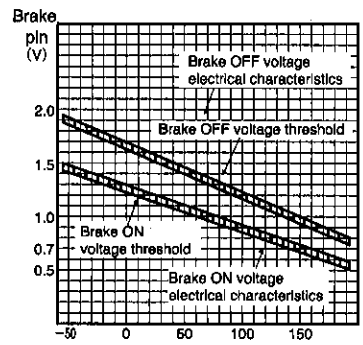
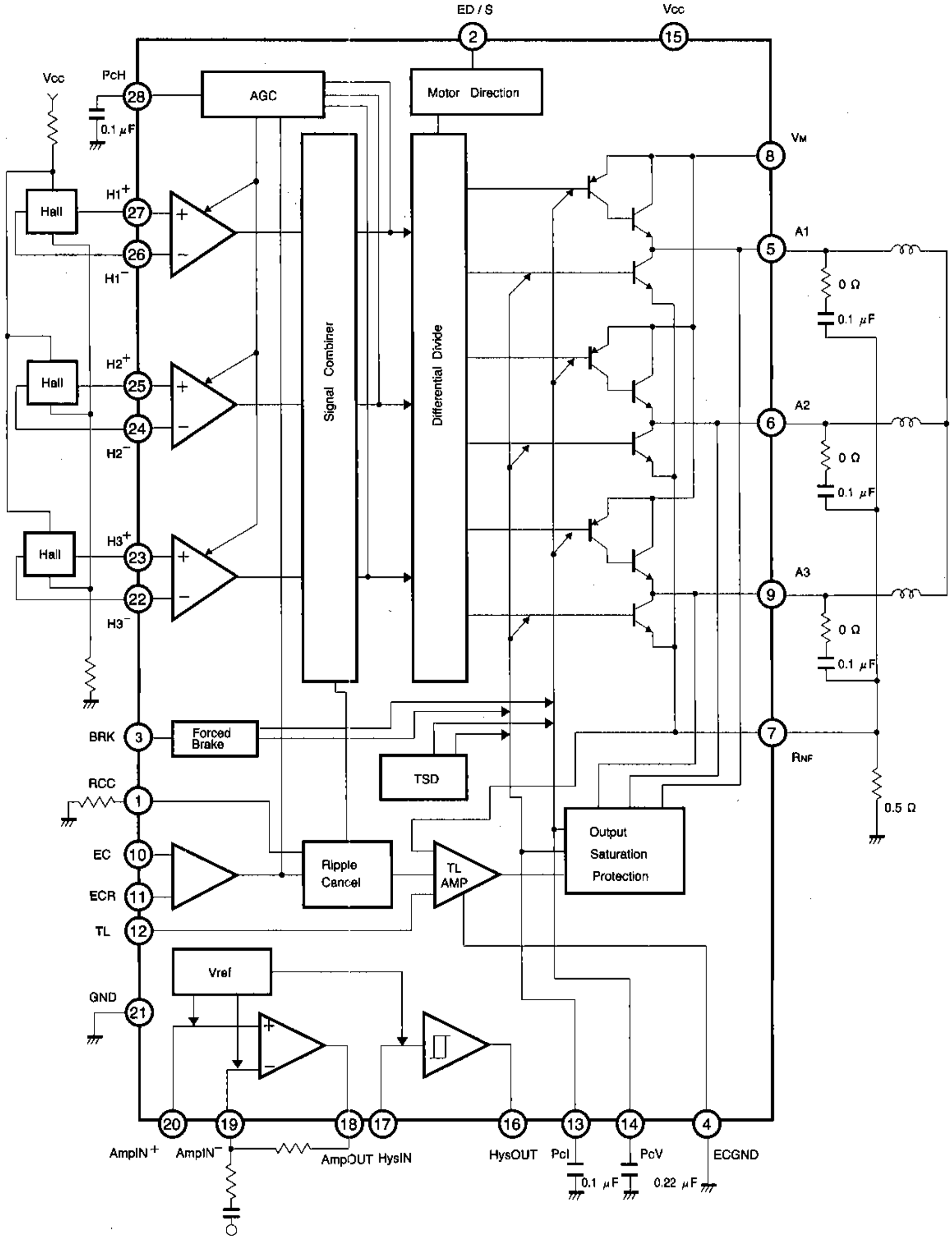


Fig.16 Brake pin threshold vs. chip temperature

●Application example



●Operation notes

(1) Thermal shut down circuit

The BA6444FP has a thermal shutdown circuit to protect the IC. The shutdown temperatures is 175℃ (typical) with a hysteresis width of 45℃ (typical).

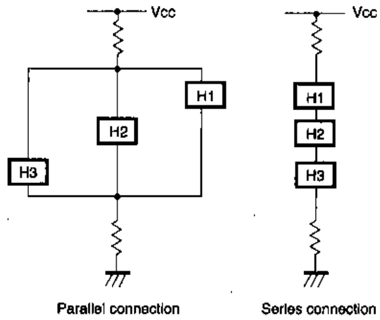
When the circuit is activated due to an increased in chip temperature, the output pins (5, 6, 9 pin) are set to the open state. The circuit is functional against excessive power dissipation, output short-circuiting, and other irregularities in the output current, but does not work against overheating caused by high internal currents due to externally caused IC damage or pin-to-pin short-circuiting.

(2) The brake circuit has temperature-dependent thresholds as shown in Fig. 16. Make sure that your application will work properly when using the IC at low or high temperatures.

(3) Be sure to connect the radiation fin to the ground.

(4) Hall input

The Hall input circuit is described in (6) of "I/O equivalent circuits." Hall devices can be connected in either series or parallel. Be sure to keep the Hall input within the range of 1.5V to (V_{CC}~1.5V).



(5) FG amplifier

Note that unpredictable outputs may occur when the FG amplifier input is outside the recommended range.

(6) ECGND pin (4 pin)

Pin 4, a torque amplifier ground pin, should be connected to the ground. By connecting this pin to a point close to the motor ground, you can prevent the effect of GND common impedance on the current-sensing resistor (0.5 Ω recommended) connected between RNF (7 pin) and the motor ground pin.

●Electrical characteristic curves

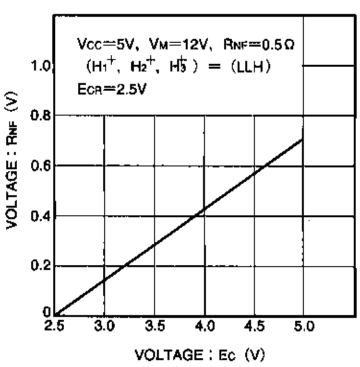


Fig.18 R_{NF}-pin voltage vs. E_C-pin voltage

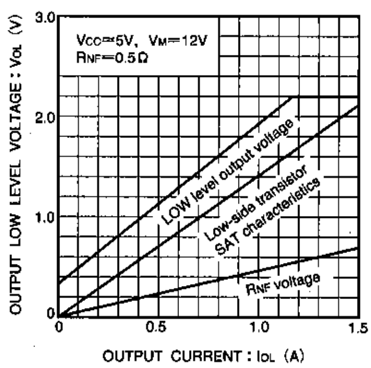


Fig.19 LOW level output voltage vs. output current

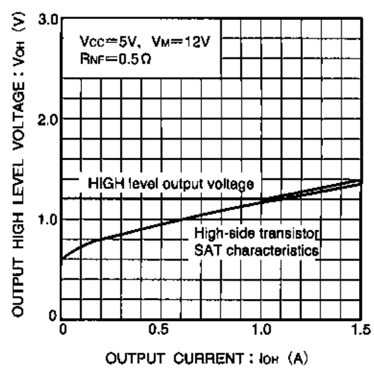


Fig.20 HIGH level output voltage vs. output current

●Electrical characteristic curves

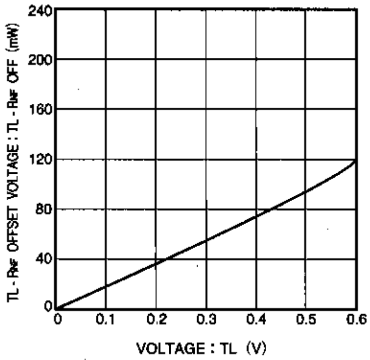


Fig.21 TL-RNF offset voltage vs. TL voltage

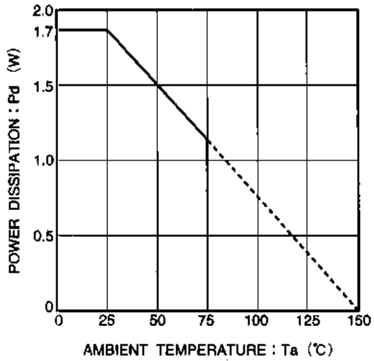
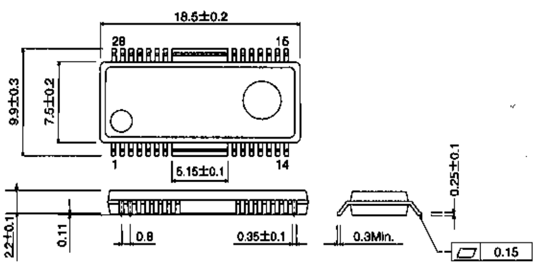


Fig.22 Power dissipation vs. ambient temperature

●External dimensions (Units: mm)



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