TOSHIBA Bi-CMOS Digital Integrated Circuit Silicon Monolithic

TB2904HQ

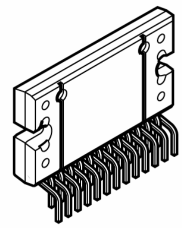
Maximum Power 43 W BTL × 4-ch Audio Power IC

The TB2904HQ is 4-ch BTL audio amplifier for car audio applications.

This IC can generate higher power: P_{OUT} MAX = 43 W as it includes the pure complementary P-ch and N-ch DMOS output stage.

It is designed to yield low distortion ratio for 4-ch BTL audio power amplifier, built-in standby function, muting function, and various kinds of protectors.

Additionally, Off-set detector is built in.



HZIP25-P-1.00F

Weight: 7.7 g (typ.)

Features

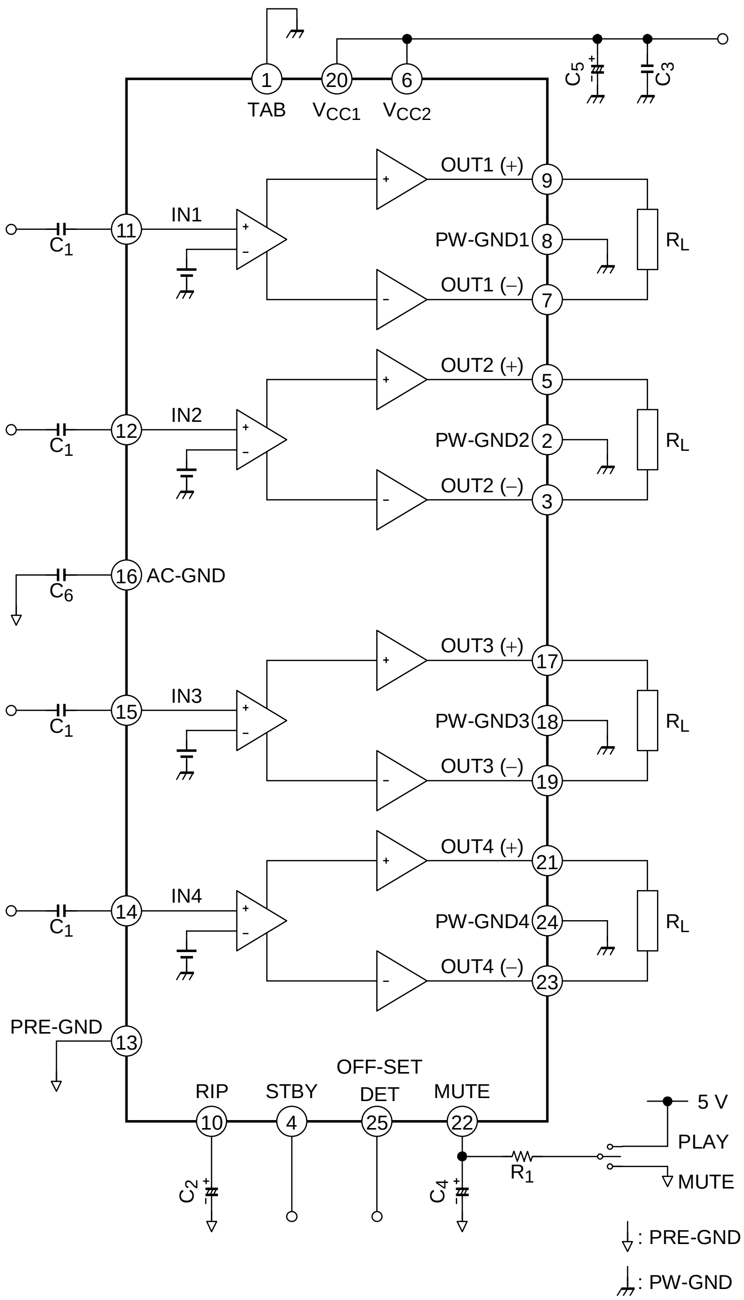
- High power output
  : P_{OUT} MAX (1) = 43 W (typ.)
  P (V_{CC} = 14.4 V, f = 1 kHz, JEITA max, R_L = 4 Ω)
  : P_{OUT} MAX (2) = 39 W (typ.)
  (V_{CC} = 13.7 V, f = 1 kHz, JEITA max, R_L = 4 Ω)
  : P_{OUT} (1) = 26 W (typ.)
  (V_{CC} = 14.4 V, f = 1 kHz, THD = 10%, R_L = 4 Ω)
  : P_{OUT} (2) = 23 W (typ.)
  (V_{CC} = 13.2 V, f = 1 kHz, THD = 10%, R_L = 4 Ω)
- Low distortion ratio: THD = 0.015% (typ.)
  (V_{CC} = 13.2 V, f = 1 kHz, P_{OUT} = 5 W, R_L = 4 Ω)
- Low noise: V_{NO} = 90 μVrms (typ.)
  (V_{CC} = 13.2 V, Rg = 0 Ω, BW = 20 Hz to 20 kHz, R_L = 4 Ω)
- Built-in standby switch function (pin 4)
- Built-in muting function (pin 22)
- Built-in Off-set detection function (pin 25)
- Built-in various protection circuits:
  Thermal shut down, overvoltage, out to GND, out to V_{CC}, out to out short, speaker burned
- Operating supply voltage: V_{CC} (opr) = 9 to 18 V (R_L = 4 Ω)

Note 1: Since this device’s pins have a low withstanding voltage, please handle it with care.

Note 2: Install the product correctly. Otherwise, it may result in break down, damage and/or degradation to the product or equipment.

Note 3: These protection functions are intended to avoid some output short circuits or other abnormal conditions temporarily. These protect functions do not warrant to prevent the IC from being damaged.
In case of the product would be operated with exceeded guaranteed operating ranges, these protection features may not operate and some output short circuits may result in the IC being damaged.

Block Diagram



Note4: Some of the functional blocks, circuits, or constants in the block diagram may be omitted or simplified for explanatory purpose.

Caution and Application Method (Description is made only on the single channel)

1. Voltage Gain Adjustment

This IC has no NF (negative feedback) Pins. Therefore, the voltage gain can not be adjusted, but it makes the device a space and total costs saver.

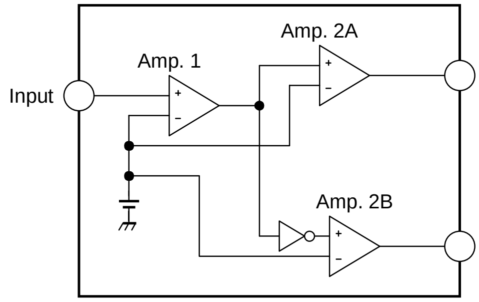


Figure 1 Block Diagram

The voltage gain of amp.1 : $G_{V1} = 0dB$
The voltage gain of amp.2A, B : $G_{V2} = 20dB$
The voltage gain of BTL connection : $G_{V\,(BTL)} = 6dB$

Therefore, the total voltage gain is decided by expression below.

$G_V = G_{V1} + G_{V2} + G_{V\,(BTL)} = 0 + 20 + 6 = 26dB$

2. Standby SW Function (pin 4)

By means of controlling pin 4 (standby pin) to High and Low, the power supply can be set to ON and OFF. The threshold voltage of pin 4 is set at about 3 V_{BE} (typ.), and the power supply current is about 2 μA (typ.) in the standby state.

Control Voltage of Pin 4: V_{SB}

Stand-by	Power	V_{SB} (V)
ON	OFF	0 to 1.5
OFF	ON	3.5 to 6 V

When changing the time constant of pin 4, check the pop noise.

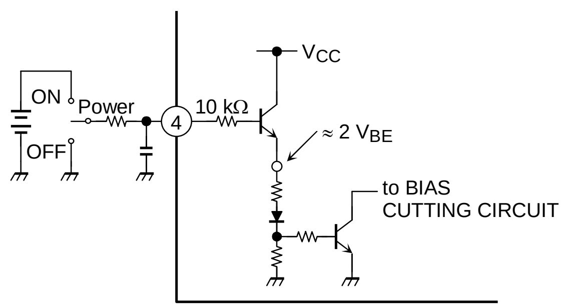


Figure 2 With pin 4 set to High, Power is turned ON

Advantage of Standby SW

(1) Since V_{CC} can directly be controlled to ON or OFF by the microcomputer, the switching relay can be omitted.

(2) Since the control current is microscopic, the switching relay of small current capacity is satisfactory for switching.

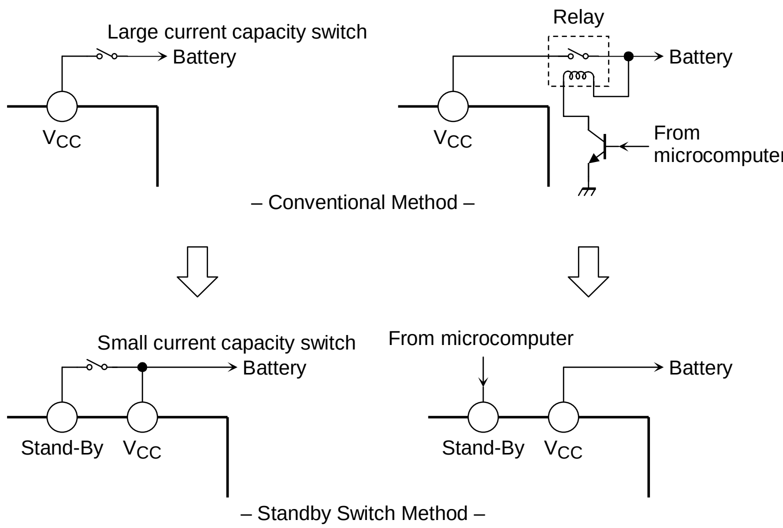


Figure 3

3. Muting Function (pin 22)

Audio muting function is enabled when pin 22 is Low. When the time constant of the muting function is determined by R_1 and C_4, it should take into account the pop noise. The pop noise, which is generated when the power or muting function is turned ON/OFF, will vary according to the time constant. (Refer to Figure 4 and Figure 5.)

The pin 22 is designed to operate off 5 V so that the outside pull-up resistor R_1 is determined on the basic of this value:

ex) When control voltage is changed in to 6 V from 5 V.

6 V/5 V × 47 k = 56 k

Additionally, as the V_{CC} is rapidly falling, the IC internal low voltage muting operates to eliminate the large pop noise basically.

The low voltage muting circuit pull 200 μA current into the IC so that the effect of the internal low voltage muting does not become enough if the R_1 is too small value.

To obtain enough operation of the internal low voltage muting, a series resistor, R_1 at pin 22 should be 47 kΩ or more.

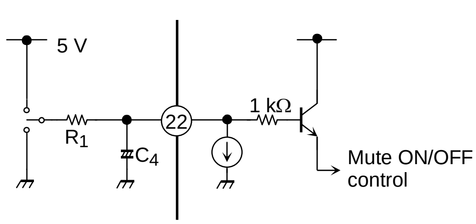


Figure 4 Muting Function

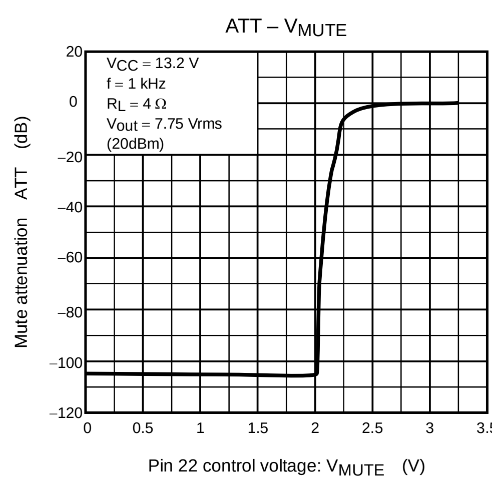


Figure 5 Mute Attenuation – V_{MUTE} (V)

4. Off-set detection function

In case of Appearing output offset voltage by Generating a Large Leakage Current on the input Capacitor etc.

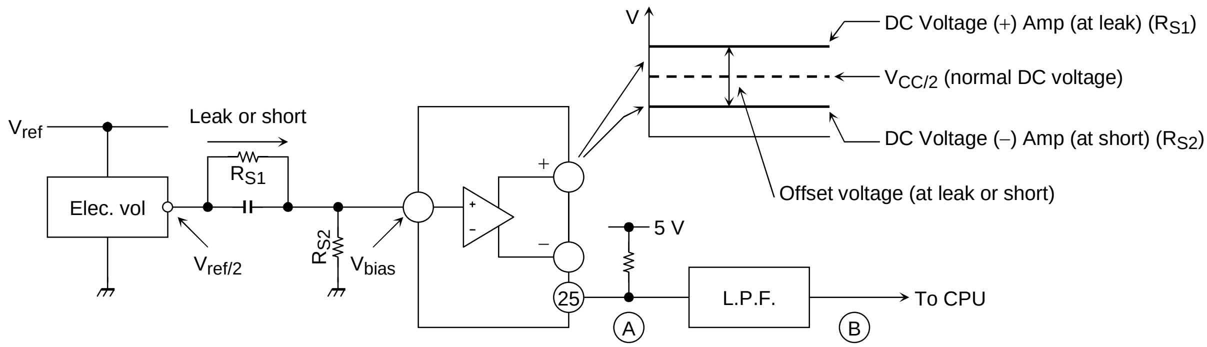


Figure 6 Application and Detection Mechanism

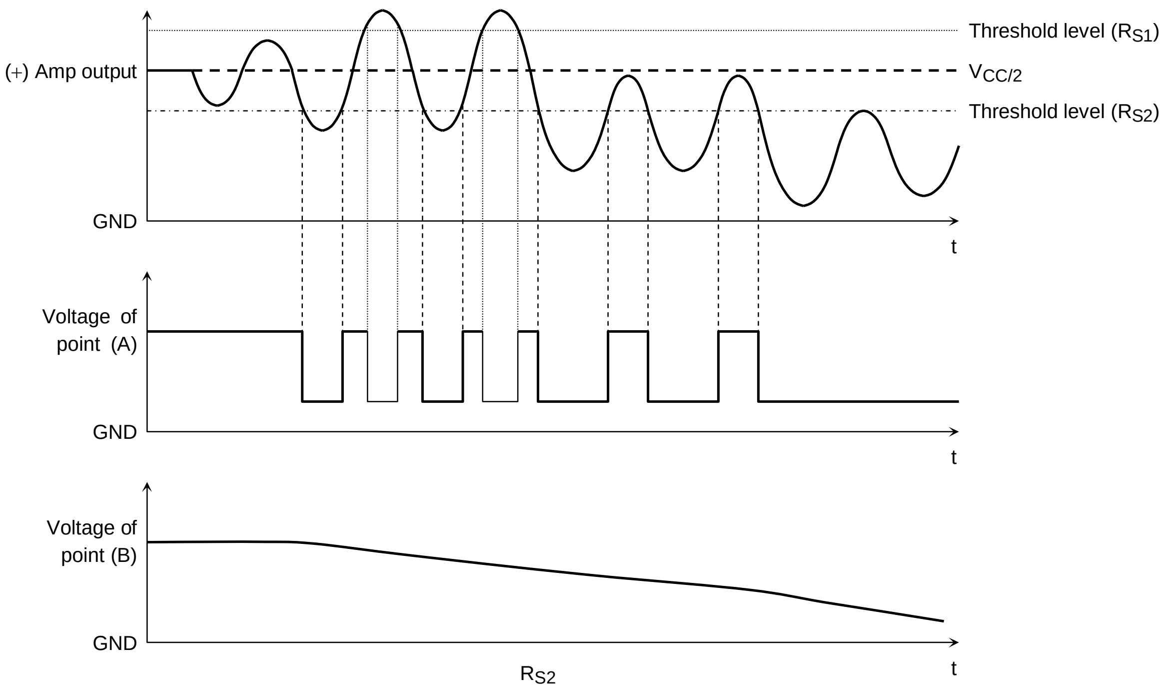


Figure 7 Wave Form

5. Prevention of speaker burning accident (in case of rare short circuit of speaker)

When the direct current resistance between OUT+ and OUT– terminal becomes 1 Ω or less and output current over 4 A flows, this IC makes a protection circuit operate and suppresses the current into a speaker. This system makes the burning accident of the speaker prevent as below mechanism.

<The guess mechanism of a burning accident of the speaker>

Abnormal output offset voltage (voltage between OUT+ and OUT–) over 4 V is made by the external circuit failure.(Note 5)

↓

The speaker imepedance becomes 1 Ω or less as it is in a rare short circuit condition.

↓

The current more than 4 A flows into the speaker and the speaker is burned.

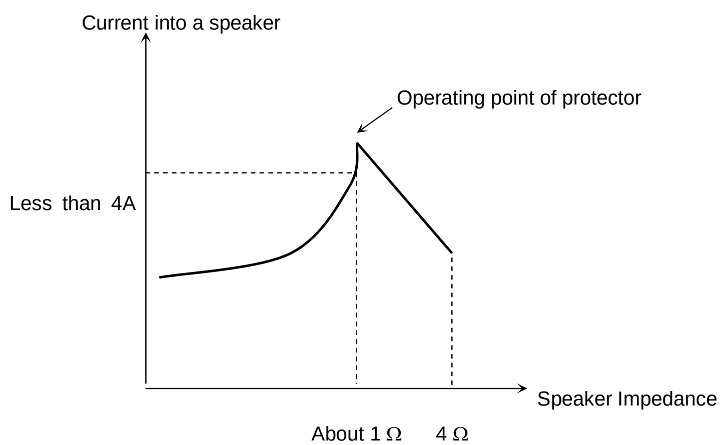


Figure 8

Note 5: It is appeared by biased input DC voltage
(For example, large leakage of the input capacitor, short-circuit between copper patterns of PCB.)

6. Pop Noise Suppression

Since the AC-GND pin (pin 16) is used as the NF pin for all amps, the ratio between the input capacitance (C1) and the AC-to-GND capacitance (C6) should be 1:4.

Also, if the power is turned OFF before the C1 and C6 batteries have been completely charged, pop noise will be generated because of the DC input unbalance.

To counteract the noise, it is recommended that a longer charging time be used for C2 as well as for C1 and C6. Note that the time which audio output takes to start will be longer, since the C2 makes the muting time (the time from when the power is turned ON to when audio output starts) is fix.

The pop noise which is generated when the muting function is turned ON/OFF will vary according to the time constant of C4.

The greater the capacitance, the lower the pop noise. Note that the time from when the mute control signal is applied to C4 to when the muting function is turned ON/OFF will be longer.

7. External Component Constants

Component Name	Recommended Value	Purpose	Effect		Notes
			Lower than recommended value	Higher than recommended value	
C1	0.22 μF	To eliminate DC	Cut-off frequency is increased	Cut-off frequency is reduced	Pop noise is generated when V_{CC} is ON
C2	10 μF	To reduce ripple	Powering ON/OFF is faster	Powering ON/OFF takes longer	
C3	0.1 μF	To provide sufficient oscillation margin	Reduces noise and provides sufficient oscillation margin		
C4	1 μF	To reduce pop noise	High pop noise. Duration until muting function is turned ON/OFF is short	Low pop noise. Duration until muting function is turned ON/OFF is long	
C5	3900 μF	Ripple filter	Power supply ripple filtering		
C6	1 μF	NF for all outputs	Pop noise is suppressed when C1:C6 = 1:4		Pop noise is generated when V_{CC} is ON

Note6: If recommended value is not used.

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Peak supply voltage (0.2 s)	$V_{CC\ (surge)}$	50	V
DC supply voltage	$V_{CC\ (DC)}$	28	V
Operation supply voltage	$V_{CC\ (opr)}$	18	V
Output current (peak)	$I_{O\ (peak)}$	9	A
Power dissipation	P_D (Note 7)	125	W
Operation temperature	T_{opr}	−40 to 85	°C
Storage temperature	T_{stg}	−55 to 150	°C

Note 7: Package thermal resistance $\theta_{j\text{-}T}$ = 1°C/W (typ.) (Ta = 25°C, with infinite heat sink)

The absolute maximum ratings of a semiconductor device are a set of specified parameter values, which must not be exceeded during operation, even for an instant. If any of these rating would be exceeded during operation, the device electrical characteristics may be irreparably altered and the reliability and lifetime of the device can no longer be guaranteed. Moreover, these operations with exceeded ratings may cause break down, damage and/or degradation to any other equipment. Applications using the device should be designed such that each absolute maximum rating will never be exceeded in any operating conditions. Before using, creating and/or producing designs, refer to and comply with the precautions and conditions set forth in this documents.

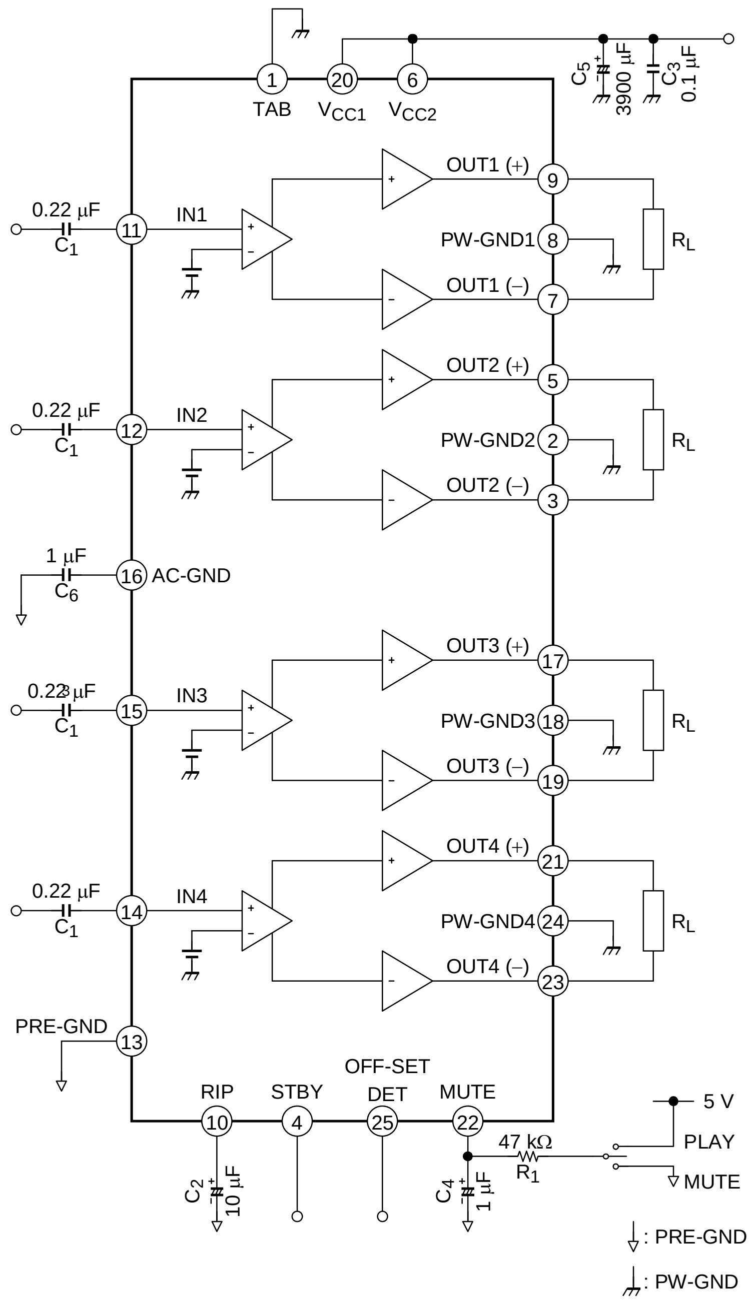
Electrical Characteristics (unless otherwise specified, V_{CC} = 13.2 V, f = 1 kHz, R_L = 4 Ω, Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Quiescent current	I_{CCQ}	—	V_{IN} = 0	—	170	340	mA
Output power	P_{OUT} MAX (1)	—	V_{CC} = 14.4 V, max POWER	—	43	—	W
	P_{OUT} MAX (2)	—	V_{CC} = 13.7 V, max POWER	—	39	—	
	P_{OUT} (1)	—	V_{CC} = 14.4 V, THD = 10%	—	26	—	
	P_{OUT} (2)	—	THD = 10%	21	23	—	
Total harmonic distortion	THD	—	P_{OUT} = 5 W	—	0.015	0.15	%
Voltage gain	G_V	—	V_{OUT} = 0.775 Vrms	24	26	28	dB
Voltage gain ratio	ΔG_V	—	V_{OUT} = 0.775 Vrms	−1.0	0	1.0	dB
Output noise voltage	V_{NO} (1)	—	Rg = 0 Ω, DIN45405	—	100	—	μVrms
	V_{NO} (2)	—	Rg = 0 Ω, BW = 20 Hz~20 kHz	—	90	200	
Ripple rejection ratio	R.R.	—	f_{rip} = 100 Hz, R_g = 620 Ω V_{rip} = 0.775 Vrms	50	60	—	dB
Cross talk	C.T.	—	R_g = 620 Ω V_{OUT} = 0.775 Vrms	—	70	—	dB
Output offset voltage	V_{OFFSET}	—	—	−150	0	150	mV
Input resistance	R_{IN}	—	—	—	90	—	kΩ
Standby current	I_{SB}	—	Standby condition	—	2	10	μA
Standby control voltage	V_{SB} H	—	POWER: ON	3.5	—	6.0	V
	V_{SB} L	—	POWER: OFF	0	—	1.5	
Mute control voltage	V_M H	—	MUTE: OFF	3.0	—	6.0	V
	V_M L	—	MUTE: ON, R_1 = 47 kΩ	0	—	0.5	
Mute attenuation	ATT M	—	MUTE: ON V_{OUT} = 7.75 Vrms→Mute: OFF	85	100	—	dB

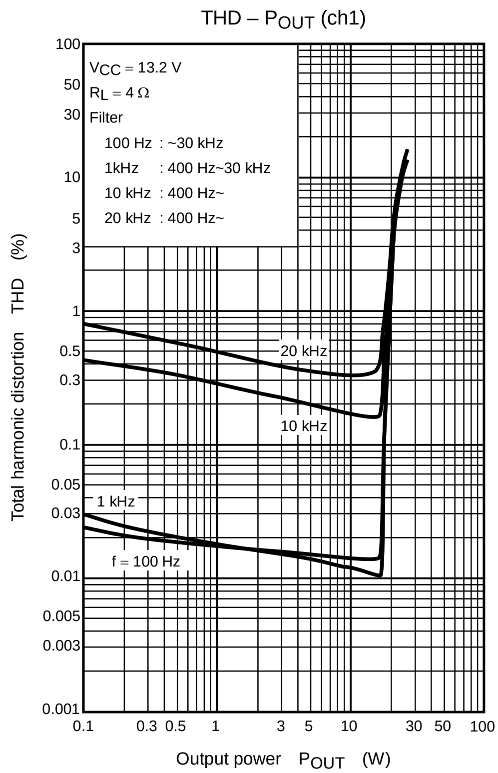
Offset detection

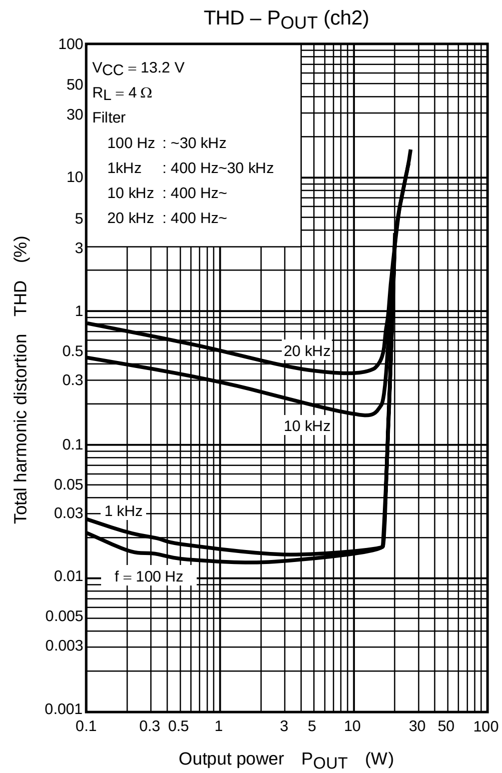
Detection threshold voltage	Voff-set	—	Rpull-up = 47 kΩ, +V = 5.0V Based on output DC voltage	±1.0	±1.5	±2.0	V

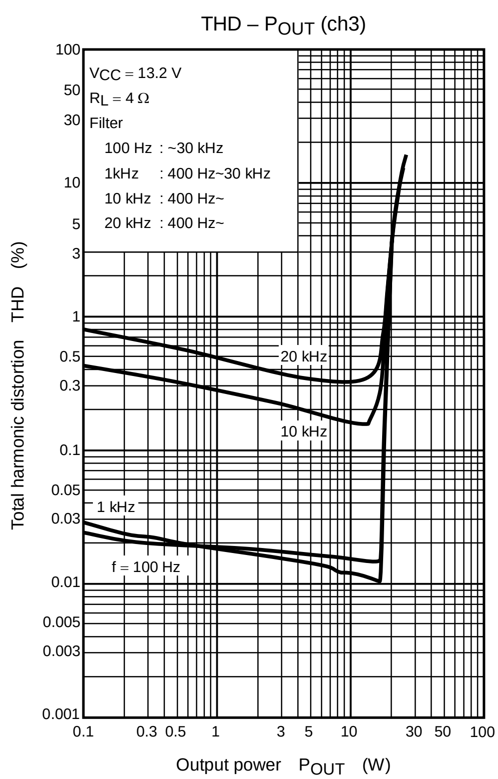
Test Circuit

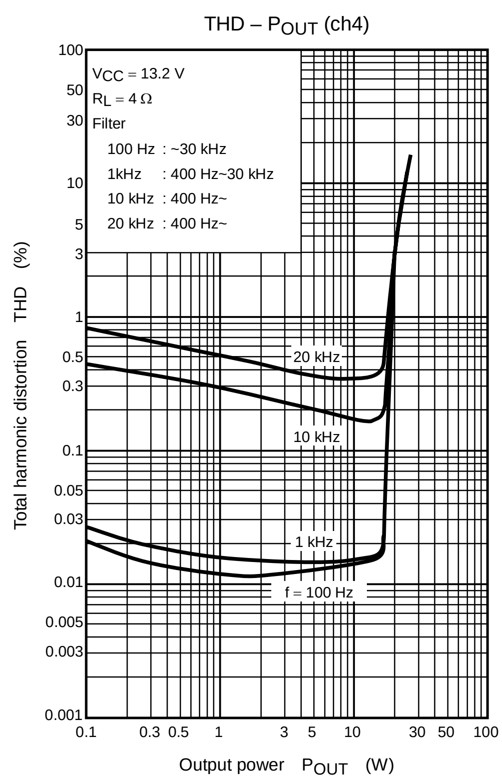


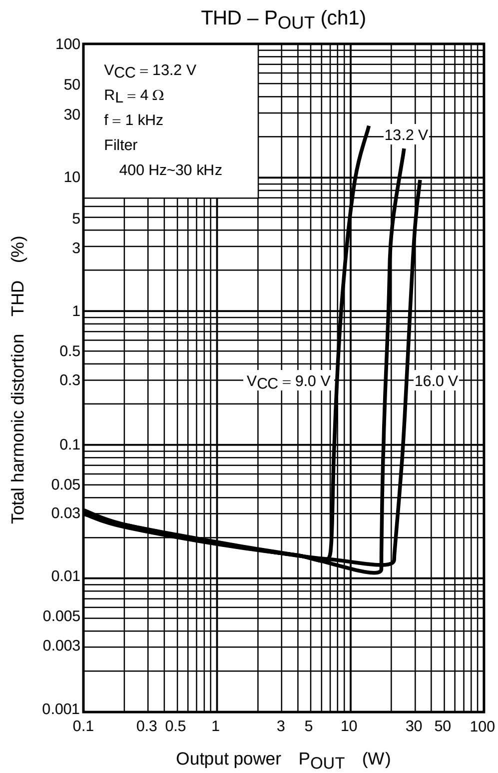
Components in the test circuits are only used to obtain and confirm the device characteristics.
These components and circuits do not warrant to prevent the application equipment from malfunction or failure.

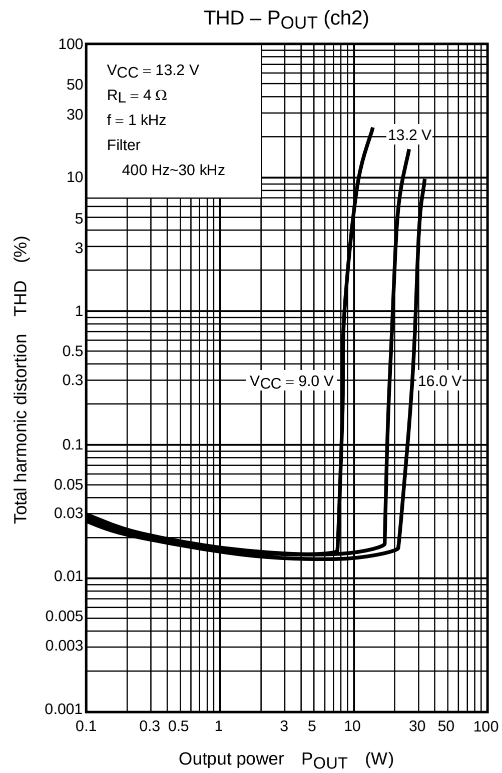

THD – POUT (ch1)
VCC = 13.2 V
RL = 4 Ω
Filter
100 Hz : ~30 kHz
1kHz : 400 Hz~30 kHz
10 kHz : 400 Hz~
20 kHz : 400 Hz~
20 kHz
10 kHz
1 kHz
f = 100 Hz
Total harmonic distortion THD (%)
Output power POUT (W)

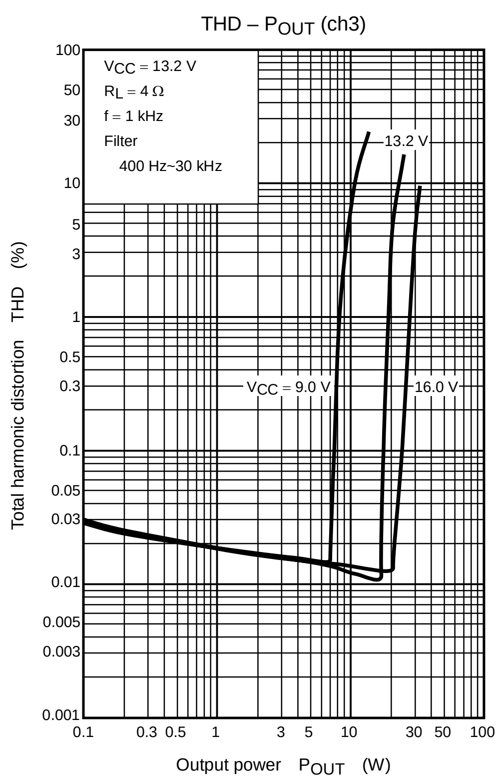

THD – POUT (ch2)
VCC = 13.2 V
RL = 4 Ω
Filter
100 Hz : ~30 kHz
1kHz : 400 Hz~30 kHz
10 kHz : 400 Hz~
20 kHz : 400 Hz~
20 kHz
10 kHz
1 kHz
f = 100 Hz
Total harmonic distortion THD (%)
Output power POUT (W)

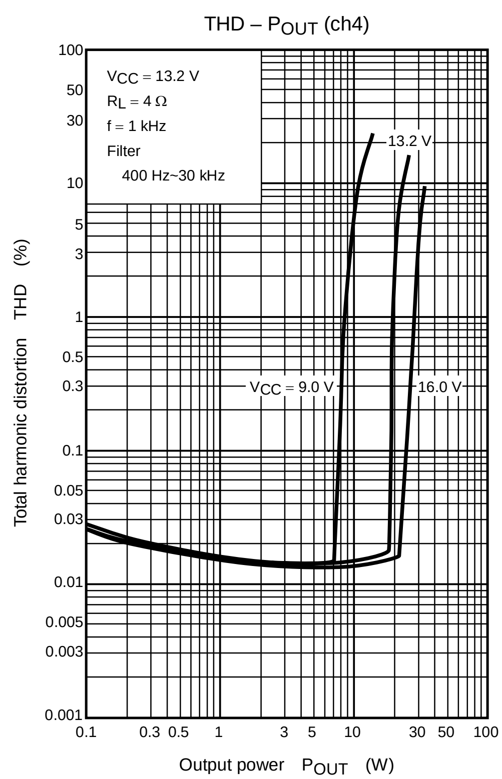

THD – POUT (ch3)
VCC = 13.2 V
RL = 4 Ω
Filter
100 Hz : ~30 kHz
1kHz : 400 Hz~30 kHz
10 kHz : 400 Hz~
20 kHz : 400 Hz~
20 kHz
10 kHz
1 kHz
f = 100 Hz
Total harmonic distortion THD (%)
Output power POUT (W)

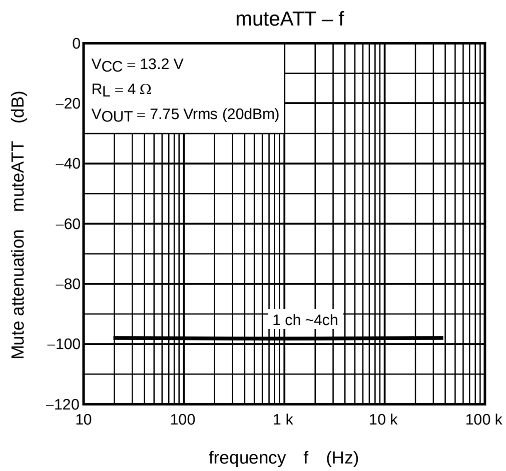

THD – POUT (ch4)
VCC = 13.2 V
RL = 4 Ω
Filter
100 Hz : ~30 kHz
1kHz : 400 Hz~30 kHz
10 kHz : 400 Hz~
20 kHz : 400 Hz~
20 kHz
10 kHz
1 kHz
f = 100 Hz
Total harmonic distortion THD (%)
Output power POUT (W)

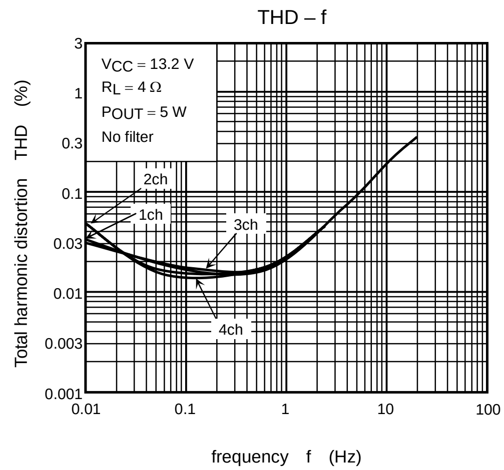

THD – POUT (ch1)
VCC = 13.2 V
RL = 4 Ω
f = 1 kHz
Filter
400 Hz~30 kHz
13.2 V
VCC = 9.0 V
16.0 V
Total harmonic distortion THD (%)
Output power POUT (W)

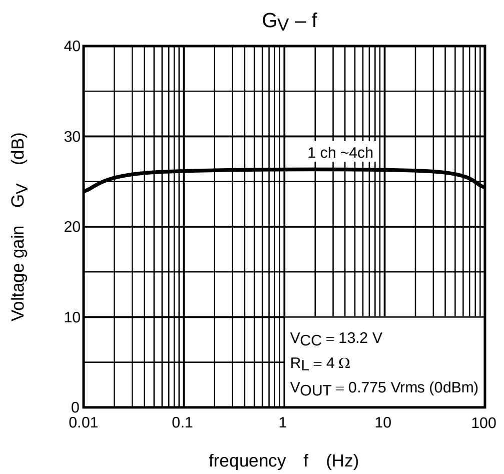

THD – POUT (ch2)
VCC = 13.2 V
RL = 4 Ω
f = 1 kHz
Filter
400 Hz~30 kHz
13.2 V
VCC = 9.0 V
16.0 V
Total harmonic distortion THD (%)
Output power POUT (W)

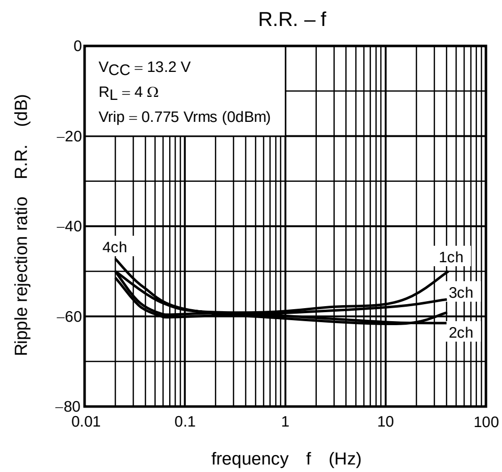

THD – POUT (ch3)
VCC = 13.2 V
RL = 4 Ω
f = 1 kHz
Filter
400 Hz~30 kHz
13.2 V
VCC = 9.0 V
16.0 V
Total harmonic distortion THD (%)
Output power POUT (W)

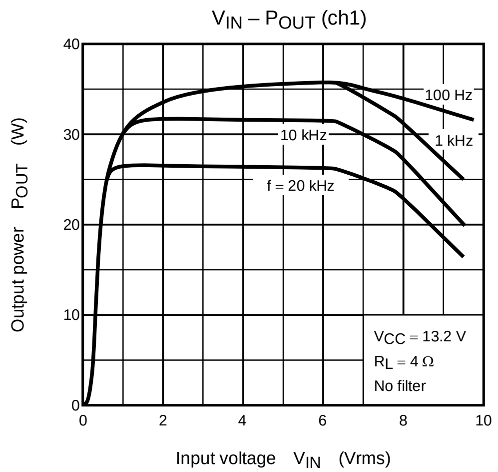

THD – POUT (ch4)
VCC = 13.2 V
RL = 4 Ω
f = 1 kHz
Filter
400 Hz~30 kHz
13.2 V
VCC = 9.0 V
16.0 V
Total harmonic distortion THD (%)
Output power POUT (W)


muteATT – f
VCC = 13.2 V
RL = 4 Ω
VOUT = 7.75 Vrms (20dBm)
1 ch ~4ch
Mute attenuation muteATT (dB)
frequency f (Hz)
0
−20
−40
−60
−80
−100
−120
10
100
1 k
10 k
100 k

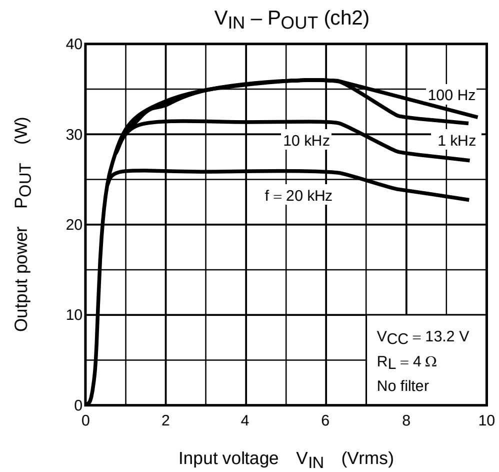

THD – f
VCC = 13.2 V
RL = 4 Ω
POUT = 5 W
No filter
2ch
1ch
3ch
4ch
Total harmonic distortion THD (%)
frequency f (Hz)
3
1
0.3
0.1
0.03
0.01
0.003
0.001
0.01
0.1
1
10
100


GV – f
1 ch ~4ch
VCC = 13.2 V
RL = 4 Ω
VOUT = 0.775 Vrms (0dBm)
Voltage gain GV (dB)
frequency f (Hz)
40
30
20
10
0
0.01
0.1
1
10
100

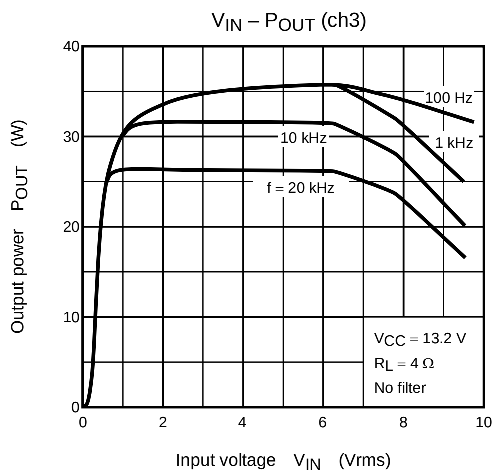

R.R. – f
VCC = 13.2 V
RL = 4 Ω
Vrip = 0.775 Vrms (0dBm)
4ch
1ch
3ch
2ch
Ripple rejection ratio R.R. (dB)
frequency f (Hz)
0
−20
−40
−60
−80
0.01
0.1
1
10
100



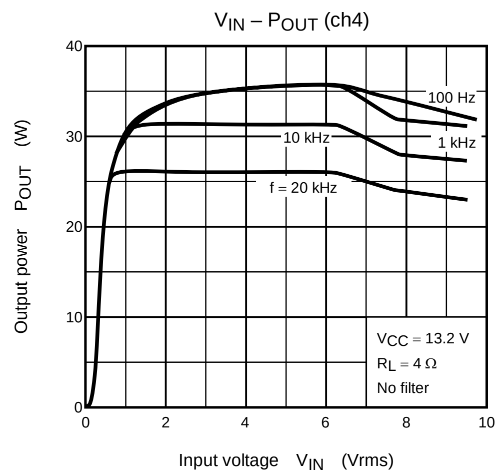
VIN – POUT (ch1)
Output power POUT (W)
Input voltage VIN (Vrms)
100 Hz
1 kHz
10 kHz
f = 20 kHz
VCC = 13.2 V
RL = 4 Ω
No filter



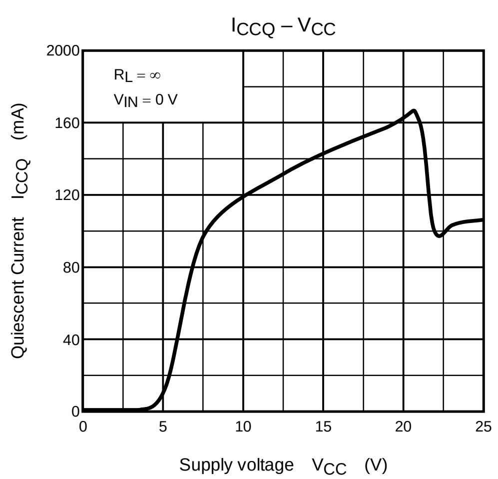
VIN – POUT (ch2)
Output power POUT (W)
Input voltage VIN (Vrms)
100 Hz
1 kHz
10 kHz
f = 20 kHz
VCC = 13.2 V
RL = 4 Ω
No filter



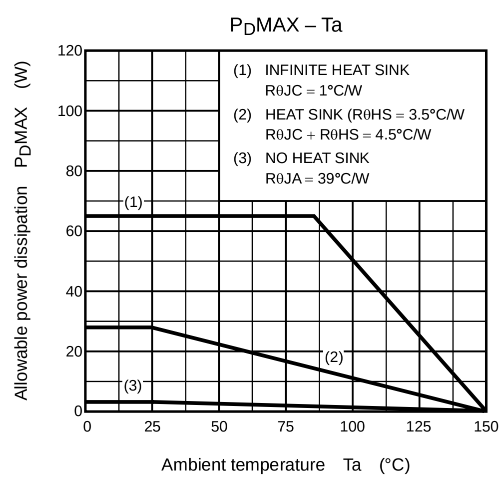
VIN – POUT (ch3)
Output power POUT (W)
Input voltage VIN (Vrms)
100 Hz
1 kHz
10 kHz
f = 20 kHz
VCC = 13.2 V
RL = 4 Ω
No filter



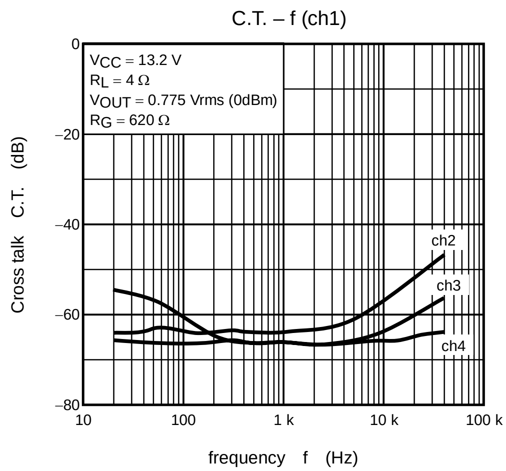
VIN – POUT (ch4)
Output power POUT (W)
Input voltage VIN (Vrms)
100 Hz
1 kHz
10 kHz
f = 20 kHz
VCC = 13.2 V
RL = 4 Ω
No filter

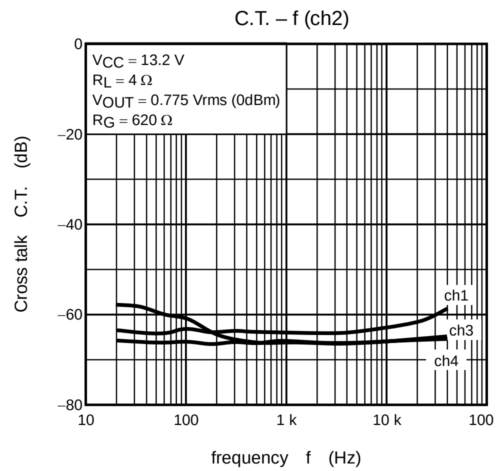


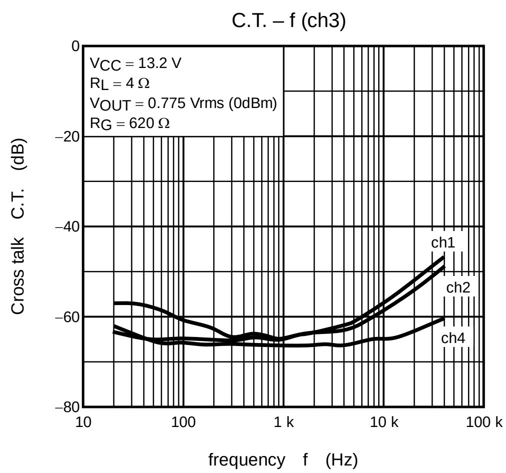
ICCQ – VCC
Quiescent Current ICCQ (mA)
Supply voltage VCC (V)
RL = ∞
VIN = 0 V

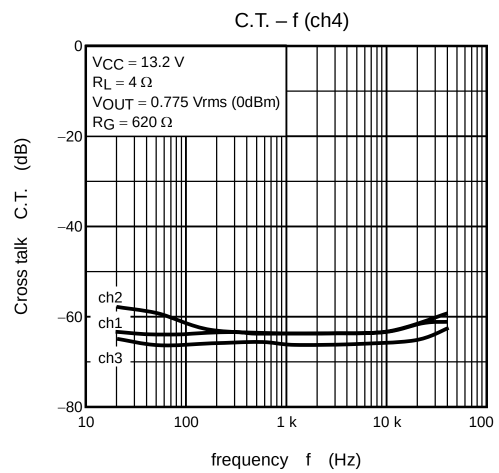


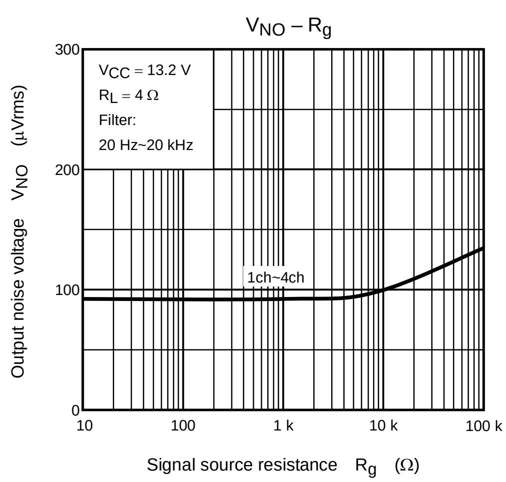
PDMAX – Ta
Allowable power dissipation PDMAX (W)
Ambient temperature Ta (°C)
(1) INFINITE HEAT SINK
RθJC = 1°C/W
(2) HEAT SINK (RθHS = 3.5°C/W
RθJC + RθHS = 4.5°C/W
(3) NO HEAT SINK
RθJA = 39°C/W

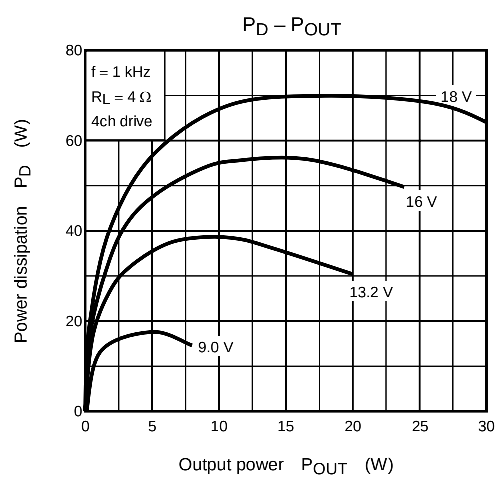

C.T. – f (ch1)
VCC = 13.2 V
RL = 4 Ω
VOUT = 0.775 Vrms (0dBm)
RG = 620 Ω
Cross talk C.T. (dB)
frequency f (Hz)
ch2
ch3
ch4


C.T. – f (ch2)
VCC = 13.2 V
RL = 4 Ω
VOUT = 0.775 Vrms (0dBm)
RG = 620 Ω
Cross talk C.T. (dB)
frequency f (Hz)
ch1
ch3
ch4


C.T. – f (ch3)
VCC = 13.2 V
RL = 4 Ω
VOUT = 0.775 Vrms (0dBm)
RG = 620 Ω
Cross talk C.T. (dB)
frequency f (Hz)
ch1
ch2
ch4

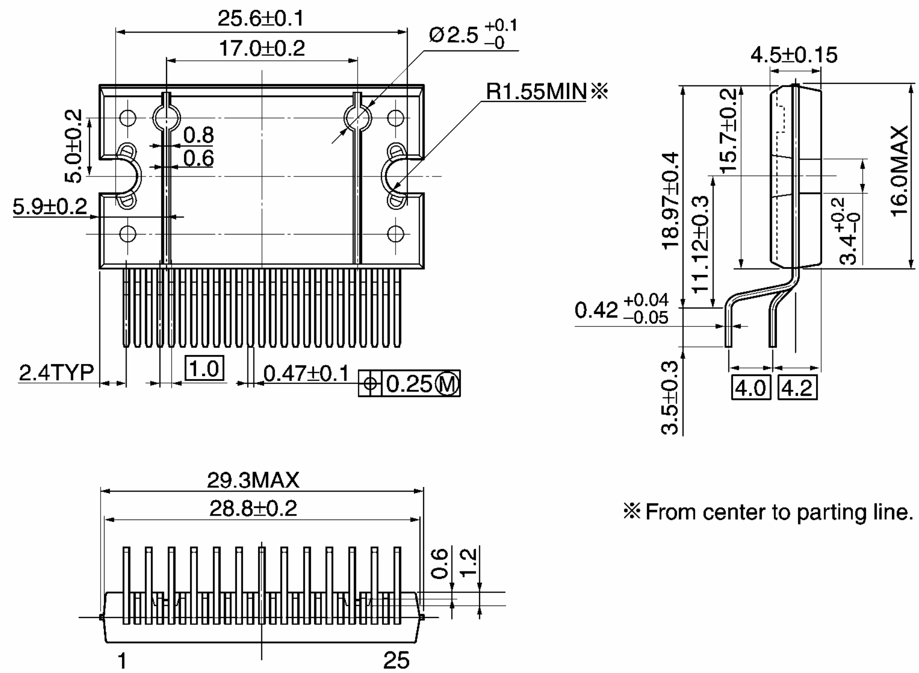

C.T. – f (ch4)
VCC = 13.2 V
RL = 4 Ω
VOUT = 0.775 Vrms (0dBm)
RG = 620 Ω
Cross talk C.T. (dB)
frequency f (Hz)
ch2
ch1
ch3


VNO – Rg
VCC = 13.2 V
RL = 4 Ω
Filter:
20 Hz~20 kHz
Output noise voltage VNO (μVrms)
Signal source resistance Rg (Ω)
1ch~4ch


PD – POUT
f = 1 kHz
RL = 4 Ω
4ch drive
Power dissipation PD (W)
Output power POUT (W)
18 V
16 V
13.2 V
9.0 V

Package Dimensions

HZIP25-P-1.00F Unit: mm



Weight: 7.7 g (typ.)

- Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.

- If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. For details on how to connect a protection circuit such as a current limiting resistor or back electromotive force adsorption diode, refer to individual IC datasheets or the IC databook. IC breakdown may cause injury, smoke or ignition.

- Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.

- Carefully select external components (such as inputs and negative feedback capacitors) and load components (such as speakers), for example, power amp and regulator. If there is a large amount of leakage current such as input or negative feedback condenser, the IC output DC voltage will increase. If this output voltage is connected to a speaker with low input withstand voltage, overcurrent or IC failure can cause smoke or ignition. (The over current can cause smoke or ignition from the IC itself.) In particular, please pay attention when using a Bridge Tied Load (BTL) connection type IC that inputs output DC voltage to a speaker directly.

- Over current Protection Circuit
  Over current protection circuits (referred to as current limiter circuits) do not necessarily protect ICs under all circumstances. If the Over current protection circuits operate against the over current, clear the over current status immediately. Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the over current protection circuit to not operate properly or IC breakdown before operation. In addition, depending on the method of use and usage conditions, if over current continues to flow for a long time after operation, the IC may generate heat resulting in breakdown.

- Thermal Shutdown Circuit
  Thermal shutdown circuits do not necessarily protect ICs under all circumstances. If the Thermal shutdown circuits operate against the over temperature, clear the heat generation status immediately. Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the thermal shutdown circuit to not operate properly or IC breakdown before operation.

- Heat Radiation Design
  When using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature (Tj) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into considerate the effect of IC heat radiation with peripheral components.

- Installation to Heat Sink
  Please install the power IC to the heat sink not to apply excessive mechanical stress to the IC. Excessive mechanical stress can lead to package cracks, resulting in a reduction in reliability or breakdown of internal IC chip. In addition, depending on the IC, the use of silicon rubber may be prohibited. Check whether the use of silicon rubber is prohibited for the IC you intend to use, or not. For details of power IC heat radiation design and heat sink installation, refer to individual technical datasheets or IC databooks.

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060116EBF

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- The products described in this document are subject to the foreign exchange and foreign trade laws. 021023_E
- This product generates heat during normal operation. However, substandard performance or malfunction may cause the product and its peripherals to reach abnormally high temperatures.
  The product is often the final stage (the external output stage) of a circuit. Substandard performance or malfunction of the destination device to which the circuit supplies output may cause damage to the circuit or to the product. 030619_R

About solderability, following conditions were confirmed

- Solderability

(1) Use of Sn-37Pb solder Bath
- solder bath temperature = 230°C
- dipping time = 5 seconds
- the number of times = once
- use of R-type flux

(2) Use of Sn-3.0Ag-0.5Cu solder Bath
- solder bath temperature = 245°C
- dipping time = 5 seconds
- the number of times = once
- use of R-type flux