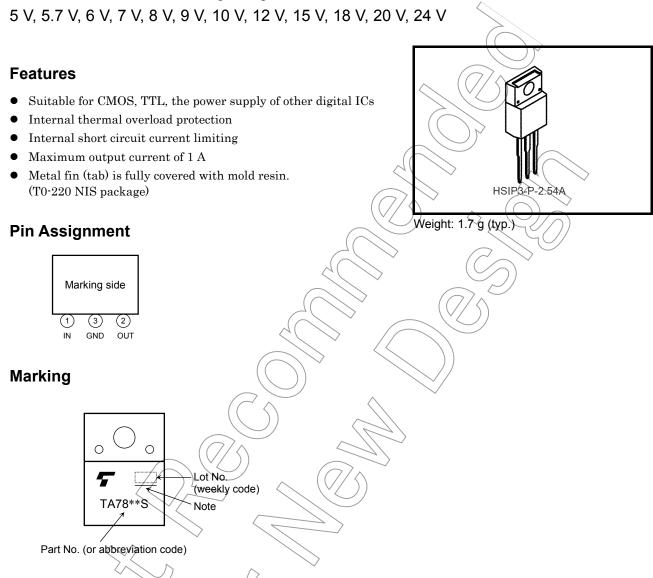
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA7805S, TA78057S, TA7806S, TA7807S, TA7808S, TA7809S, TA7810S, TA7812S, TA7815S, TA7818S, TA7820S, TA7824S

Three Terminal Positive Voltage Regulators



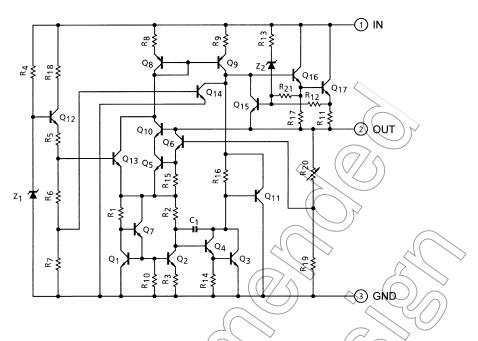
Note: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

Characteris	tics	Symbol	Rating	Unit
	TA7805S	<		
	TA78057S			
	TA7806S			
	TA7807S			`
	TA7808S	(())	35	
Input voltage	TA7809S	VIN	1	
input voitage	TA7810S	// SVIN		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
	TA7812S		(7/4)	
<	TA7815S			
	TA7818S			
^ ^	TA7820S		40	
	TA7824S	~	\rightarrow	
Power dissipation	(Ta = 25°C)	PD	2	W
Tower dissipation	(Tc = 25°C)	D	20	VV
Operating temperature	$\langle \rangle$	Topt	−30 to 85	°C
Storage temperature		T _{Stg}	−55 to 150	°C
Junction temperature		Tj	150	°C
Thermal resistance		R _{th (j-c)}	6.25	°C/W
The marres stance		R _{th (j-a)}	62.5	0, **

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



TA7805S Electrical Characteristics (Unless otherwise specified, V_{IN} = 10 V, I_{OUT} = 500 mA, 0° C \leq T_{j} \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C,	I _{OUT} = 100 mA	4.8	5.0	5.2	V
Line regulation	Reg·line	1	T _i = 25°C	7.0 V ≤ V _{IN} ≤ 25 V	4	3	100	mV
Line regulation	Regillie	'	1 _j = 25 C	8.0 V ≤ V _{IN} ≤ 12 V	(-)	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	50	IIIV
Load regulation	Reg·load	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 1.4 A		15	100	mV
Load regulation	rteg load	'	1] - 23 0	250 mA ≤ I _{OUT} ≤ 750 mA		5	50	IIIV
Output voltage	V _{OUT}	1	T _j = 25°C	7.0 V \leq V _{IN} \leq 20 V 5.0 mA \leq I _{OUT} \leq 1.0 A	4.75		5.25	V
Quiescent current	I _B	1	T _j = 25°C,	I _{OUT} = 5 mA	_	4.2	8.0	mA
Quiescent current change	Δl _B	1	7.0 V ≤ V _{II} I _{OUT} = 5 n	N ≤ 25 V, nA, T _j = 25°C	_	£	1.3	mA
Output noise voltage	V _{NO}	2	Ta = 25°C I _{OUT} = 50	, 10 Hz ≤ f ≤ 100 kHz mA	-6	50	> -	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz I _{OUT} = 50	, 8.0 V ≤ V _{IN} ≤ 18 V mA, T ₁ = 25°C	62	78) _	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1,0	$A, T_j = 25^{\circ}C$	7	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C	7	()	1.6	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	loυτ = 5 n	nA ()/) –	-0.6	_	mV/°C

TA78057S Electrical Characteristics (Unless otherwise specified, V_{IN} = 10.7 V, J_{OUT} = 500 mA, 0°C ≤ T_j ≤ 125°C)

Characteristics	Symbol	Test Circuit	Test	t Condition	Min	Тур.	Max	Unit
Output voltage	(YOUT \wedge	1	T _j = 25°C, TOUT	= 100 mA	5.47	5.7	5.93	V
Line regulation	Reg-line	1	$T_j = 25^{\circ}C$	/ ≤ V _{IN} ≤ 25 V	_	4	110	mV
Line regulation	/ Treg line		1) - 25 0 8.7 V	/ ≤ V _{IN} ≤ 12.7 V	_	2	55	1110
Load regulation	Reg·load 〈	1	T ₁ = 25°C 5 mA	A ≤ I _{OUT} ≤ 1.4 A	_	15	110	mV
Load regulation	Treg load		250 r	mA ≤ I _{OUT} ≤ 750 mA	_	5	55	1110
Output voltage	Vout	1	$T_{j} = 25^{\circ}C$ $\begin{bmatrix} 7.7 \text{ V} \\ 5.0 \text{ m} \end{bmatrix}$	/ ≤ V _{IN} ≤ 20.7 V nA ≤ I _{OUT} ≤ 1.0 A	5.42	_	5.98	V
Quiescent current	I _B	1	T _j = 25°C, I _{OUT}	= 5 mA		4.3	8.0	mA
Quiescent current change	Δlβ	1	$7.7 \text{ V} \le \text{V}_{\text{IN}} \le 25$ $\text{I}_{\text{OUT}} = 5 \text{ mA}, \text{T}_{\text{j}}$	5 V, = 25°C		1	1.3	mA
Output noise voltage	VNO	2	Ta = 25°C, 10 H I _{OUT} = 50 mA	lz ≤ f ≤ 100 kHz		55	_	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz, 8.8 \ I _{OUT} = 50 mA, T	$V \le V_{IN} \le 18.8 \text{ V},$ $V_j = 25^{\circ}\text{C}$	62	77	_	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1.0 A, T _j	= 25°C		2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C		_	1.5	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 mA		_	-0.7	_	mV/°C



TA7806S Electrical Characteristics (Unless otherwise specified, V_{IN} = 11 V, I_{OUT} = 500 mA, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C,	I _{OUT} = 100 mA	5.75	6.0	6.25	V
Line regulation	Reg·line	1	T _i = 25°C	8.0 V ≤ V _{IN} ≤ 25 V	7	4	120	mV
Line regulation	Negriille	'	1 _j = 25 C	9 V ≤ V _{IN} ≤ 13 V	(-	2	60	IIIV
Load regulation	Reg load	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 1.4 A		15	120	mV
Load regulation	rteg load	'	1] - 23 0	250 mA ≤ I _{OUT} ≤ 750 mA		5	60	IIIV
Output voltage	V _{OUT}	1	T _j = 25°C	$8 \text{ V} \le \text{V}_{\text{IN}} \le 21 \text{ V}$ 5.0 mA \le I _{OUT} \le 1.0 A	5.7		6.3	V
Quiescent current	Ι _Β	1	T _j = 25°C,	I _{OUT} = 5 mA	_	4.3	8.0	mA
Quiescent current change	ΔI _B	1	8.0 V ≤ V _{II} I _{OUT} = 5 n	$N \le 25 \text{ V},$ nA, $T_j = 25^{\circ}\text{C}$	_	£	1.3	mA
Output noise voltage	V _{NO}	2	Ta = 25°C I _{OUT} = 50	, 10 Hz ≤ f ≤ 100 kHz mA	-	55	> -	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz I _{OUT} = 50	, 9 V ≤ V _{IN} ≤ 19 V mA, T ₁ = 25°C	61	77) _	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1,0	$A, T_j = 25^{\circ}C$	7	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C	7	/)	1.5	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	IOUT = 5 n	nA ()//) –	-0.7	_	mV/°C

TA7807S Electrical Characteristics (Unless otherwise specified, V_{IN} = 12 V, I_{OUT} = 500 mA, 0°C ≤ T_j ≤ 125°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Output voltage	(VOUT	1	$T_j = 25^{\circ}\text{C}, 100\text{T} = 100 \text{ mA}$	6.72	7.0	7.28	V
Line regulation	Reg-line	1	$T_{j} = 25^{\circ}C$ 9 V \leq V _{IN} \leq 25 V	_	5	140	mV
Line regulation	/ Keg inte		10 V ≤ V _{IN} ≤ 14 V	_	2	70	IIIV
Load regulation	Reg·load 〈	1	T ₁ = 25°C 5 mA ≤ I _{OUT} ≤ 1.4 A	_	15	140	mV
Load regulation	Treg load		250 mA ≤ I _{OUT} ≤ 750 mA	. —	5	70	1110
Output voltage	Vout	1	$T_{j} = 25^{\circ}C$ $9 \text{ V} \le \text{V}_{IN} \le 22 \text{ V}$ 5.0 mA $\le \text{I}_{OUT} \le 1.0 \text{ A}$	6.65	_	7.35	V
Quiescent current	I _B	1	$T_j = 25$ °C, $I_{OUT} = 5$ mA		4.3	8.0	mA
Quiescent current change	Δlβ	1	$9 \text{ V} \le \text{V}_{\text{IN}} \le 25 \text{ V},$ $\text{I}_{\text{OUT}} = 5 \text{ mA}, \text{T}_{j} = 25^{\circ}\text{C}$	_	_	1.3	mA
Output noise voltage	VNO	2	Ta = 25°C, 10 Hz ≤ f ≤ 100 kHz I _{OUT} = 50 mA	_	60	_	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz, 10 V \leq V _{IN} \leq 20 V I _{OUT} = 50 mA, T _j = 25°C	59	75	_	dB
Dropout voltage	V _D	1	I _{OUT} = 1.0 A, T _j = 25°C	_	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C	_	1.3	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 mA	_	-0.8	_	mV/°C

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TA7808S Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 500 mA, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C,	I _{OUT} = 100 mA	7.7	8.0	8.3	V
Line regulation	Reg·line	1	T _i = 25°C	10.5 V ≤ V _{IN} ≤ 25 V	4	6	160	. mV
Line regulation	Regillie	'	1 _j = 25 C	11 V ≤ V _{IN} ≤ 17 V	(-)	2	80	IIIV
Load regulation	Reg·load	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 1.4 A		12	160	. mV
Load regulation	rteg load	'	1] - 23 0	250 mA ≤ I _{OUT} ≤ 750 mA		4	80	1110
Output voltage	V _{OUT}	1	T _j = 25°C	10.5 V ≤ V _{IN} ≤ 23 V 5.0 mA ≤ I _{OUT} ≤ 1.0 A	7.6		8.4	V
Quiescent current	I _B	1	T _j = 25°C,	I _{OUT} = 5 mA	_	4.3	8.0	mA
Quiescent current change	Δl _B	1	10.5 V ≤ V I _{OUT} = 5 n	V _{IN} ≤ 25 V, nA, T _j = 25°C	_	£	1.0	mA
Output noise voltage	V _{NO}	2	Ta = 25°C I _{OUT} = 50	, 10 Hz ≤ f ≤ 100 kHz mA	-6	70	> -	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz I _{OUT} = 50	, 11.5 V ≤ V _{IN} ≤ 21.5 V mA, T ₁ = 25°C	_58	74) _	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1,0	A, T _j = 25°C	7	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C		/))	1.1	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	IOUT = 5 n	nA ()/) –	-1.0	1	mV/°C

TA7809S Electrical Characteristics (Unless otherwise specified, V_{IN} = 15 V₂ I_{OUT} = 500 mA, 0°C ≤ T_j ≤ 125°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Output voltage	(VOUT	1	T _j = 25°C, 1001 = 100 mA	8.64	9.0	9.36	V
Line regulation	Reg-line	1	$T_j = 25^{\circ}C$ 11.5 V $\leq V_{IN} \leq 26$ V	_	7	180	mV
Line regulation	/ Keg inte		13 V ≤ V _{IN} ≤ 19 V	_	2.5	90	IIIV
Load regulation	Reg·load 〈	1	T ₁ = 25°C 5 mA ≤ I _{OUT} ≤ 1.4 A	_	12	180	mV
Load regulation	Treg load		250 mA ≤ I _{OUT} ≤ 750 mA	_	4	90	IIIV
Output voltage	Vout	1	$T_{j} = 25^{\circ}C$ 11.5 V \leq V _{IN} \leq 24 V 5.0 mA \leq I _{OUT} \leq 1.0 A	8.55	_	9.45	V
Quiescent current	I _B	1	T _j = 25°C, I _{OUT} = 5 mA	_	4.3	8.0	mA
Quiescent current change	Δlβ	1	11.5 V \leq V _{IN} \leq 26 V, I _{OUT} = 5 mA, T _j = 25°C	_	_	1.0	mA
Output noise voltage	VNO	2	Ta = 25°C, 10 Hz ≤ f ≤ 100 kHz I _{OUT} = 50 mA	_	75	_	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz, 12.5 V \leq V _{IN} \leq 22.5 V I _{OUT} = 50 mA, T _j = 25°C	56	72	_	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1.0 A, T _j = 25°C	_	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C	_	1.0	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 mA	_	-1.1		mV/°C



TA7810S Electrical Characteristics (Unless otherwise specified, V_{IN} = 16 V, I_{OUT} = 500 mA, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C,	I _{OUT} = 100 mA	9.6	10.0	10.4	V
Line regulation	Reg·line	1	T _i = 25°C	12.5 V ≤ V _{IN} ≤ 27 V	1	8	200	mV
Line regulation	Regime	!	1 _j = 25 C	14 V ≤ V _{IN} ≤ 20 V	(2.5	100	IIIV
Load regulation	Reg·load	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 1.4 A	$\left. ight) angle _{r}$	12	200	mV
Load regulation	rteg load	'	1] - 23 0	250 mA ≤ I _{OUT} ≤ 750 mA	<u> </u>	4	100	IIIV
Output voltage	V _{OUT}	1	T _j = 25°C	12.5 V \leq V _{IN} \leq 25 V 5.0 mA \leq I _{OUT} \leq 1.0 A	9.5	_	10.5	>
Quiescent current	IB	1	T _j = 25°C,	I _{OUT} = 5 mA	_	4.3	8.0	mA
Quiescent current change	Δl _B	1	12.5 V ≤ V I _{OUT} = 5 n	V _{IN} ≤ 27 V, nA, T _j = 25°C	_	A	1.0	mA
Output noise voltage	V _{NO}	2	Ta = 25°C I _{OUT} = 50	, 10 Hz ≤ f ≤ 100 kHz mA	-6	80	> -	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz I _{OUT} = 50	, 13.5 V ≤ V _{IN} ≤ 23.5 V mA, T _I = 25°C	55	72) _	dB
Dropout voltage	V _D	1	I _{OUT} = 1,0	A, T _j = 25°C	7	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C		(\mathcal{A})	0.9	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	loυτ = 5 n	nA ()/) –	-1.3	_	mV/°C

TA7812S Electrical Characteristics (Unless otherwise specified, V_{IN} = 19 V, I_{OUT} = 500 mA, 0°C ≤ T_j ≤ 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	(YOUT \wedge	1	T _j = 25°C,	100 mA	11.5	12.0	12.5	V
Line regulation	Reg-line	1	T _j = 25°C/	14.5 V ≤ V _{IN} ≤ 30 V	_	10	240	mV
Line regulation	/ Keg inte		1] - 20 0	16 V ≤ V _{IN} ≤ 22 V	_	3	120	IIIV
Load regulation	Reg·load 《	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 1.4 A	_	12	240	mV
Load regulation	Treg load		1 20	250 mA ≤ I _{OUT} ≤ 750 mA	_	4	120	1110
Output voltage	Vout	1	T _j = 25°C	$14.5 \text{ V} \le \text{V}_{\text{IN}} \le 27 \text{ V}$ 5.0 mA \le I _{OUT} \le 1.0 A	11.4	_	12.6	V
Quiescent current	I _B	1	T _j = 25°C,	I _{OUT} = 5 mA	_	4.3	8.0	mA
Quiescent current change	Δlβ	1	14.5 V ≤ V I _{OUT} = 5 n	′ _{IN} ≤ 30 V, nA, T _j = 25°C	_	_	1.0	mA
Output noise voltage	VNO	2	Ta = 25°C I _{OUT} = 50	, 10 Hz ≤ f ≤ 100 kHz mA	_	90	_	μV _{rms}
Ripple rejection	R.R.	3		t, 15 V ≤ V _{IN} ≤ 25 V mA, T _j = 25°C	55	71	_	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1.0	A, T _j = 25°C	_	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C		_	0.7	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-1.6	_	mV/°C



TA7815S Electrical Characteristics (Unless otherwise specified, V_{IN} = 23 V, I_{OUT} = 500 mA, 0° C \leq $T_i \leq$ 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C,	I _{OUT} = 100 mA	14.4	15.0	15.6	V
Line regulation	Reg·line	1	T _i = 25°C	17.5 V ≤ V _{IN} ≤ 30 V	1	11	300	mV
Line regulation	Negriille	'	1 _j = 25 C	20 V ≤ V _{IN} ≤ 26 V	(3	150	IIIV
Load regulation	Reg load	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 1.4 A	$\left. ight) angle _{r}$	12	300	mV
Load regulation	rteg load	'	1] - 23 0	250 mA ≤ I _{OUT} ≤ 750 mA	<u> </u>	4	150	IIIV
Output voltage	V _{OUT}	1	T _j = 25°C	$17.5 \text{ V} \le \text{V}_{\text{IN}} \le 30 \text{ V}$ 5.0 mA \le I _{OUT} \le 1.0 A	14.25	-	15.75	V
Quiescent current	Ι _Β	1	T _j = 25°C,	I _{OUT} = 5 mA	_	4.4	8.0	mA
Quiescent current change	ΔI _B	1	17.5 V ≤ V I _{OUT} = 5 n	Y _{IN} ≤ 30 V, nA, T _j = 25°C	_	£	1.0	mA
Output noise voltage	V _{NO}	2	Ta = 25°C I _{OUT} = 50	, 10 Hz ≤ f ≤ 100 kHz mA	-6	110	> -	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz I _{OUT} = 50	, 18.5 V ≤ V _{IN} ≤ 28.5 V mA, T ₁ = 25°C	54	70) _	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1,0	A, T _j = 25°C	7	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C	7	()	0.5	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	loυτ = 5 n	nA ()//) –	-2.0	_	mV/°C

TA7818S Electrical Characteristics (Unless otherwise specified, V_{IN} = 27 V, I_{OUT} = 500 mA, 0°C ≤ T_j ≤ 125°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Output voltage	(VOUT	1	$T_j = 25^{\circ}\text{C}$, $T_j = 100 \text{ mA}$	17.3	18.0	18.7	V
Line regulation	Reg-line	1	$T_{j} = 25^{\circ} C$ 21 V \leq V _{IN} \leq 33 V	_	13	360	mV
Line regulation	/ Treg line		24 V ≤ V _{IN} ≤ 30 V	_	4	180	IIIV
Load regulation	Reg·load 〈	1	T ₁ = 25°C 5 mA ≤ I _{OUT} ≤ 1.4 A	_	12	360	mV
Load regulation	Treg load		250 mA ≤ I _{OUT} ≤ 750 mA	. —	4	180	IIIV
Output voltage	Vout	1	$T_j = 25^{\circ}\text{C}$ 21 V \leq V _{IN} \leq 33 V 5.0 mA \leq I _{OUT} \leq 1.0 A	17.1	_	18.9	V
Quiescent current	I _B	1	$T_j = 25$ °C, $I_{OUT} = 5$ mA		4.5	8.0	mA
Quiescent current change	Δlβ	1	$21 \text{ V} \le \text{V}_{\text{IN}} \le 33 \text{ V},$ $\text{I}_{\text{OUT}} = 5 \text{ mA}, \text{T}_{j} = 25^{\circ}\text{C}$	_	_	1.0	mA
Output noise voltage	VNO	2	Ta = 25°C, 10 Hz ≤ f ≤ 100 kHz I _{OUT} = 50 mA	_	125	_	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz, 22 V \leq V _{IN} \leq 32 V I _{OUT} = 50 mA, T _j = 25°C	52	68	_	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1.0 A, T _j = 25°C		2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C	_	0.4	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 mA	_	-2.5	_	mV/°C



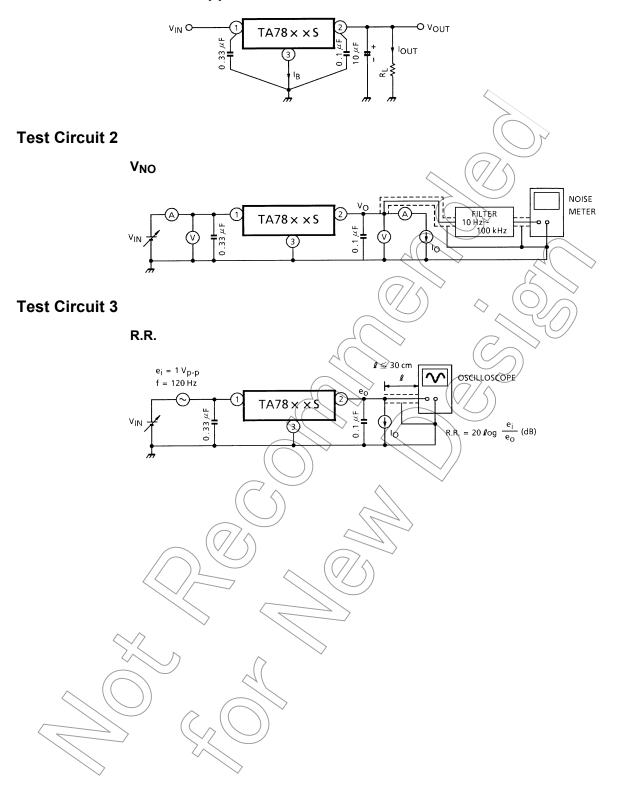
TA7820S Electrical Characteristics (Unless otherwise specified, V_{IN} = 29 V, I_{OUT} = 500 mA, 0°C \leq T_j \leq 125°C)

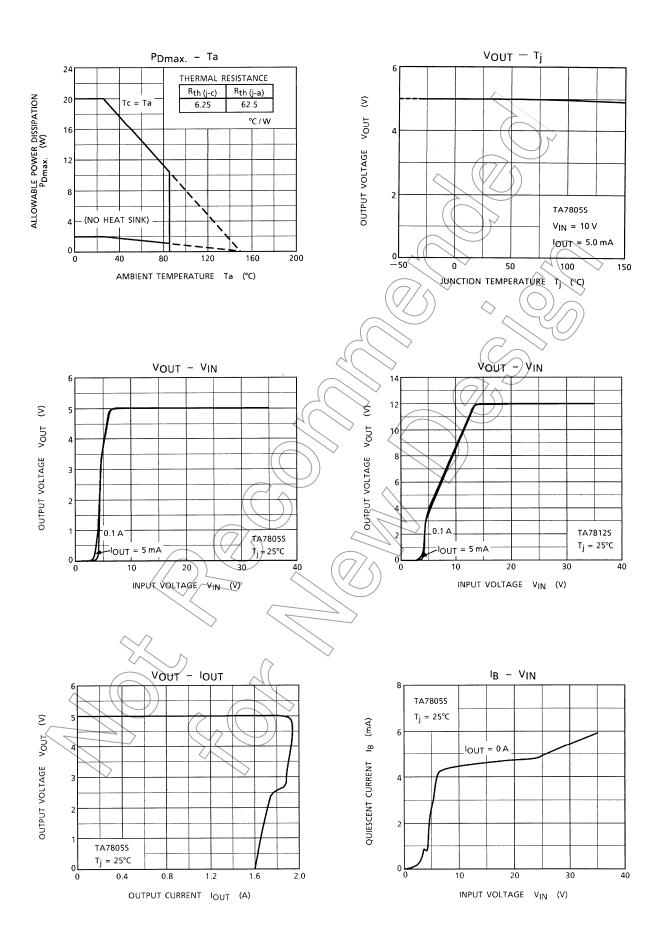
Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C,	I _{OUT} = 100 mA	19.2	20.0	20.8	V
Line regulation	Reg·line	1	T _i = 25°C	23 V ≤ V _{IN} ≤ 35 V	V	15	400	mV
Line regulation	Regille	'	1 _j = 25 C	26 V ≤ V _{IN} ≤ 32 V	(-)	5	200	IIIV
Load regulation	Reg·load	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 1.4 A		12	400	mV
Load regulation	rteg load	'	1] - 23 0	250 mA ≤ I _{OUT} ≤ 750 mA		4	200	IIIV
Output voltage	V _{OUT}	1	T _j = 25°C	23 V \leq V _{IN} \leq 35 V 5.0 mA \leq I _{OUT} \leq 1.0 A	19.0	-	21.0	V
Quiescent current	I _B	1	T _j = 25°C,	I _{OUT} = 5 mA	_	4.6	8.0	mA
Quiescent current change	Δl _B	1	23 V ≤ V _{IN} I _{OUT} = 5 n	ı ≤ 35 V, nA, T _j = 25°C	_	£	1.0	mA
Output noise voltage	V _{NO}	2	Ta = 25°C I _{OUT} = 50	, 10 Hz ≤ f ≤ 100 kHz mA	-6	135	> -	μV _{rms}
Ripple rejection	R.R.	3	f = 120 Hz I _{OUT} = 50	, 24 V ≤ V _{IN} ≤ 34 V mA, T ₁ = 25°C	_50	66) _	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1,0	A, T _j = 25°C	7	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C		/)	0.4	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	loυτ = 5 n	nA ()/) –	-3.0	_	mV/°C

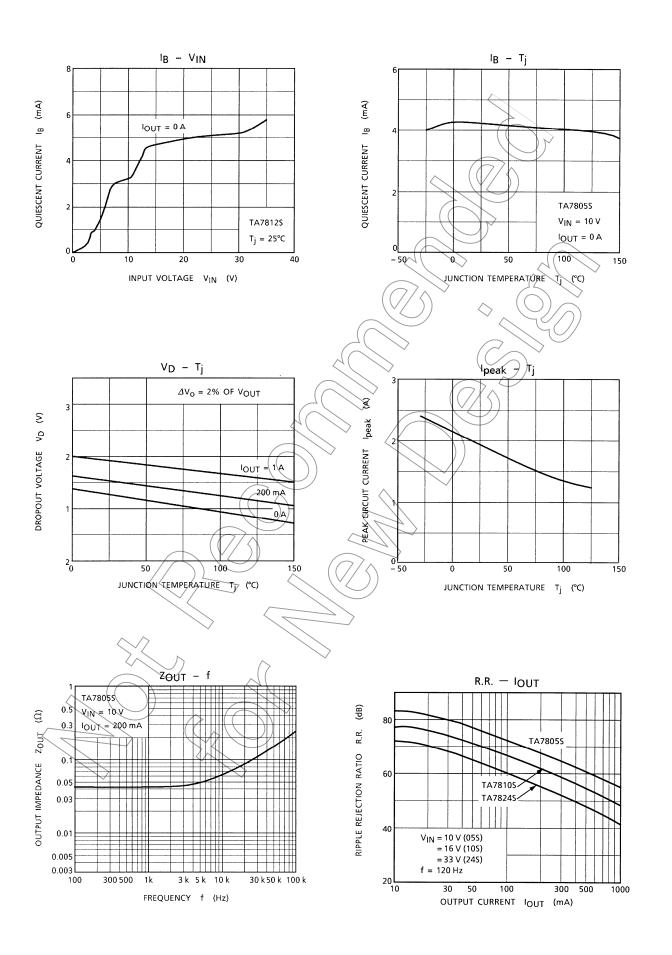
TA7824S Electrical Characteristics (Unless otherwise specified, V_{IN} = 33 V_{γ} I_{OUT} = 500 mA, 0°C \leq T_{j} \leq 125°C)

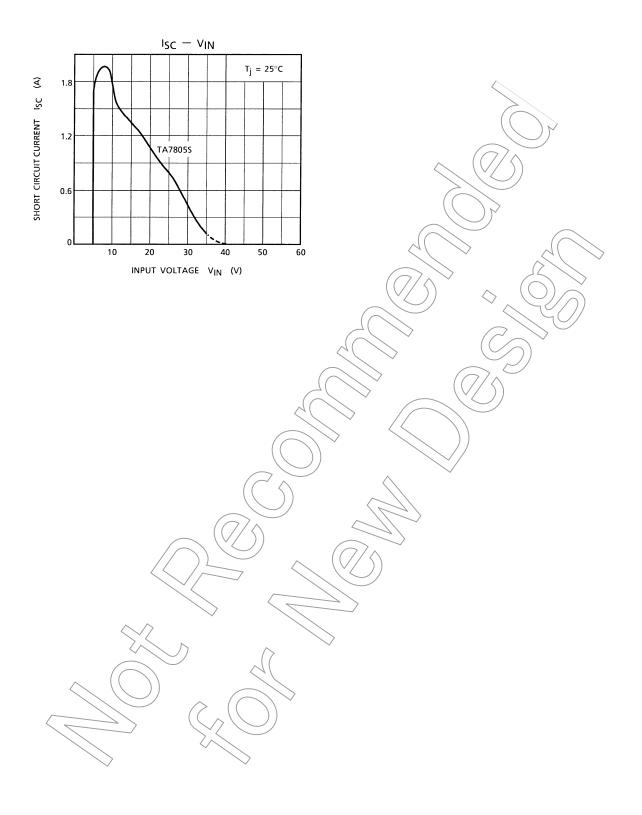
Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	(YOUT \wedge	1	T _j = 25°C,	100 mA	23.0	24.0	25.0	V
Line regulation	Reg-line	1	T _j = 25°C/	27 V ≤ V _{IN} ≤ 38 V	_	18	480	mV
Line regulation	Regriille		17 - 200	30 V ≤ V _{IN} ≤ 36 V	_	6	240	IIIV
Load regulation	Reg·load 〈	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 1.4 A	_	12	480	mV
Load regulation	Neg load (250 mA ≤ I _{OUT} ≤ 750 mA	_	4	240	IIIV
Output voltage	Vout	1	T _j = 25°C	27 V ≤ V _{IN} ≤ 38 V 5.0 mA ≤ I _{OUT} ≤ 1.0 A	22.8	_	25.2	V
Quiescent current	I _B	1	T _j = 25°C,	I _{OUT} = 5 mA	_	4.6	8.0	mA
Quiescent current change	Δlβ	1	27 V ≤ V _{IN} I _{OUT} = 5 n	ı ≤ 38 V, nA, T _j = 25°C	_	_	1.0	mA
Output noise voltage	VNO	2	Ta = 25°C I _{OUT} = 50	, 10 Hz ≤ f ≤ 100 kHz mA	_	150	_	μV _{rms}
Ripple rejection	R.R.	3	$f = 120 \text{ Hz}, 28 \text{ V} \le \text{V}_{\text{IN}} \le 38 \text{ V}$ $\text{I}_{\text{OUT}} = 50 \text{ mA}, \text{T}_{\text{j}} = 25^{\circ}\text{C}$		50	66	_	dB
Dropout voltage	V_{D}	1	I _{OUT} = 1.0 A, T _j = 25°C		_	2.0	_	V
Short circuit current limit	I _{SC}	1	T _j = 25°C		_	0.3	_	Α
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 mA		_	-3.5	_	mV/°C

Test Circuit 1/Standard Application Circuit









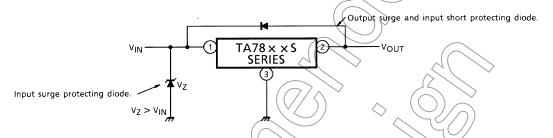


Precautions on Application

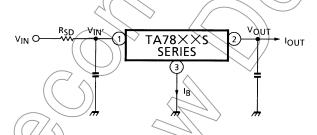
- (1) In regard to GND, be careful not to apply a negative voltage to the input/output terminal. Further, special care is necessary in the case of a voltage boost application.
- (2) If a surge voltage exceeding the maximum rating is applied to the input terminal or if a voltage in excess of the input terminal voltage is applied to the output terminal, the circuit may be destroyed. Particular care is necessary in the case of the latter.

Circuit destruction may also occur if the input terminal shorts to GND in a state of normal operation, causing the output terminal voltage to exceed the input voltage (GND potential) and the electrical charge of the chemical capacitor connected to the output terminal to flow into the input side.

Where these risks exist, take steps such as connecting zener and general silicon diodes to the circuit, as shown in the figure below.



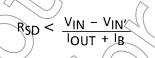
(3) When the input voltage is too high, the power dissipation of the three terminal regulator, which is a series regulator, increases, causing the junction temperature to rise. In such a case, it is recommended to reduce the power dissipation, and hence the junction temperature, by inserting a power-limiting resistor RSD in the input terminal.



The power dissipation Pp of the IC is expressed in the following equation.

Reducing V_{IN} below the lowest voltage necessary for the IC will cause ripple, deterioration in output regulation and, in certain circumstances, parasitic oscillation.

To determine the resistance value of RSD, design with a margin, referring to the following equation.



4) Be sure to connect a capacitor near the input terminal and output terminal between both terminals and GND. The capacitances should be determined experimentally because they depend on PCB patterns. In particular, adequate investigation should be made to ensure there is no problem even in high or low temperatures

(5) Installation of IC for power supply

To obtain high reliability on the heat sink design of a regulator IC, it is generally required to derate more than 20% of maximum junction temperature (T_j max).

Further, full consideration should be given to the installation of a heat sink in the IC.

(a) Heat sink design

The thermal resistance of the IC itself is required from the viewpoint of the design of elements, but the thermal resistance from the IC package to the open air varies with the contact thermal resistance

Table 1 shows how much the value of the contact thermal resistance $(\theta_c + \theta_s)$ is changed by insulating sheet (mica) and heat sink grease.

Table 1		$\langle \langle \langle \rangle \rangle \rangle$	Unit: °C/W
Package	Model No.	Torque Mica	$\theta_{c} + \theta_{s}$
TO-220NIS	TA78××S	0.6 N·m Nøt provided	0.4 to 0.6 (1.0 to 1.5)

The figures given in parentheses denote the values for when there is no grease.

The regulator IC package serves as GND, therefore of the value for when there is "no mica" should be used.

(b) Silicone grease

In the design of a circuit not exceeding the maximum rating, grease should be used if possible. If it is necessary to reduce the contact thermal resistance for the sake of circuit design, the following methods are recommended.

If using grease, use YG6260 (TOSHIBA SILICON CORPORATION).

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(c) Torque

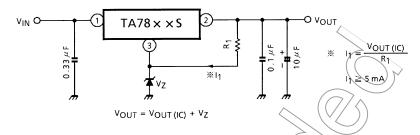
When installing the IC on a heat sink or the like, tighten the IC with a torque of less than the rated value. Tightening in excess of the rated value may cause internal elements of the IC to be adversely affected. Therefore, great care should be given to the installation procedure. Further, if polycarbonate screws are used, the torque causes a change with the passage of time, which may lessen the effect of radiation.



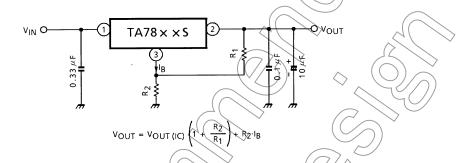
Application Circuits

(1) Voltage boost regulator

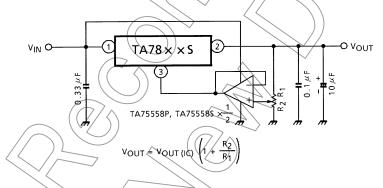
(a) Voltage boost by use of zener diode



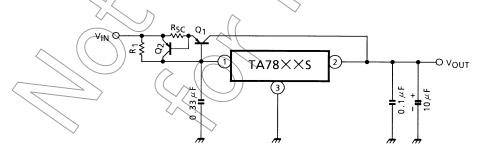
(b) Voltage boost by use of resistor



(c) Adjustable output regulator



(2) Current boost regulator



Heat sink is needed for Q_1 .

$$R_1 \le \frac{V_{BE1}}{I_{B MAX}}$$

where,

 V_{BE1} : V_{BE} of external transistor Q_1 . $I_{B\ MAX}$: Quiescent current of IC.

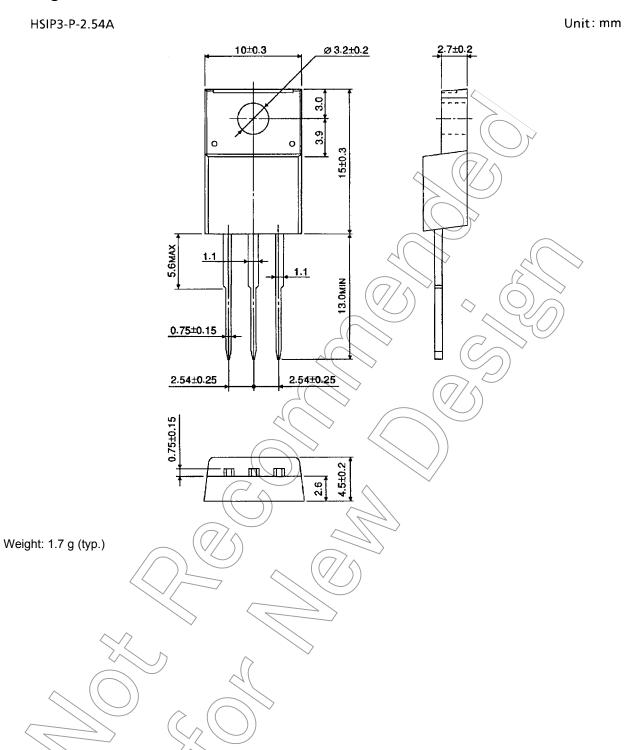
$$R_{SC} = \frac{V_{BE2}}{I_{SC}}$$

where,

 $\ensuremath{\mathsf{I}_{\mathsf{SC}}}$: Short-circuit current.



Package Dimensions





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