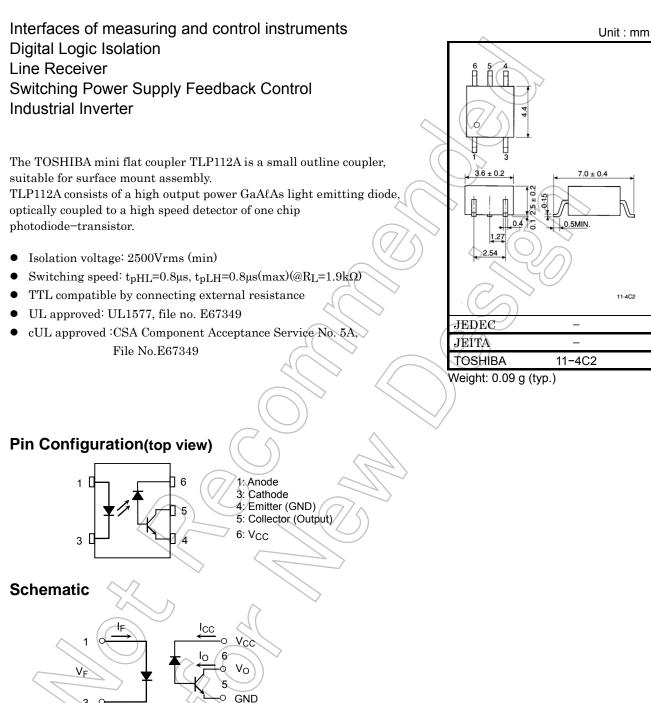
TOSHIBA Photocoupler GaA{As IRED & Photo-IC

TLP112A



Start of commercial products 1989-05

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
LED	Forward current		١ _F	20	mA
	Forward Current Derating (Ta ≥ 70 °C)		∆IF/°C	-0.36	mA/°C
	Pulse forward current	(Note 1)	I _{FP}	40	mA
	Peak transient forward current	(Note 2)	I _{FPT}	X	А
	Reverse voltage		V _R	(5)	> v
	Diode power dissipation	(Note 3)	PD	45	mW
	Output current		to (8	mA
	Peak output current		IOP	16	mA
Detector	Supply voltage		Vcc	-0.5 to 15	V
	Output voltage		Vo	-0.5 to 15	V
	Output power dissipation	4	Po	100	mW
	Output Power Dissipation Derating (Ta ≥ 70°C)		∆Po/°C	-1.8	mW/°C
Operating temperature range			Topr	-55 to 100	°C
Storage temperature range			T _{stg}	-55 to 125	$^{\circ}$
Lead soldering temperature(10 s)			T _{sol}	260	°C
Isola (AC	ation voltage , 60 s, R.H ≤ 60%)	(Note 4)	BVS	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- (Note 1) 50% duty cycle,1ms pulse width. Derate 1.6mA / °C above 70°C
- (Note 2) Pulse width $\leq 1 \mu s$, 300pps.
- (Note 3) Derate 0.9 mW / °C above 70°C
- (Note 4) This device is regarded as a two terminal device: pins 1 and 3 are shorted together, as are pins 4, 5 and 6.

Electrical Characteristics(Ta = 25°C)

Characteristic		Symbol	Test Condition	Min.	Тур.	Max.	Unit		
LED	Forward voltage	V _F	I _F =16mA	1.22	1.42	1.72	V		
	Forward voltage temperature coefficient	ΔV _F / ΔTa	I _F =16mA		-2	_	mV / °C		
	Reverse current	I _R	V _R =3V	$\langle \mathcal{F} \rangle$	—	10	μA		
	Capacitance between terminals	CT	V _F =0V, f=1MHz	E	30	_	pF		
Detector	High level output current	I _{OH(1)}	I _F =0mA, V _{CC} =V _O =5.5V		3	500	nA		
		I _{OH(2)}	I _F =0mA, V _{CC} =V _O =15V	$\left(\right)$	_	5			
		IOH	I _F =0mA, V _{CC} =V _O =15V Ta=70°C		_	50	μA		
	High level supply current	Іссн	I _F =0mA, V _{CC} =15V	—	0.01	1	μA		
	Current transfer ratio	I _O / I _F	I _F =16mA, V _{CC} =4.5V V _O =0.4V	20		\geq	%		
Coupled	Low level output voltage	V _{OL}	I _F =16mA, V _{CC} =4.5V I _O =2.4mA	-((0.4	V		
	Isolation resistance	R _S	R.H.≤ 60% V _S =500V DC (Note 1)	5×10 ¹⁰	10 ¹⁴	_	Ω		
	Stray capacitance between input to output	CS	V _S =0V, f=1MHz (Note 1)	D)	0.8	_	pF		

(Note 1) Device considered a two-terminal device: Pins 1 and 3 shorted together and pin 4, 5 and 6 shorted together.

Switching Characteristics(Ta = 25°C, V_{CC}=5V)

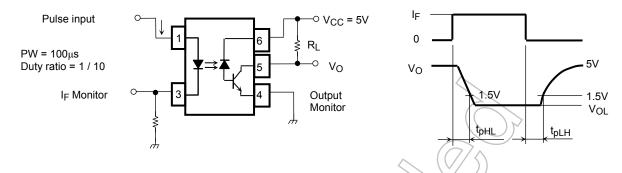
Characteristic	Symbol	Test Cir- cuit	Test Condition	Min.	Тур.	Max.	Unit	
Propagation delay time $(H \rightarrow L)$	tpHL	1	lϝ=0→16mA V _{CC} =5V, RL=1.9kΩ	_	_	0.8	μS	
Propagation delay time $(L \rightarrow H)$	tpLH	1	l _F =16⊖0mA V _{CC} =5V, R _L =1.9kΩ	_	_	0.8	μS	
Common mode transient imunity at high output level	CM _H (Note 1)	2	T _F =0mA, V _{CM} =200V _p -p R _L =4.1kΩ	_	1500	_	V / μs	
Common mode transient imunity at low output level	CM _L (Note 1)	2	I _F =16mA, V _{CM} =200V _{p−p} R _L =4.1kΩ		-1500	_	V / μs	

(Note) Maximum electrostatic discharge voltage for any pins: 100V(C=200pF, R=0)

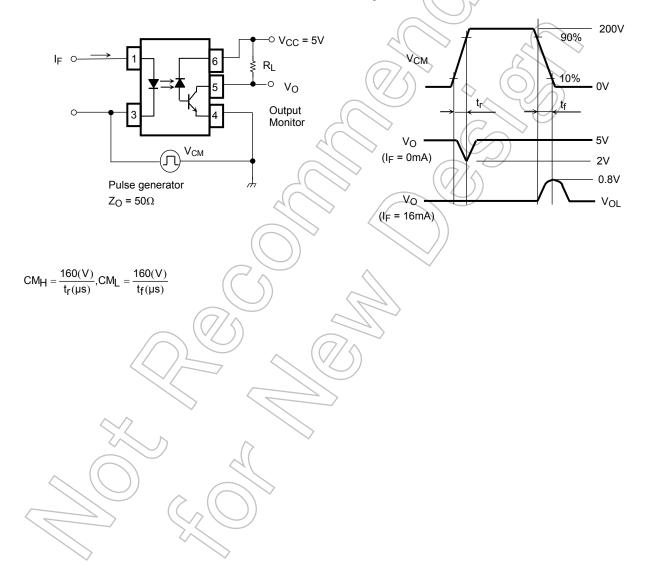
(Note 1) : CM_L is the maximum falling common mode voltage waveform (voltage/time) that can keep low level (V_O <0.8 V).

 CM_H is the maximum rising common mode voltage waveform (voltage/time) that can keep high level (V_O> 2.0 V).

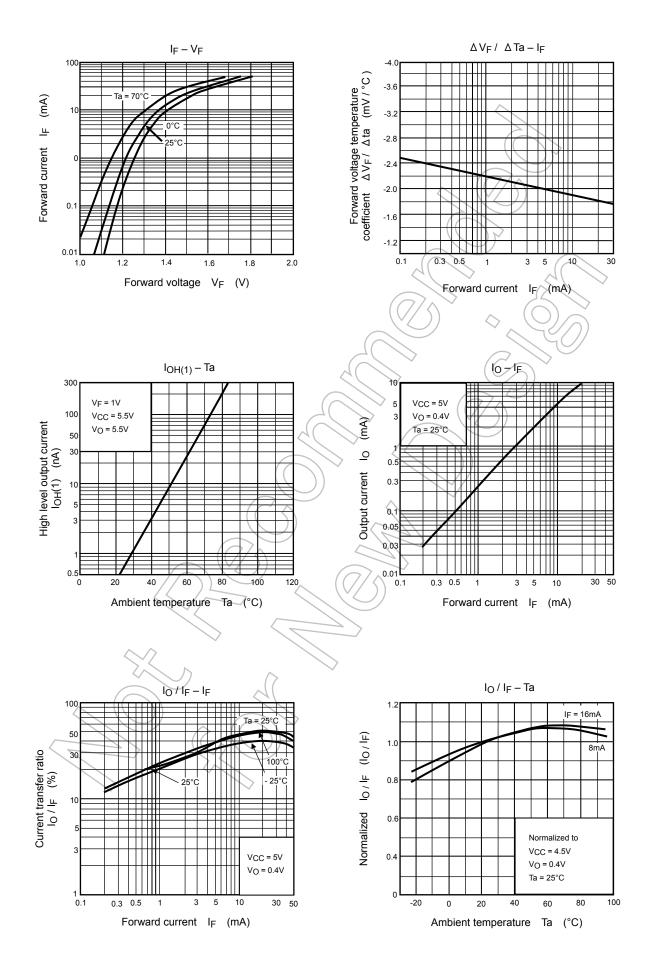
Test Circuit 1: Switching Time Test Circuit



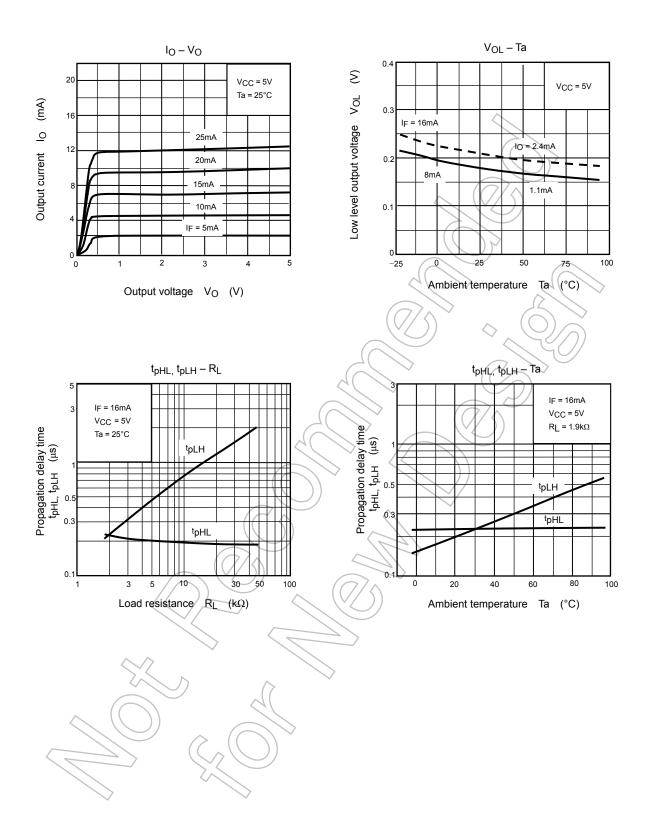
Test Circuit 2: Common Mode Transient Immunity Test Circuit



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