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### **FDS2670**

### 200V N-Channel PowerTrench® MOSFET

### **General Description**

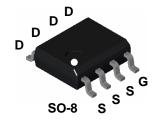
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

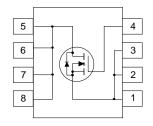
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable  $RDS_{(\text{ON})}$  specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

### **Features**

- 3.0 A, 200 V.  $R_{DS(ON)} = 130 \text{ m}\Omega$  @  $V_{GS} = 10 \text{ V}$
- Low gate charge
- · Fast switching speed
- High performance trench technology for extremely low  $R_{\mbox{\scriptsize DS(ON)}}$
- High power and current handling capability





Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	
V <sub>DSS</sub>	Drain-Source Voltage		200	V
V <sub>GSS</sub>	Gate-Source Voltage		±20	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	3.0	A
	- Pulsed		20	
P <sub>D</sub>	Power Dissipation for Single Operation	(Note 1a)	2.5	W
		(Note 1b)	1.2	
		(Note 1c)	1.0	
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	3.2	V/ns
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range		-55 to +150	°C

### **Thermal Characteristics**

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	50	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1c)	125	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	(Note 1)	25	°C/W

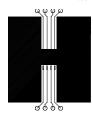
**Package Marking and Ordering Information** 

Device Marking	Device	Reel Size	Tape width	Quantity
FDS2670 FDS2670 133		13"	12mm	2500 units

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-So	ource Avalanche Ratings (Note	1)	1			
W <sub>DSS</sub>	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 100 \text{ V},  I_D = 3.0 \text{ A}$			375	mJ
I <sub>AR</sub>	Maximum Drain-Source Avalanche Current				3.0	Α
Off Char	acteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, \qquad I_{D} = 250 \mu\text{A}$	200			V
ΔBV <sub>DSS</sub> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$		214		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 160 V, V <sub>GS</sub> = 0 V			1	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	$V_{GS} = 20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	$V_{GS} = -20 \text{ V}$ $V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	2	4	4.5	V
$\Delta V_{GS(th)} \over \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$		-10		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance	$V_{GS} = 10 \text{ V},  I_D = 3.0 \text{ A}$ $V_{GS} = 10 \text{ V}, I_D = 3.0 \text{ A}, T_J = 125^{\circ}\text{C}$		100 205	130 275	mΩ
$I_{D(on)}$	On-State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 10 \text{ V}$	20			Α
<b>g</b> FS	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_{D} = 3.0 \text{ A}$		15		S
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 100 \text{ V},  V_{GS} = 0 \text{ V},$		1228		pF
Coss	Output Capacitance	f = 1.0 MHz		112		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			17		pF
Switchir	ng Characteristics (Note 2)					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 100 \text{ V},  I_D = 1 \text{ A},$		13	23	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		8	16	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time			30	48	ns
t <sub>f</sub>	Turn-Off Fall Time			25	40	ns
Qg	Total Gate Charge	$V_{DS} = 100 \text{ V},  I_{D} = 3 \text{ A},$		27	43	nC
$Q_{gs}$	Gate-Source Charge	V <sub>GS</sub> = 10 V		7		nC
$Q_{gd}$	Gate-Drain Charge			10		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				2.1	Α
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, \qquad I_{S} = 2.1 \text{ A (Note 2)}$		0.7	1.2	V

#### Notes:

1. R<sub>BJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>BJC</sub> is guaranteed by design while R<sub>BCA</sub> is determined by the user's board design.



a) 50°/W when mounted on a 1in² pad of 2 oz copper



b) 105°/W when mounted on a 0.04 in² pad of 2 oz copper



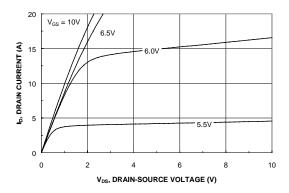
c) 125°/W when mounted on a minimum pad.

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width <  $300\mu$ s, Duty Cycle < 2.0%

3.  $I_{SD} \leq 3A$ , di/dt  $\leq 100A/\mu s$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^{\circ}C$ 

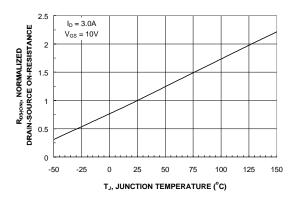
### **Typical Characteristics**



1.6 | OBS | 1.4 | OBS | 5.5V | OBS | 5.5V | OBS | 1.2 | OBS | OBS

Figure 1. On-Region Characteristics.

Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.



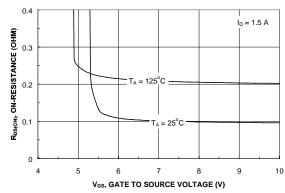
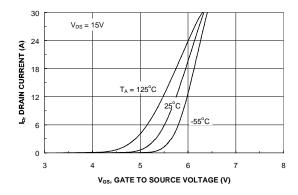


Figure 3. On-Resistance Variation with Temperature.

Figure 4. On-Resistance Variation with Gate-to-Source Voltage.



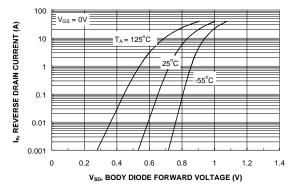
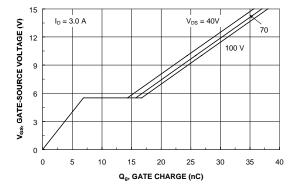


Figure 5. Transfer Characteristics.

Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

### **Typical Characteristics**



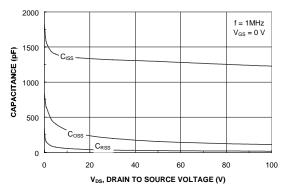
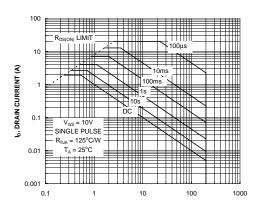


Figure 7. Gate Charge Characteristics.





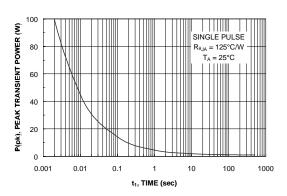


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

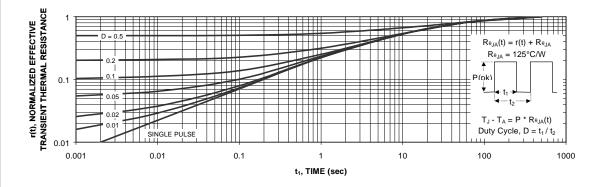


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.

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