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FGH50N3 300 V SMPS IGBT

General Description

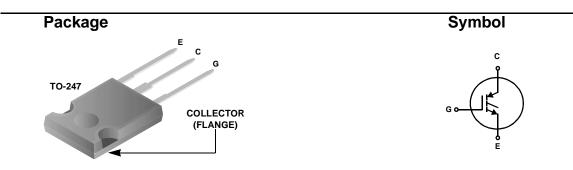
Using Fairchild[®]s planar technology, this IGBT is ideal for many high voltage switching applications operating at high frequencies where low conduction losses are essential. This device has been optimized for medium frequency switch mode power supplies.

Applications

• SMPS

Features

- Low Saturation Voltage: VCE(sat) = 1.4 V max
- Low EOFF = 6.6 uJ/A
- SCWT = 8 us @ = 125℃
- 300V Switching SOA Capability
- Positive Temperature Coefficient above 50 A



Device Maximum Ratings T_C= 25°C unless otherwise noted

Symbol	Parameter	Ratings	Unit	
BV _{CES}	V _{CES} Collector to Emitter Breakdown Voltage		V	
I _{C25}	Collector Current Continuous, T _C = 25°C	75	Α	
I _{C110}	Collector Current Continuous, T _C = 110°C	75	Α	
I _{CM}	Collector Current Pulsed (Note 1)	240	Α	
V _{GES}	Gate to Emitter Voltage Continuous	±20	V	
V _{GEM}	Gate to Emitter Voltage Pulsed	±30	V	
SSOA	Switching Safe Operating Area at T _J = 150°C, Figure 2	150A at 300V		
E _{AS}	Single Pulse Avalanche Energy, I_{CE} = 30A, L = 1.78mH, V_{DD} = 50V	800	mJ	
E _{ARV}	Single Pulse Reverse Avalanche Energy, I _{EC} = 30A, L = 1.78mH, V _{DD} = 50V		mJ	
PD	Power Dissipation Total $T_C = 25^{\circ}C$	463	W	
	Power Dissipation Derating $T_C > 25^{\circ}C$	3.7	W/°C	
TJ	Operating Junction Temperature Range		°C	
T _{STG}	Storage Junction Temperature Range	-55 to 150	°C	
t _{SC}	Short Circuit Withstand Time (Note 2)	8	μs	

CAUTION: Stresses above those listed in "Device Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. NOTE:

1. Pulse width limited by maximum junction temperature.

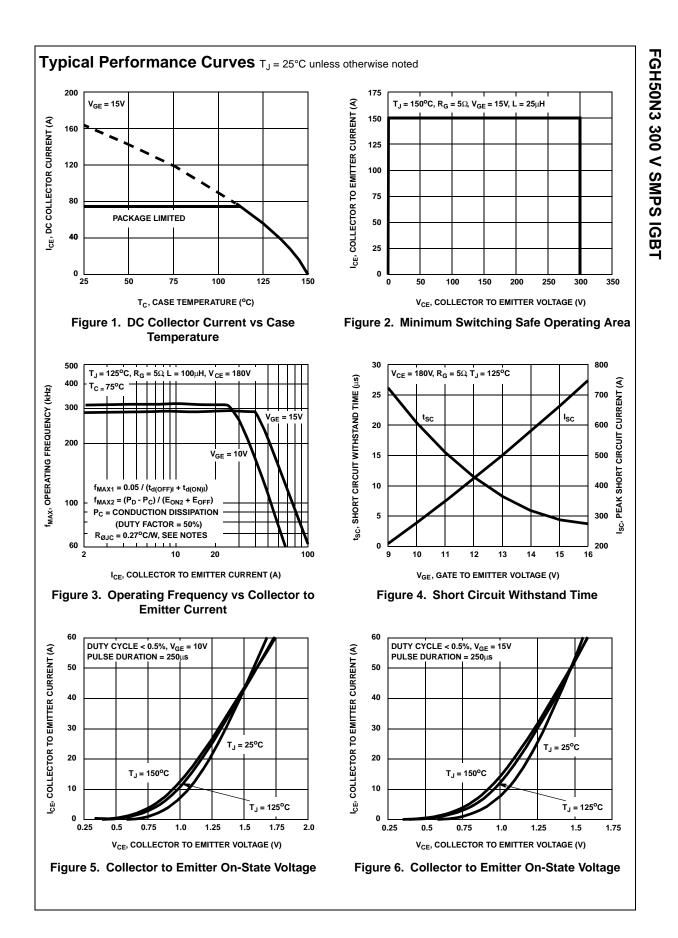
2. $V_{CE(PK)} = 180V$, $T_J = 125^{\circ}C$, $V_{GE} = 12Vdc$, $R_G = 5\Omega$

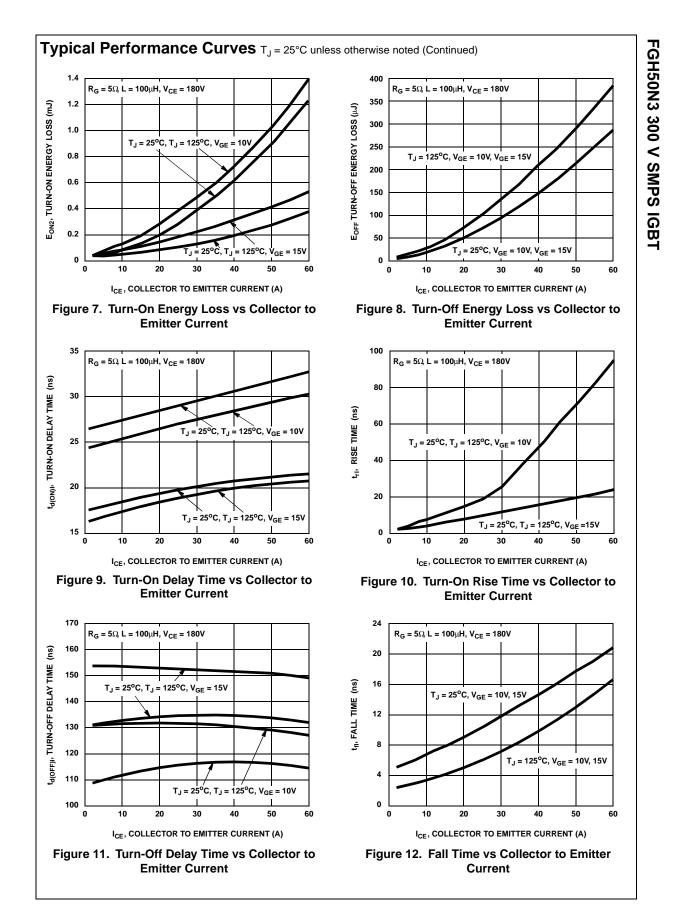
Device Marking		Device Package Tape			Width /A		Quantity 30	
FGH50N3 FGH50N3			TO-247 N/					
	al Char	acteristics T _J = 25°C u	nless otherwise	noted				
Symbol		Parameter	Test C	onditions	Min	Тур	Max	Unit
off State	Characte	eristics						
BV _{CES}	Collector to Emitter Breakdown Voltage		$I_{CE} = 250 \mu A, V_{CE} = 0 V$		300V	-	-	V
BV _{ECS}	Emitter to Collector Breakdown Voltage $I_{EC} = 10mA$, $V_{GE} = 0V$		-	15V	-	-	V	
I _{CES}	Collector to Emitter Leakage Current		$V_{CE} = 300V$	T _J = 25°C	-	-	250	μA
		-	01	T _J = 125°C	-	-	2.0	mA
I _{GES}	Gate to E	mitter Leakage Current	$V_{GE} = \pm 20V$, ,	-	-	±250	nA
n State	Characte	aristics						
V _{CE(SAT)}		to Emitter Saturation Voltage	I _{CE} = 30A	T _J = 25°C	-	1.30	1.4	V
	Concotor	to Emilier outdration voltage	$V_{GE} = 15V$	$T_{J} = 125^{\circ}C$	-	1.25	1.4	V
ynamic	Characte	eristics						
Q _{G(ON)}	Gate Cha	rge	I _{CE} = 30A	V _{GE} = 15V	-	180	-	nC
			$V_{CE} = 150V$	$V_{GE} = 20V$	-	228	-	nC
V _{GE(TH)}	Gate to Emitter Threshold Voltage $I_{CE} = 250\mu A, V_{CE} = V_{GE}$		4.0	4.8	5.5	V		
V_{GEP}			I _{CE} = 30A, V _{CE} = 150V		-	7.0	-	V
witching	o Charac	teristics						
SSOA	Switching SOA		$T_J = 150^{\circ}C, R_G = 5\Omega,$ $V_{GE} = 15V, L = 25\mu H,$ Vce = 300V		150	-	-	А
t _{d(ON)} I	Current T	urn-On Delay Time	IGBT and Diode at $T_J = 25^{\circ}C$,		-	20	-	ns
t _{rl}	Current R	Rise Time I _{CE} = 30A,		-	15	-	ns	
t _{d(OFF)} I	Current T	urn-Off Delay Time	V _{CE} = 180V, V _{GE} = 15V, R _G = 5Ω, L = 100μH, Test Circuit - Figure 20		-	135	-	ns
t _{fl}	Current F	all Time			-	12	-	ns
E _{ON2}	Turn-On I	Energy (Note 1)			-	130	-	μJ
E _{OFF}	Turn-Off I	Energy (Note 2)			-	92	120	μJ
t _{d(ON)} I	Current T	urn-On Delay Time	IGBT and Diode at $T_J = 125^{\circ}C$,		-	19	-	ns
t _{rl}	Current R	lise Time	I _{CE} = 30A,		-	13	-	ns
t _{d(OFF)} I	Current T	urn-Off Delay Time	$V_{CE} = 180V,$		-	155	190	ns
t _{fl}	Current F	all Time	V _{GE} = 15V, R _G = 5Ω,		-	7	15	ns
E _{ON2}	-	Energy (Note 1)	$K_G = 522$ L = 100µH,		-	225	270	μJ
E _{OFF}		Energy (Note 2)	Test Circuit - Figure 20		-	135	200	لىل
hermal (1	-	1			ı <u> </u>
R _{θJC}	1	Resistance Junction-Case	TO-247		1		0.27	°C/W

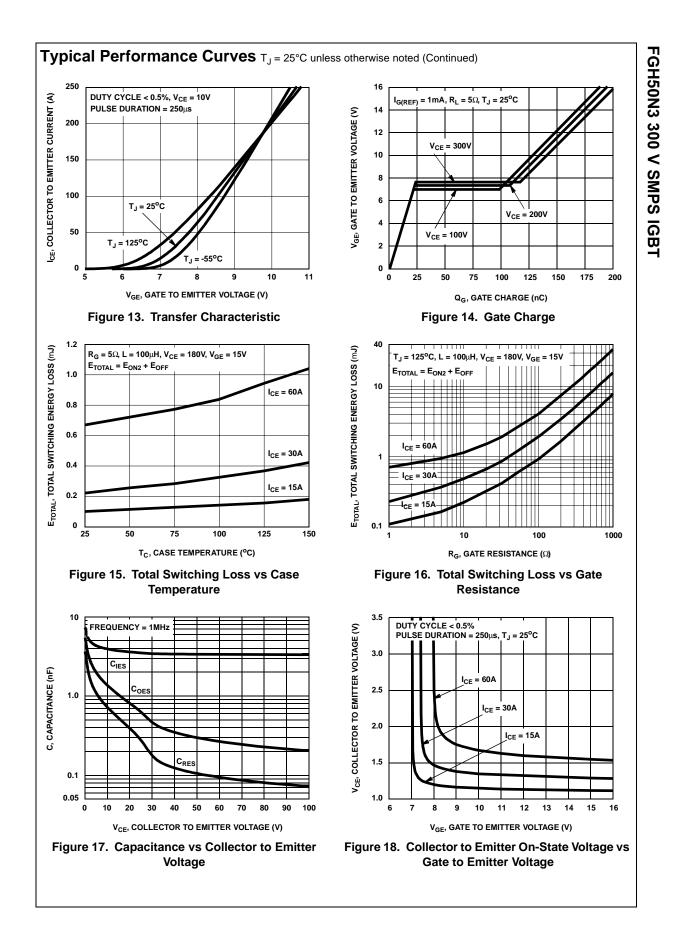
1. E_{ON2} is the turn-on loss when a typical diode is used in the test circuit and the diode is at the same T_J as the IGBT. The diode type is specified in figure 20.

2. Turn-Off Energy Loss (E_{OFF}) is defined as the integral of the instantaneous power loss starting at the trailing edge of the input pulse and ending at the point where the collector current equals zero (I_{CE} = 0A). All devices were tested per JEDEC Standard No. 24-1 Method for Measurement of Power Device Turn-Off Switching Loss. This test method produces the true total Turn-Off Energy Loss.

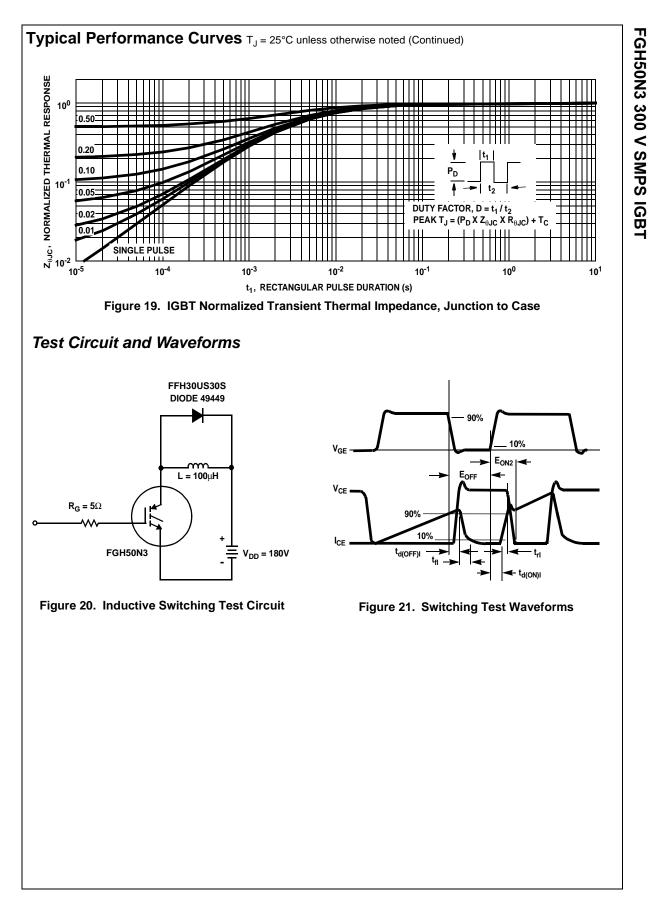
FGH50N3 300 V SMPS IGBT

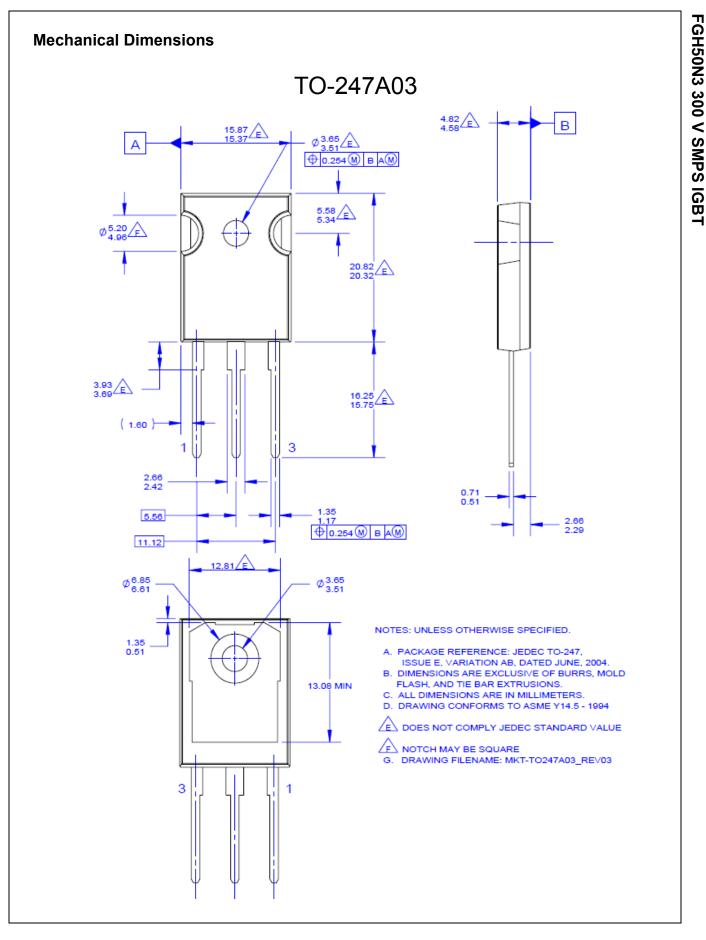






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