

Monolithic Digital IC LB11995

Three-Phase Brushless Motor Driver for CD-ROM Spindle Drive

Overview

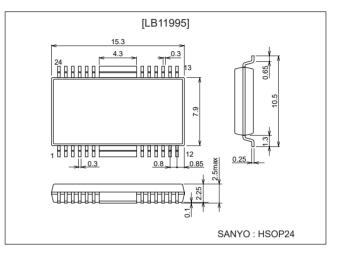
The LB11995 is a 3-phase brushless motor driver especially suited for CD-ROM spindle motor drives.

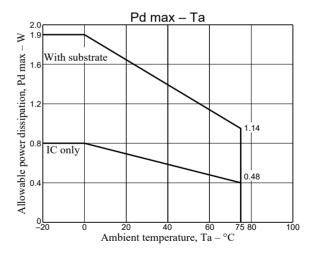
Functions

- Current linear drive
- Control V type amplifier
- Separate power supply for output upper side bias circuit allows low output saturation by boosting this power supply only (useful for 5V power supply types).
- Upper side current detection technique reduces loss voltage of current detection resistor. Voltage drop caused by this resistor reduces internal power dissipation of IC.
- Built-in short braking circuit
- Built-in reverse blocking circuit
- Hall FG output
- Built-in S/S function
- Built-in current limiter circuit (selectable, 2 steps)
- Built-in Hall power supply
- Built-in thermal shutdown circuit
- Supports 3.3V DSP

Package Dimensions

unit: mm 3227-HSOP24





- Any and all SANYO products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO representative nearest you before using any SANYO products described or contained herein in such applications.
- SANYO assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO products described or contained herein.

SANYO Electric Co., Ltd. Semiconductor Company TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|-----------------------|--|-------------------|------|
| Power supply voltage | V _{CC} 1 max | | 7.0 | V |
| | V _{CC} 2 max | | 14.4 | V |
| | V _{CC} 3 max | | 14.4 | V |
| Applied output voltage | V _O max | | 14.4 | V |
| Applied intput voltage | V _{IN} max | | V _{CC} 1 | V |
| Output current | I _O max | | 1.3 | А |
| Allowable power dissipation | Pd max | IC only | 0.8 | W |
| | | with substrate (114.3 x 76.1 x 1.6 mm ³ , glass exposy) | 1.9 | W |
| Operating temperature | Topr | | -20 to +75 | ĉ |
| Storage temperature | Tstg | | –55 to +150 | S |

Operating Conditions at $Ta = 25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|----------------------|-------------------|--------------------|-----------|------|
| Power supply voltage | V _{CC} 1 | | 4 to 6 | V |
| | V _{CC} 2 | ≥V _{CC} 1 | 4 to 13.6 | V |
| | V _{CC} 3 | | 4 to 13.6 | V |

Sample Application at Ta = $25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------|---------------------|--|-----------|------|
| 12V type | V _{CC} 1 | Regulated voltage | 4 to 6 | V |
| | $V_{CC}2 = V_{CC}3$ | Unregulated voltage | 4 to 13.6 | V |
| 5V type | $V_{CC}1 = V_{CC}3$ | Regulated voltage | 4 to 6 | V |
| | V _{CC} 2 | Boost-up voltage or regulated voltage (Note) | 4 to 13.6 | V |

Note: When boost-up voltage is used at V_{CC}^2 , output can be set to low-saturation.

| Electrical Characteristics at Ta = 25° C, V _{CC} 1 = 5V, V _{CC} 2 = V _{CC} 3 = 12V |
|--|
|--|

| Parameter | Symbol | Conditions | Ratings | | | Unit |
|-------------------------------------|---------------------|--|---------|------|-----------------------|-------------------|
| i ulumotor | Cymbol | | min | typ | max | 01110 |
| [Power supply current] | | | | | | |
| Power supply current | I _{CC} 1 | V _C = V _{CREF} | | 8 | | mA |
| | I _{CC} 2 | $V_{C} = V_{CREF}$ | | 0 | | mA |
| | I _{CC} 3 | $V_{C} = V_{CREF}$ | | 150 | 250 | μΑ |
| Output idle current | I _{CC} 10Q | $V_{S/S} = 0V$ | | | 200 | μA |
| | I _{CC} 2OQ | $V_{S/S} = 0V$ | | | 30 | μA |
| | I _{CC} 3OQ | $V_{S/S} = 0V$ | | | 30 | μA |
| [Output] | | • | | | | |
| Saturation voltage, upper side 1 | V _{OU} 1 | $I_0 = -0.5A, V_{CC}1 = 5V, V_{CC}2 = V_{CC}3 = 12V$ | | 1.0 | | V |
| lower side 1 | V _{OD} 1 | I _O = 0.5A, V _{CC} 1 = 5V, V _{CC} 2 = V _{CC} 3 = 12V | | 0.3 | | V |
| Saturation voltage, upper side 2 | V _{OU} 2 | $I_0 = -0.5A, V_{CC}1 = V_{CC}3 = 5V, V_{CC}2 = 12V$ | | 0.3 | | V |
| lower side 2 | V _{OD} 2 | $I_0 = 0.5A, V_{CC}1 = V_{CC}3 = 5V, V_{CC}2 = 12V$ | | 0.3 | | V |
| Current limiter setting voltage | V _{CL} 1 | R _{RF} = 0.33Ω, LMC; OPEN | | 0.24 | | V |
| | V _{CL} 2 | R _{RF} = 0.33Ω, LMC; GND | | 0.35 | | V |
| [Hall amplifier] | 02 | | II | | | |
| Common mode input voltage range | V _{HCOM} | | 1.2 | | V _{CC} 1–1.0 | V |
| Input bias current | I _{HIB} | | | 1 | 00 | μA |
| Minimum Hall input level | V _{HIN} | | 60 | | | mV _{P-P} |
| [S/S pin] | | 1 | I I | | 11 | 1-1 |
| High level voltage | V _{S/SH} | | 2.0 | | V _{CC} 1 | V |
| Low level voltage | V _{S/SL} | | | | 0.7 | V |
| Input current | I _{S/SI} | $V_{S/S} = 5V$ | | | 200 | μA |
| Leak current | I _{S/SL} | $V_{S/S} = 0V$ | -30 | | | μA |
| [Control] | -/ | | 1 | | 1 | |
| V _C pin input current | I _{VC} | $V_{C} = V_{CREF} = 1.65V$ | | | 1 | μA |
| V _{CRFF} pin input current | IVCREF | $V_{\rm C} = V_{\rm CREF} = 1.65 V$ | | | 1 | μA |
| Voltage gain | | $\Delta V_{RF} \Delta V_{C}$ | | 0.35 | | times |
| Startup voltage | V _{CTH} | V _{CREF} = 1.65V | 1.5 | | 1.8 | V |
| Startup voltage width | | V _{CREF} = 1.65V | 50 | | 150 | mV |
| [Hall power supply] | 0111 | oker | 1 | | 1 | |
| Hall power supply voltage | V _H | I _H = 5 mA | | 0.8 | | V |
| Allowable current | I _H | | 20 | | | mA |
| [Thermal shutdown] | | 1 | II | | | |
| Operating temperature | T _{TSD} | Design target value | 150 | 180 | 210 | °C |
| Hysteresis | | Design target value | | 15 | | °C |
| [Short braking] | 130 | | II | | | |
| Brake pin at High level | V _{BRH} | | 4 | | 5 | V |
| | DRIT | | | | | |

Note:

During S/S OFF (standby), the Hall comparator is at High.Items shown to be design target values are not measured.

Truth Table

| | Course & Ciale | | Hall input | Control | |
|---|--------------------|---|------------|---------|----------------|
| | Source -> Sink | U | V | W | V _C |
| 1 | Phase W -> Phase V | н | Н | 1 | Н |
| I | Phase V -> Phase W | | | - | L |
| 2 | Phase W -> Phase U | н | L | 1 | Н |
| 2 | Phase U -> Phase W | | | | L |
| 3 | Phase V -> Phase W | 1 | L | н | Н |
| | Phase W -> Phase V | | | | L |
| 4 | Phase U -> Phase V | | н | | н |
| 4 | Phase V -> Phase U | | | L | L |
| 5 | Phase V -> Phase U | н | L | н | Н |
| | Phase U -> Phase V | | | | L |
| 6 | Phase U -> Phase W | | н | н | н |
| 0 | Phase W -> Phase U | L | п | | L |

Input:

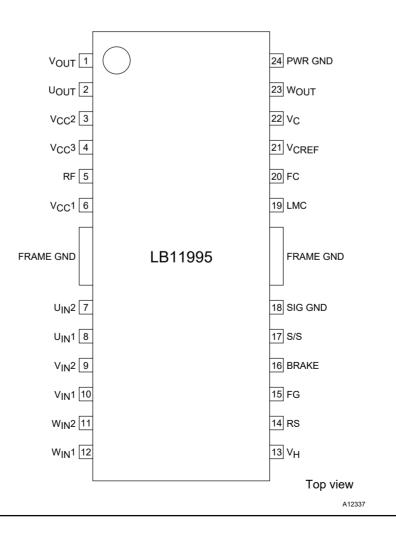
H: Input 1 is higher in potential than input 2 by at least 0.2V.

L: Input 1 is lower in potential than input 2 by at least 0.2V.

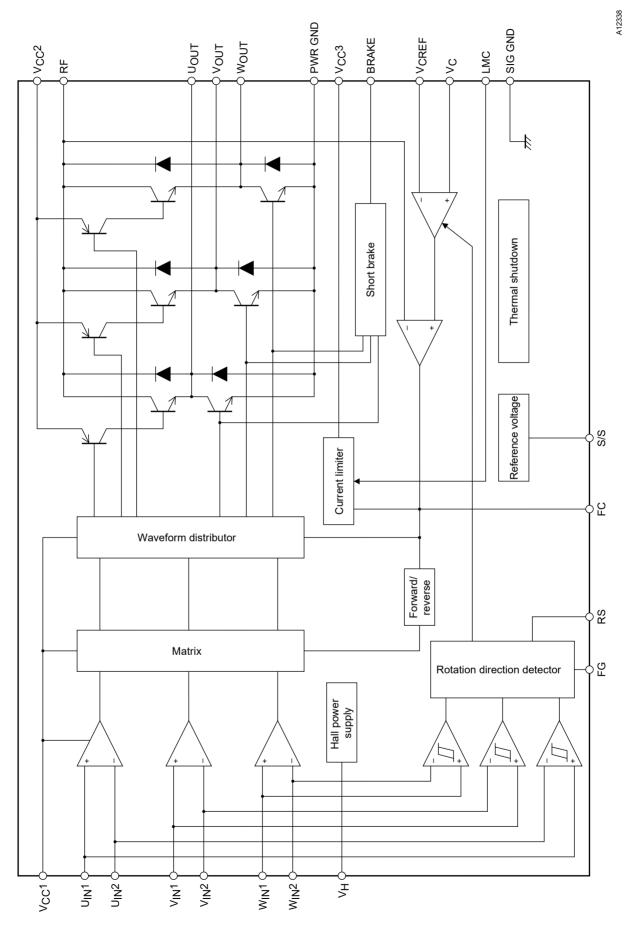
Brake Operation Truth Table

| BRAKE pin | Operation | |
|-------------|-----------------|--|
| н | Short brake | |
| Low or open | Normal rotation | |

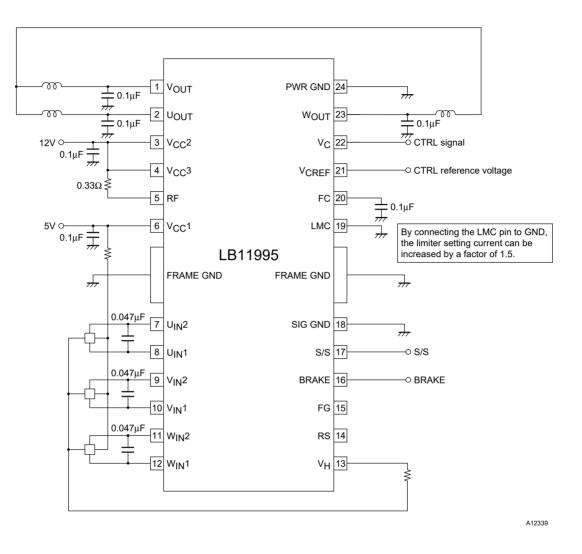
Pin Assignment



Block Diagram

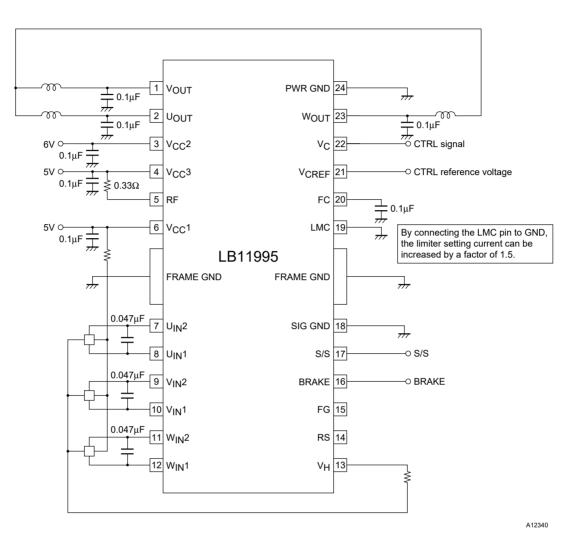


Sample Application Circuit 1 (12V Version)



Power supply - GND Output - GND Between Hall inputs Capacitor requirements may change depending on motor. For some motors, capacitor between Hall inputs may not be needed.

Sample Application Circuit 2 (5V Version)



Power supply - GND Output - GND Between Hall inputs Capacitor requirements may change depending on motor. For some motors, capacitor between Hall inputs may not be needed.

Pin Descriptions

| Pin number | Pin name | Pin voltage | Equivalent circuit | Pin function |
|------------|--|---------------------------------|---|---|
| 3 | V _{CC} 2 | 4V to 13.6V | | Source side predrive voltage supply pin |
| 4 | V _{CC} 3 | 4V to 13.6V | | Constant current control amplifier voltage supply pin |
| 6 | V _{CC} 1 | 4V to 6V | | Power supply pin for all circuits except output transistors, source predriver, and constant current control amplifier |
| 14 | RS | | 100μA ↓ \$10kΩ V _{CC} 1 110μA ↓ 110kΩ | Reverse detector pin Forward rotation: High Reverse rotation: Low |
| 15 | FG | | A12341 | 1 Hall element waveform Schmitt comparator composite output |
| 8 7 | U _{IN} 1 U _{IN} 2 | | V _{CC} 1 | U phase Hall element input and reverse detector U phase Schmitt comparator input pin Logic High indicates U _{IN} 1 > U _{IN} 2. |
| 10 9 | V _{IN} 1 V _{IN} 2 | 1.2V to V _{CC} 1–1V | | V phase Hall element input and reverse detector V phase Schmitt comparator input pin Logic High indicates $V_{IN}1 > V_{IN}2$. |
| 12 11 | W _{IN} 1 W _{IN} 2 | | 25µA (↓) (↓) 25µA 777 777 777 777 777 A12342 | W phase Hall element input and reverse detector W phase Schmitt comparator input pin Logic High indicates W _{IN} 1 > W _{IN} 2. |
| 13 | V _H | | V _{CC} 1 75μA ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ | Hall element lower side bias voltage supply pin |
| 17 | S/S | 0V to V _{CC} 1 | Vcc1 17 50kΩ \$ 17 17 17 17 17 17 17 17 17 17 | When this pin is at 0.7V or lower, or when it is open, all circuits are inactive. When driving motor, set this pin to 2V or higher. |
| 18 | SIG GND | | | GND pin for all circuits except output |
| 20 | FC | | V _{CC} 1 20 | Control loop frequency compensa- tor pin. Connecting a capacitor between this pin and GND prevents closed loop oscillation in current limiting circuitry. |

Continued on next page

Continued from preceding page

| Pin number | Pin name | Pin voltage | Equivalent circuit | Pin function |
|------------|-------------------|----------------------------------|--|---|
| 21 | V _{CREF} | 0V to V _{CC} 1 -1.5V | 15μA (μ) 25μA (μ) 15μA | Control reference voltage supply pin. Determines control start voltage. |
| 22 | v _c | 0V to V _{CC} 1 | | Speed control voltage supply pin V type control technique V _C > V _{CREF} : Forward V _C < V _{CREF} : Slowdown (Reverse-blocking circuit prevents reverse rotation.) |
| 23 | W _{OUT} | | | W phase output |
| 24 | PWR GND | | | Output transistor GND |
| 1 | V _{OUT} | | V _{CC²} | V phase output |
| 2 | U _{OUT} | | | U phase output |
| 5 | RF | | 3.9Ω 3.9Ω 3.9Ω 23(1)(2) (24) A12347 | Upper side output NPN transistor collector pin (common for all 3 phases). For current detection, connect resistor between V _{CC} 3 pin and RF pin. Constant current control and current limiter works by detecting this voltage. |
| 19 | LMC | | V _{CC} 1 | When this pin is connected to GND, the limiter setting current is increased by a factor of 1.5. |
| 16 | BRAKE | | 100μA Vcc1 75kΩ 16 50kΩ 412349 | Short brake pin BRAKE: High> Short brake operation Low/Open> Motor drive operation |

- Specifications of any and all SANYO products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- SANYO Electric Co., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO products(including technical data,services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of SANYO Electric Co., Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of June, 1999. Specifications and information herein are subject to change without notice.