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January 2001

FDN304P

SEMICONDUCTOR IM

P-Channel 1.8V Specified PowerTrench[®] MOSFET

General Description

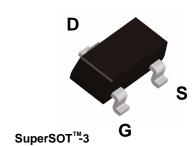
This P-Channel 1.8V specified MOSFET uses Fairchild's advanced low voltage PowerTrench process. It has been optimized for battery power management applications.

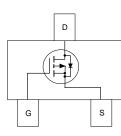
Applications

- Battery management
- Load switch
- Battery protection

Features

- -2.4 A, -20 V. $R_{DS(ON)} = 52 \text{ m}\Omega @ V_{GS} = -4.5 \text{ V}$ $R_{DS(ON)} = 70 \text{ m}\Omega @ V_{GS} = -2.5 \text{ V}$ $R_{DS(ON)} = 100 \text{ m}\Omega @ V_{GS} = -1.8 \text{ V}$
- Fast switching speed
- High performance trench technology for extremely low R_{DS(ON)}
- SuperSOTTM -3 provides low R_{DS(ON)} and 30% higher power handling capability than SOT23 in the same footprint





Absolute Maximum Ratings T.=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		-20	V
V _{GSS}	Gate-Source Voltage		±8	V
I _D	Drain Current – Continuous	(Note 1a)	-2.4	A
	– Pulsed		-10	
PD	Maximum Power Dissipation	(Note 1a)	0.5	W
		(Note 1b)	0.46	
T _J , T _{STG}	Operating and Storage Junction Tempera	ture Range	-55 to +150	°C

R _{BJA} Thermal Resistance, Junction-to-Ambient (Note 1a) 250	°C/W
	0/11
R _{eJC} Thermal Resistance, Junction-to-Case (Note 1) 75	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
304	FDN304P	7"	8mm	3000 units

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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics					
BV _{DSS}	Drain–Source Breakdown Voltage	$V_{GS} = 0 V, I_D = -250 \mu A$	-20			V
<u>ΔBV_{DSS}</u> ΔT _J	Breakdown Voltage Temperature Coefficient	$I_D = -250 \ \mu\text{A}, \text{Referenced to } 25^\circ\text{C}$		-13		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μA
I _{GSSF}	Gate-Body Leakage, Forward	$V_{GS} = 8 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I _{GSSR}	Gate-Body Leakage, Reverse	$V_{GS} = -8 \text{ V} \qquad V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$	-0.4	-0.8	-1.5	V
$rac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D = -250 \ \mu\text{A}, \text{Referenced to } 25^\circ\text{C}$		3		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	$ \begin{array}{ll} V_{GS} = -4.5 \ V, & I_D = -2.4 \ A \\ V_{GS} = -2.5 \ V, & I_D = -2.0 \ A \\ V_{GS} = -1.8 \ V, & I_D = -1.8 \ A \end{array} $		36 47 65	52 70 100	mΩ
I _{D(on)}	On–State Drain Current	$V_{GS} = -4.5 \text{ V}, \qquad V_{DS} = -5 \text{ V}$	-10			Α
g fs	Forward Transconductance	$V_{DS} = -5 V$, $I_D = -1.25 A$		12		S
Dynamic	c Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = -10 V$, $V_{GS} = 0 V$,		1312		pF
Coss	Output Capacitance	f = 1.0 MHz		240		pF
C _{rss}	Reverse Transfer Capacitance			106		pF
Switchin	g Characteristics (Note 2)					
t _{d(on)}	Turn–On Delay Time	$ \begin{array}{ll} V_{\text{DD}} = -10 \ \text{V}, & I_{\text{D}} = -1 \ \text{A}, \\ V_{\text{GS}} = -4.5 \ \text{V}, & R_{\text{GEN}} = 6 \ \Omega \end{array} $		15	27	ns
tr	Turn–On Rise Time			15	27	ns
t _{d(off)}	Turn–Off Delay Time			40	64	ns
t _f	Turn–Off Fall Time			25	40	ns
Qg	Total Gate Charge	$V_{DS} = -10 V$, $I_D = -2.4 A$,		12	20	nC
Q _{gs}	Gate-Source Charge	$V_{GS} = -4.5 V$		2		nC
Q _{gd}	Gate-Drain Charge			2		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				-0.42	А
V _{SD}	Drain–Source Diode Forward Voltage	$V_{GS} = 0 V$, $I_{S} = -0.42$ (Note 2)		-0.6	-1.2	V

1. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.

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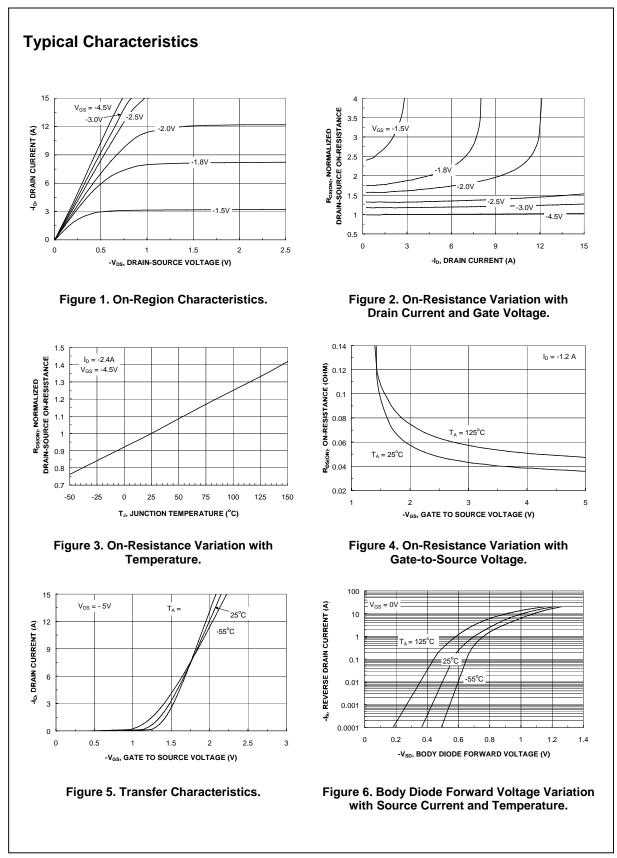
a) 250°C/W when mounted on a 0.02 in² pad of 2 oz. copper.

b) 270°C/W when mounted on a minimum pad.

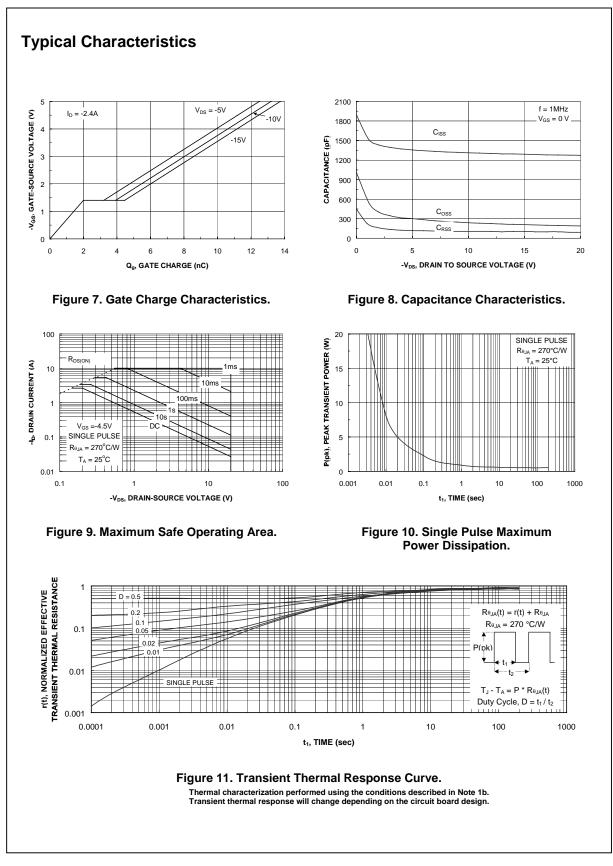
6 Scale 1 : 1 on letter size paper

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2. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%



FDN304P



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