TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type ( $\pi$  –MOS IV)

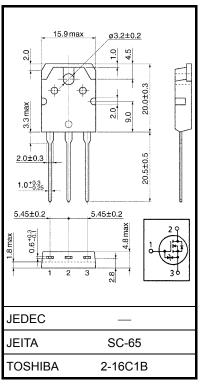
# 2SK3633

#### Switching Regulator Applications

- Low drain-source ON-resistance: R<sub>DS</sub> (ON) = 1.35 Ω (typ.)
- High forward transfer admittance: |Y<sub>fs</sub>| = 5.2 S (typ.)
- Low leakage current: I<sub>DSS</sub> = 100 μA (V<sub>DS</sub> = 640 V)
- Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	800	V
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		V <sub>DGR</sub>	800	V
Gate-source voltage		V <sub>GSS</sub>	±30	V
Drain current	DC (Note 1)	ID	7	
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	21	A
Drain power dissipation (Tc = $25^{\circ}$ C)		PD	150	W
Single-pulse avalanche energy (Note 2)		E <sub>AS</sub>	420	mJ
Avalanche current		I <sub>AR</sub>	7	А
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	15	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristic	Symbol	Мах	Unit	
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	0.833	°C/W	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V\_DD = 90 V, T\_{ch} = 25°C (initial), L = 15.7 mH, I\_{AR} = 7 A, R\_G = 25  $\Omega$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

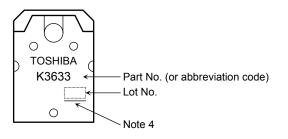
Electrical Characteristics (Ta = 25°C)

Char	acteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 25~V,~V_{DS}=0~V$			±10	μA
Gate-source breakdown voltage		V (BR) GSS	$I_D=\pm 10~\mu A,~V_{GS}=0~V$	±30		_	V
Drain cutoff curre	ent	IDSS	$V_{DS} = 640 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		100	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	800		_	V
Gate threshold ve	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	-resistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3.5 \text{ A}$	_	1.35	1.7	Ω
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 3.5 \text{ A}$	2.5	5.2	_	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		1500		pF
Reverse transfer capacitance		C <sub>rss</sub>			25		
Output capacitance		C <sub>oss</sub>			140		
Switching time	Rise time	tr	$V_{GS}$ $0 V$ $V_{GS}$ $0 V$ $V_{GS}$ $0 V$ $V_{GS}$ $0 V$ $V_{CD}$ $R_{L} =$ $114 \Omega$ $V_{DD} \simeq 400 V$		35		
	Turn-on time	t <sub>on</sub>		_	80	_	20
	Fall time	t <sub>f</sub>		_	50	_	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, $t_W=$ 10 $\mu s$	_	220		
Total gate charge		Qg		_	35	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD}\simeq 400~V,~V_{GS}=10~V,~I_{D}=7~A$	_	22	_	nC
Gate-drain charge		Q <sub>gd</sub>	]	_	13		

#### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Мах	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	7	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	21	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 7 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 7 \text{ A}, V_{GS} = 0 \text{ V},$	_	1200	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	11.5	_	μC

#### Marking

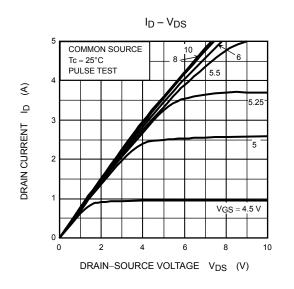


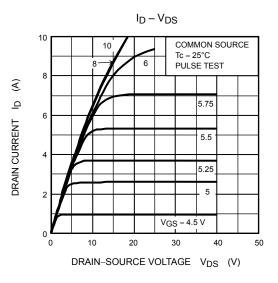
Note 4: A line under a Lot No. identifies the indication of product Labels.

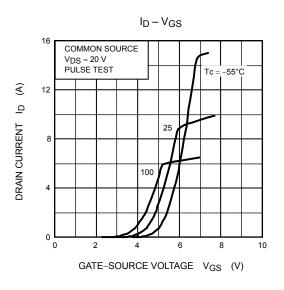
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

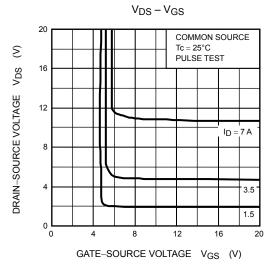
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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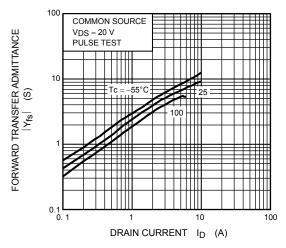




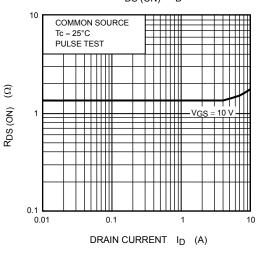




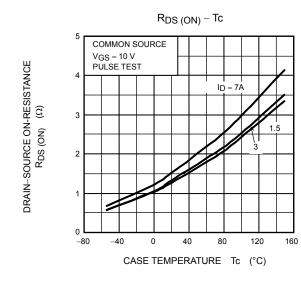


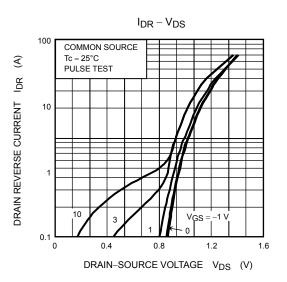


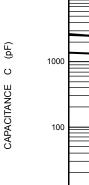
 $R_{DS(ON)} - I_D$ 

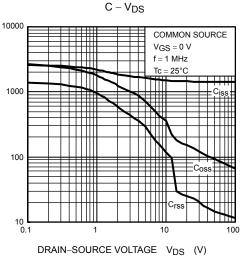


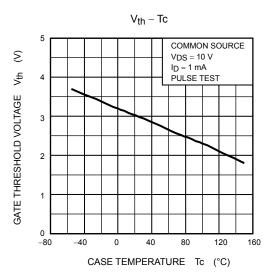
DRAIN-SOURCE ON-RESISTANCE

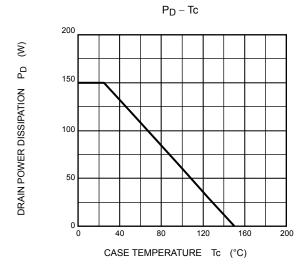




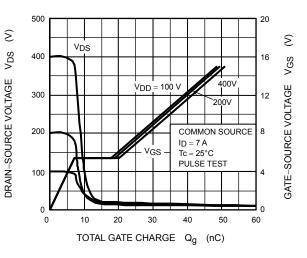


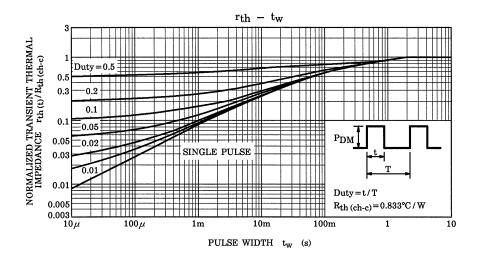


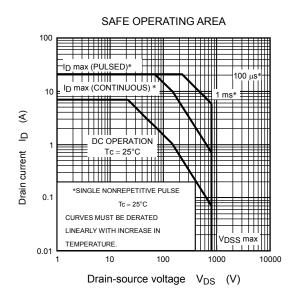


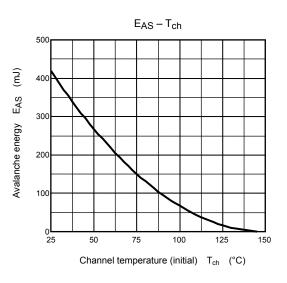


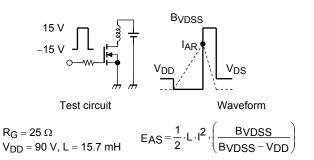












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