Low-power 2-input NAND Schmitt trigger Rev. 8 — 14 January 2022

**Product data sheet** 

### 1. General description

The 74AUP1G132 is a single 2-input NAND gate with Schmitt-trigger inputs. Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device ensures very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- High noise immunity
- Overvoltage tolerant inputs to 3.6 V
- Low static power consumption; I<sub>CC</sub> = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8C (2.7 V to 3.6 V)
- ESD protection:
  - HBM JESD22-A114F Class 3A exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

### 3. Applications

- · Wave and pulse shaper
- Astable multivibrator
- Monostable multivibrator.



# 4. Ordering information

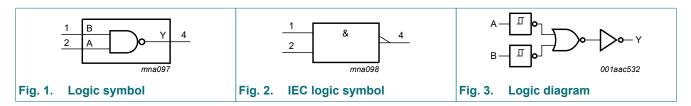
Type number	Package									
	Temperature range Name Description									
74AUP1G132GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1						
74AUP1G132GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886						
74AUP1G132GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115						
74AUP1G132GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202						
74AUP1G132GX	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.32 mm	SOT1226-3						

### 5. Marking

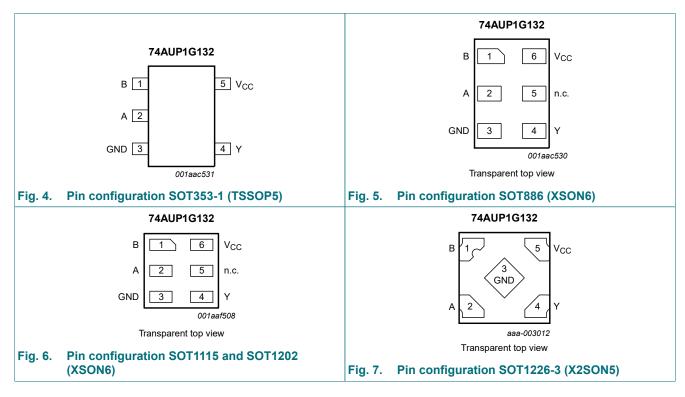
Table 2. Marking						
Type number	Marking code [1]					
74AUP1G132GW	aE					
74AUP1G132GM	aE					
74AUP1G132GN	aE					
74AUP1G132GS	aE					
74AUP1G132GX	aE					

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

### 6. Functional diagram



## 7. Pinning information



### 7.1. Pinning

### 7.2. Pin description

Symbol	Pin	Pin			
	TSSOP5 and X2SON5	XSON6			
В	1	1	data input		
A	2	2	data input		
GND	3	3	ground (0 V)		
Y	4	4	data output		
n.c.	-	5	not connected		
V <sub>CC</sub>	5	6	supply voltage		

### Table 3 Pin description

### 8. Functional description

#### Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

Input	Output	
Α	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

### 9. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Мах	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>1</sub> < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>0</sub> < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
Ι <sub>Ο</sub>	output current	$V_{O} = 0 V \text{ to } V_{CC}$	-	±20	mA
I <sub>CC</sub>	supply current		-	50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C [2]	-	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT886 (XSON6) package: P<sub>tot</sub> derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package: P<sub>tot</sub> derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

For SOT1226-3 (X2SON5) package: P<sub>tot</sub> derates linearly with 3.0 mW/K above 67 °C.

### 10. Recommended operating conditions

Table 6.	Table 6. Recommended operating conditions										
Symbol	Parameter	Conditions	Min	Max	Unit						
V <sub>CC</sub>	supply voltage		0.8	3.6	V						
VI	input voltage		0	3.6	V						
Vo	output voltage	Active mode	0	V <sub>CC</sub>	V						
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V						
T <sub>amb</sub>	ambient temperature		-40	+125	°C						

# **11. Static characteristics**

#### Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
T <sub>amb</sub> = 2	25 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.1	μA
I <sub>OFF</sub>	power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.2	μA
Δl <sub>OFF</sub>	additional power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μA
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
Δl <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 V; I_{O} = 0 A; V_{CC} = 3.3 V$ [1]	-	-	40	μA
CI	input capacitance	$V_{I}$ = GND or $V_{CC}$ ; $V_{CC}$ = 0 V to 3.6 V	-	1.1	-	pF
Co	output capacitance	$V_0 = GND; V_{CC} = 0 V$	-	1.7	-	pF
T <sub>amb</sub> = -	40 °C to +85 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		$I_0 = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
I <sub>I</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μA
I <sub>OFF</sub>	power-off leakage current	$V_{1}$ or $V_{0}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.5	μA
∆I <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V;}$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μA
I <sub>CC</sub>	supply current		-	-	0.9	μA
ΔI <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 V; I_{O} = 0 A; V_{CC} = 3.3 V$ [1]	-	-	50	μA
T <sub>amb</sub> = -4	40 °C to +125 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	$0.6 \times V_{CC}$	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.75	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.75	μA
∆I <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V;}$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA
ΔI <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 V; I_{O} = 0 A; V_{CC} = 3.3 V$ [1]	-	-	75	μA

[1] One input at V\_{CC} - 0.6 V, other input at V\_{CC} or GND.

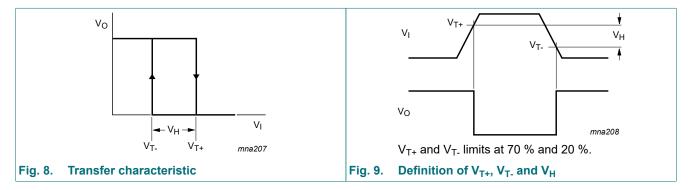
### 11.1. Transfer characteristics

#### Table 8. Transfer characteristics

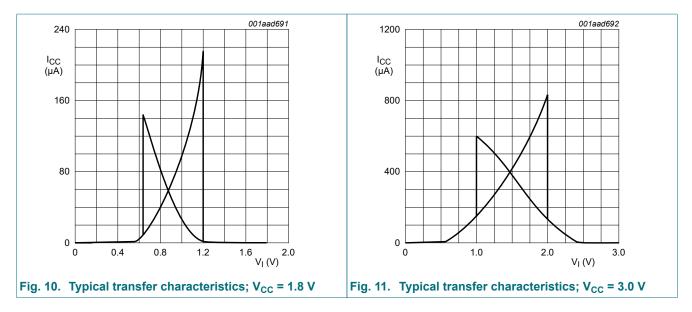
Voltages are referenced to GND (ground = 0 V; for test circuit see Fig. 13.

Symbol	Parameter	Conditions		25 °C			°C to 5 °C		°C to 5 °C	Unit
			Min	Тур	Мах	Min	Мах	Min	Max	1
V <sub>T+</sub>	positive-going	see <u>Fig. 8</u> and <u>Fig. 9</u>								
	threshold voltage	V <sub>CC</sub> = 0.8 V	0.30	-	0.60	0.30	0.60	0.30	0.62	V
	Voltage	V <sub>CC</sub> = 1.1 V	0.53	-	0.90	0.53	0.90	0.53	0.92	V
		V <sub>CC</sub> = 1.4 V	0.74	-	1.11	0.74	1.11	0.74	1.13	V
		V <sub>CC</sub> = 1.65 V	0.91	-	1.29	0.91	1.29	0.91	1.31	V
		V <sub>CC</sub> = 2.3 V	1.37	-	1.77	1.37	1.77	1.37	1.80	V
		V <sub>CC</sub> = 3.0 V	1.88	-	2.29	1.88	2.29	1.88	2.32	V
V <sub>T-</sub>	negative-going	see <u>Fig. 8</u> and <u>Fig. 9</u>								
	threshold voltage	V <sub>CC</sub> = 0.8 V	0.10	-	0.60	0.10	0.60	0.10	0.60	V
	voltage	V <sub>CC</sub> = 1.1 V	0.26	-	0.65	0.26	0.65	0.26	0.65	V
		V <sub>CC</sub> = 1.4 V	0.39	-	0.75	0.39	0.75	0.39	0.75	V
		V <sub>CC</sub> = 1.65 V	0.47	-	0.84	0.47	0.84	0.47	0.84	V
		V <sub>CC</sub> = 2.3 V	0.69	-	1.04	0.69	1.04	0.69	1.04	V
		V <sub>CC</sub> = 3.0 V	0.88	-	1.24	0.88	1.24	0.88	1.24	V
V <sub>H</sub>	hysteresis voltage	(V <sub>T+</sub> - V <sub>T-</sub> ); see <u>Fig. 8</u> , <u>Fig. 9</u> , <u>Fig. 10</u> and <u>Fig. 11</u>								
		V <sub>CC</sub> = 0.8 V	0.07	-	0.50	0.07	0.50	0.07	0.50	V
		V <sub>CC</sub> = 1.1 V	0.08	-	0.46	0.08	0.46	0.08	0.46	V
		V <sub>CC</sub> = 1.4 V	0.18	-	0.56	0.18	0.56	0.18	0.56	V
		V <sub>CC</sub> = 1.65 V	0.27	-	0.66	0.27	0.66	0.27	0.66	V
		V <sub>CC</sub> = 2.3 V	0.53	-	0.92	0.53	0.92	0.53	0.92	V
		V <sub>CC</sub> = 3.0 V	0.79	-	1.31	0.79	1.31	0.79	1.31	V

### 11.2. Waveforms transfer characteristics



#### Low-power 2-input NAND Schmitt trigger



## **12. Dynamic characteristics**

#### Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V; for test circuit see Fig. 13.

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to	o +125 °C	Unit
			Min	Typ [1]	Max	Min	Мах	Min	Max	
C <sub>L</sub> = 5 p	F			1						
t <sub>pd</sub>	propagation	A or B to Y; see <u>Fig. 12</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	22.5	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.6	6.3	13.4	2.4	15.1	2.4	16.6	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.2	4.6	8.2	1.9	9.7	1.9	10.7	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.9	3.9	6.6	1.7	7.9	1.7	8.7	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	3.2	5.3	1.5	6.2	1.5	6.8	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.6	2.9	4.7	1.4	5.6	1.4	6.2	ns
C <sub>L</sub> = 10	pF									
t <sub>pd</sub>	propagation delay	A or B to Y; see <u>Fig. 12</u> [2]								
		V <sub>CC</sub> = 0.8 V	-	26.1	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.0	7.2	15.4	2.7	17.3	2.7	19.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.5	5.2	9.3	2.2	11.0	2.2	12.1	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.3	4.5	7.5	2.0	9.0	2.0	9.9	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.1	3.8	6.1	1.8	7.2	1.8	7.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	3.5	5.5	1.8	6.5	1.8	7.2	ns
C <sub>L</sub> = 15	pF								1	
t <sub>pd</sub>	propagation	A or B to Y; see <u>Fig. 12</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	29.6	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.3	8.0	17.2	3.0	19.4	3.0	21.3	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.8	5.8	10.4	2.5	12.3	2.5	13.5	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.6	5.0	8.3	2.3	10.0	2.3	11.0	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.3	4.2	6.7	2.1	7.9	2.1	8.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.2	3.9	6.1	2.0	7.3	2.0	8.0	ns

74AUP1G132

#### Low-power 2-input NAND Schmitt trigger

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to	o +125 °C	Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 30	pF	,							1	
t <sub>pd</sub>	propagation	A or B to Y; see <u>Fig. 12</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	39.9	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.3	10.2	22.6	3.8	25.4	3.8	27.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.6	7.3	13.3	3.2	15.8	3.2	17.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.2	6.3	10.6	2.9	12.8	2.9	14.1	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	3.0	5.3	8.5	2.7	10.1	2.7	11.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.8	5.0	7.8	2.7	9.2	2.7	10.1	ns
C <sub>L</sub> = 5 p	F, 10 pF, 15 pF	F and 30 pF		· · · · · ·		•			1	
C <sub>PD</sub>	power dissipation	$f_i = 1 \text{ MHz};$ [3] V <sub>I</sub> = GND to V <sub>CC</sub>								
	capacitance	V <sub>CC</sub> = 0.8 V	-	2.6	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.9	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	3.0	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	3.2	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	3.8	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.4	-	-	-	-	-	pF

All typical values are measured at nominal  $V_{CC}$ . [1]

[2] [3]

 $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu$ W).  $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

fo = output frequency in MHz;

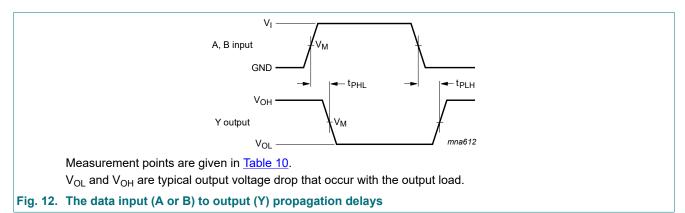
 $C_{L}$  = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$  = sum of the outputs.

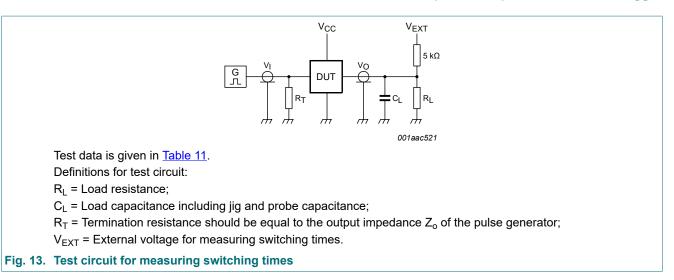
### 12.1. Waveforms and test circuit



#### **Table 10. Measurement points**

Supply voltage	Output	Input				
V <sub>CC</sub>	V <sub>M</sub>	$V_M$ $V_l$ $t_r = t_f$				
0.8 V to 3.6 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns		

#### Low-power 2-input NAND Schmitt trigger



#### Table 11. Test data

Supply voltage	Load		V <sub>EXT</sub>		
V <sub>cc</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	$2 \times V_{CC}$

[1] For measuring enable and disable times  $R_L = 5 k\Omega$ .

For measuring propagation delays, setup and hold times and pulse width  $R_L$  = 1 M $\Omega$ .

74AUP1G132

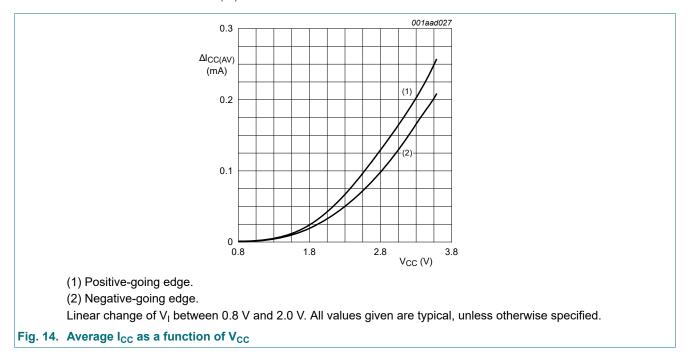
## **13. Application information**

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $\mathsf{P}_{\mathsf{add}} = \mathsf{f}_{\mathsf{i}} \times (\mathsf{t}_{\mathsf{r}} \times \Delta \mathsf{I}_{\mathsf{CC}(\mathsf{AV})} + \mathsf{t}_{\mathsf{f}} \times \Delta \mathsf{I}_{\mathsf{CC}(\mathsf{AV})}) \times \mathsf{V}_{\mathsf{CC}} \text{ where:}$ 

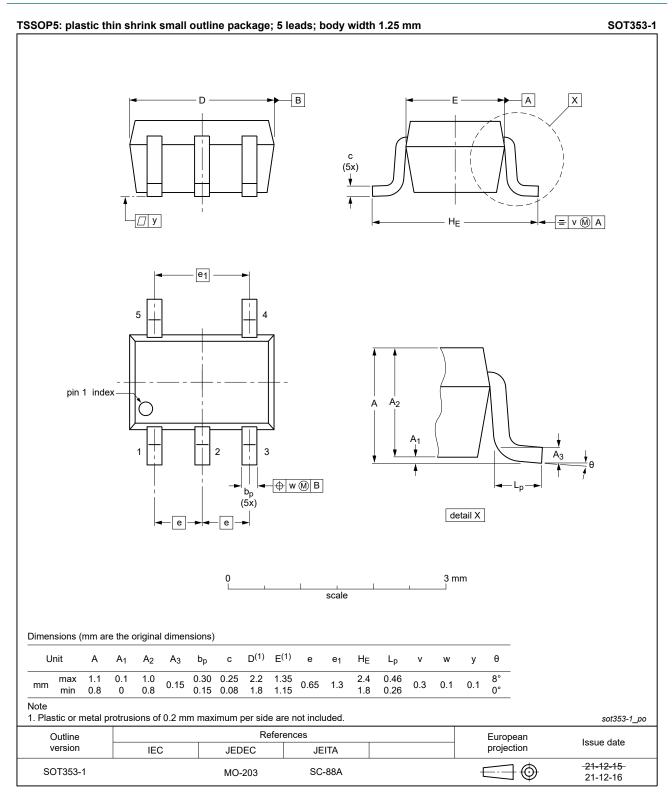
- P<sub>add</sub> = additional power dissipation (µW);
- f<sub>i</sub> = input frequency (MHz);
- t<sub>r</sub> = input rise time (ns); 10 % to 90 %;
- t<sub>f</sub> = input fall time (ns); 90 % to 10 %;
- ΔI<sub>CC(AV)</sub> = average additional supply current (µA).

Average  $\Delta I_{CC(AV)}$  differs with positive or negative input transitions, as shown in Fig. 14.



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## 14. Package outline



#### Fig. 15. Package outline SOT353-1 (TSSOP5)

### Low-power 2-input NAND Schmitt trigger

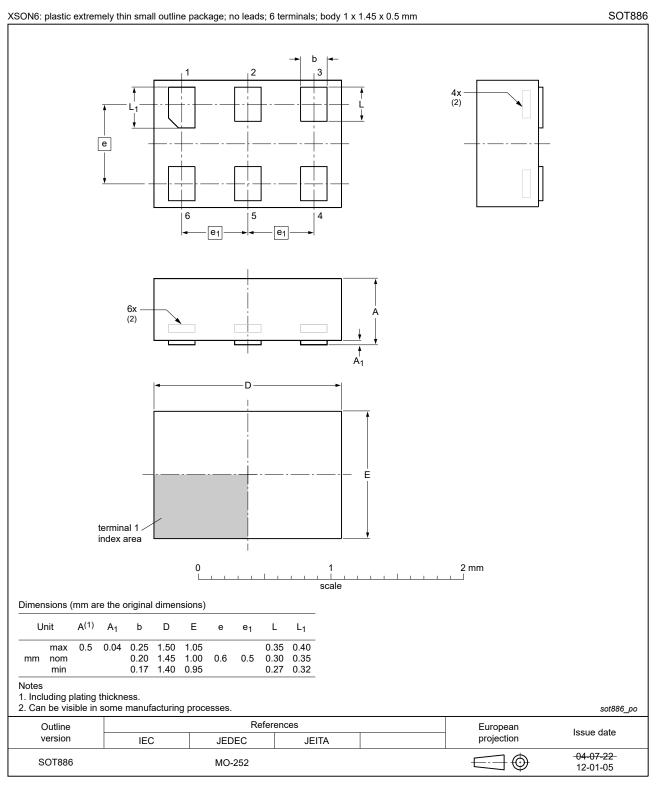
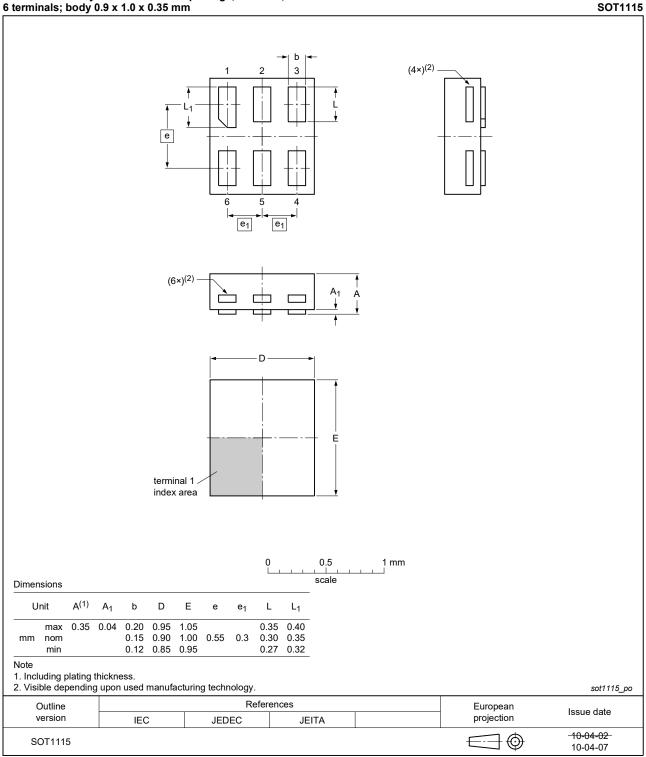


Fig. 16. Package outline SOT886 (XSON6)

### Low-power 2-input NAND Schmitt trigger

#### XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm

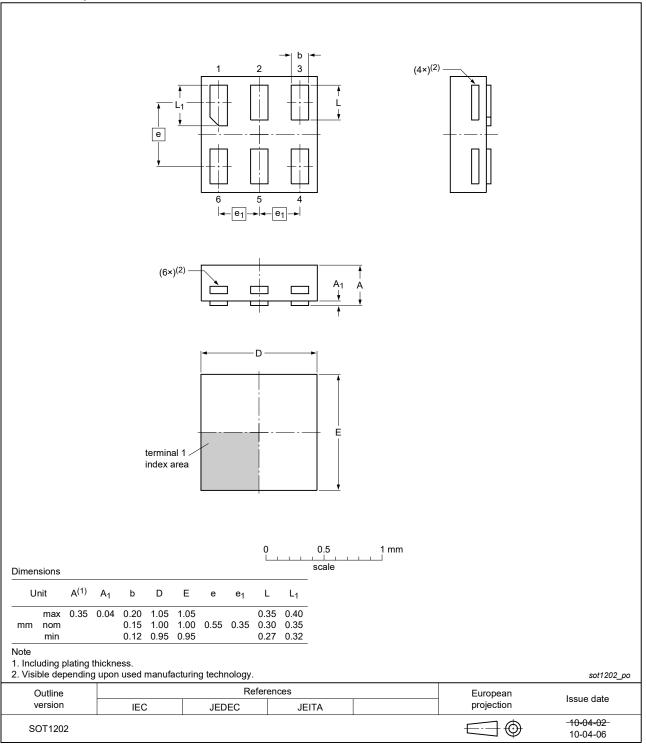




SOT1202

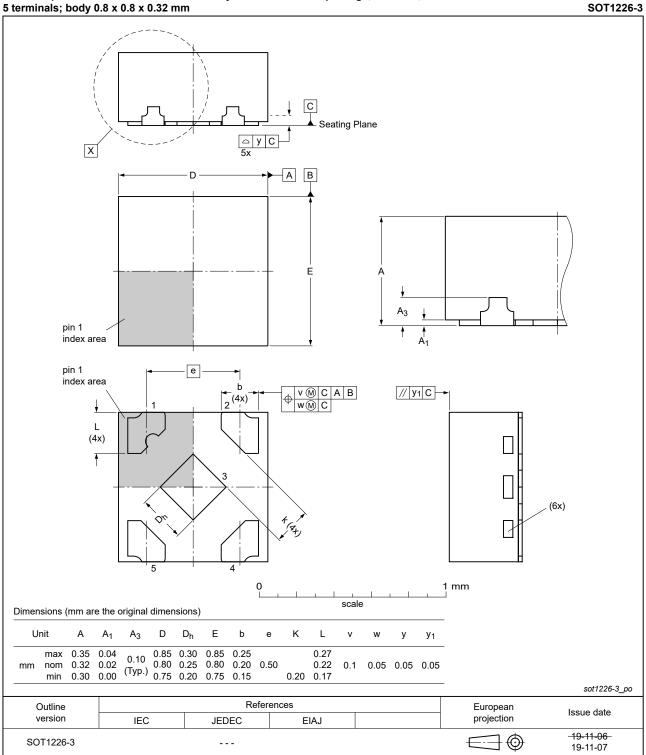
### Low-power 2-input NAND Schmitt trigger

XSON6: extremely thin small outline package; no leads;	
6 terminals; body 1.0 x 1.0 x 0.35 mm	





#### Low-power 2-input NAND Schmitt trigger



#### X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.32 mm

Fig. 19. Package outline SOT1226-3 (X2SON5)

## **15. Abbreviations**

Table 12. Abbreviations			
Acronym	Description		
CDM	Charged Device Model		
DUT	Device Under Test		
ESD	ElectroStatic Discharge		
НВМ	Human Body Model		
MM	Machine Model		

### 16. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AUP1G132 v.8	20220114	Product data sheet	-	74AUP1G132 v.7	
Modifications:	• <u>Fig. 15</u> : Pac	kage outline drawing for S	SOT353-1 (TSSOP	5) has changed.	
74AUP1G132 v.7	20210709	Product data sheet	-	74AUP1G132 v.6	
Modifications:	<ul> <li>Type number</li> <li>Section 1 and</li> </ul>	(2SON5) package change er 74AUP1G132GF (SOT) nd <u>Section 2</u> updated. rating values for P <sub>tot</sub> total	891) removed.		
74AUP1G132 v.6	20190501	Product data sheet	-	74AUP1G132 v.5	
Modifications:	of Nexperia <ul> <li>Legal texts</li> </ul>		new company nam	nply with the identity guidelines e where appropriate.	
74AUP1G132 v.5	20120629	Product data sheet	-	74AUP1G132 v.4	
Modifications:		<ul> <li>Added type number 74AUP1G132GX (SOT1226)</li> <li>Package outline drawing of SOT886 (Fig. 16) modified.</li> </ul>			
74AUP1G132 v.4	20111124	Product data sheet	-	74AUP1G132 v.3	
Modifications:	Legal pages	updated.			
74AUP1G132 v.3	20101029	Product data sheet	-	74AUP1G132 v.2	
74AUP1G132 v.2	20090615	Product data sheet	-	74AUP1G132 v.1	
74AUP1G132 v.1	20061020	Product data sheet	-	-	

## 17. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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