



















# **Datasheet**

# **Tianma**

NL10276BC20-18

10.4" TFT Display

NL-01-013

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# TFT COLOR LCD MODULE

NL10276BC20-18

26cm (10.4 Type) XGA LVDS interface (1port)



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### INTRODUCTION

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The **Standard:** Applications as any failure, malfunction or error of the products are free from any damage to death, human bodily injury or other property (Products Safety Issue) and not related the safety of the public (Social Issues), like general electric devices.

Examples: Office equipment, audio and visual equipment, communication equipment, test and measurement equipment, personal electronic equipment, home electronic appliances, car navigation system (with no vehicle control functions), seat entertainment monitor for vehicles and airplanes, fish finder (except marine radar integrated type), PDA, etc.

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Examples: Vehicle/train/ship control system, traffic signals system, traffic information control system, air traffic control system, surgery/operation equipment monitor, disaster/crime prevention system, etc.

The **Specific:** Applications as any failure, malfunction or error of the products might severe cause any damage to death, human bodily injury or other property (Products Safety Issue) and the safety of the public (Social Issues) and developed, designed and manufactured in accordance with the standards or quality assurance program designated by the customer who requires extremely high level reliability and quality.

Examples: Aerospace system (except seat entertainment monitor), nuclear control system, life support system, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.



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### 1. OUTLINE

### 1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL10276BC20-18 is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

### 1.2 APPLICATION

• For industrial use

### 1.3 FEATURES

- Wide Viewing Angle
- High Contrast
- Wide temperature range
- LVDS interface
- Reversible-scan direction
- Selectable 8-bit or 6-bit digital signals for data of RGB
- Long life LED backlight
- Replaceable lamp for backlight
- Acquisition product for UL60950-1/CSA C22.2 No.60950-1-03 (File number: E170632)
- Compliant with the European RoHS directive (2011/65/EU)



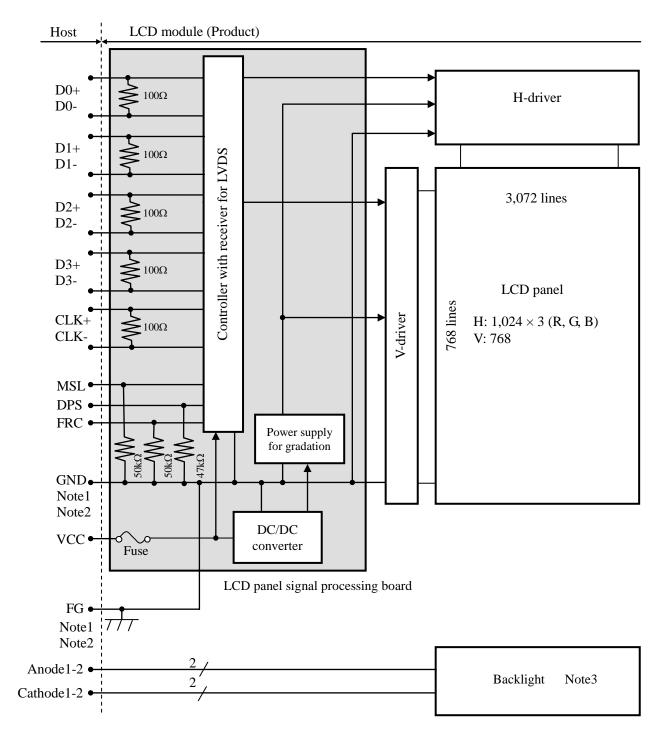


# 2. GENERAL SPECIFICATIONS

Display area	210.432 (H) × 157.824 (V) mm							
Diagonal size of display	26cm (10.4 inches)							
Drive system	a-Si TFT active matrix							
Display color	16,777,216 colors (At 8-bit input, FRC terminal= High) 262,144 colors (At 6-bit input, FRC terminal= Low or Open)							
Pixel	1,024 (H) × 768 (V) pixels							
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe							
Dot pitch	$0.0685 \text{ (H)} \times 0.2055 \text{ (V)} \text{ mm}$							
Pixel pitch	$0.2055 \text{ (H)} \times 0.2055 \text{ (V)} \text{ mm}$							
Module size	$228.0 \text{ (W)} \times 178.5 \text{ (H)} \times 8.7 \text{ (D)} \text{ mm (typ.)}$							
Weight	380 g (typ.)							
Contrast ratio	900: 1 (typ.)							
Viewing angle	At the contrast ratio ≥10:1  • Horizontal: Right side 80° (typ.), Left side 80° (typ.)  • Vertical: Up side 80° (typ.), Down side 80° (typ.)							
Designed viewing direction	<ul> <li>At DPS= Low or Open: Normal scan</li> <li>Viewing direction without image reversal: Up side (12 o'clock)</li> <li>Viewing direction with contrast peak: Down side (6 o'clock)</li> <li>Viewing angle with optimum grayscale (γ≒2.2): Normal axis (perpendicular)</li> </ul>							
Polarizer surface	Clear							
Polarizer pencil-hardness	3H (min.) [by JIS K5600]							
Color gamut	At LCD panel center 40 % (typ.) [against NTSC color space]							
Response time	$Ton+Toff (10\% \longleftrightarrow 90\%)$ 18 ms (typ.)							
Luminance	At $IL = 60mA$ one circuit $400 \text{ cd/m}^2 \text{ (typ.)}$							
Signal system	LVDS 1port (Receiver: THC63LVDF84B, THine Electronics Inc. or equivalent) 8-bit/6-bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE)							
Power supply voltage	LCD panel signal processing board: 3.3V							
Backlight	LED backlight  Replaceable part  Lamp holder set: 104LHS55  Recommended LED driver board (Option)  LED driver board: 104PW02F							
	• Corresponding wiring harness: 104CBL01							



### 3. BLOCK DIAGRAM



Note1: Relation between GND (Signal ground), FG (Frame ground) in the LCD module is as follows.

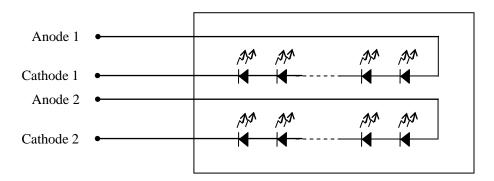
GND - FG Connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds to be connected together in customer equipment.



Note3: Backlight in detail

# Backlight





### 4. DETAILED SPECIFICATIONS

### 4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$228.0 \pm 0.5 \text{ (W)} \times 178.5 \pm 0.5 \text{ (H)} \times 8.7 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	210.432 (H) × 157.824 (V)	Note1	mm
Weight	380 (typ.), 400 (max.)		g

Note1: See "8. OUTLINE DRAWINGS".

### 4.2 ABSOLUTE MAXIMUM RATINGS

	Paramete	r	Symbol	Rating	Unit	Remarks
Power supply voltage	LCD panel si	gnal processing board	VCC	-0.3 to +4.0	V	
Input voltage	Dis	splay signals Note1	VD	-0.3 to VCC+0.3	V	-
for signals	Fun	ction signals Note2	VF	-0.3 to VCC+0.3	V	
Backlight	For	ward current	IL	0 to +70	mA	per one circuit
	Storage tempe	rature	Tst	-30 to +80	°C	-
Omanatina ta	man anatuma	Front surface	TopF	-30 to +80	°C	Note3
Operating te	mperature	Rear surface	TopR	-30 to +80	°C	Note4
				≤ 95	%	Ta≤ 40°C
				≤ 85	%	40°C <ta≤ 50°c<="" td=""></ta≤>
	Relative hum Note5	idity	RH	≤ 55	%	50°C <ta≤ 60°c<="" td=""></ta≤>
	1,000			≤ 36	%	60°C <ta≤ 70°c<="" td=""></ta≤>
				≤ 24	%	70°C <ta≤ 80°c<="" td=""></ta≤>
	Absolute hum Note5	nidity	АН	≤ 70 Note6	g/m <sup>3</sup>	Ta= 80°C

Note1: D0+/-, D1+/-, D2+/-, D3+/-, CLK+/-

Note2: DPS, FRC, MSL

Note3: Measured at LCD panel surface (including self-heat)

Note4: Measured at LCD module's rear shield surface (including self-heat)

Note5: No condensation

Note6: Water amount at Ta= 80°C and RH= 24%

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# 4.3 ELECTRICAL CHARACTERISTICS

# 4.3.1 LCD panel signal processing board

 $(Ta=25^{\circ}C)$ 

Parameter		Symbol	min.	typ.	max.	Unit	Remarks
Power supply voltage		VCC	3.0	3.3	3.6	V	-
Power supply current	ICC	-	450 Note1	780 Note2	mA	at VCC= 3.3V	
Permissible ripple voltage		VRP	-	-	100	mVp-p	for VCC
Differential input threshold voltage	High	VTH	-	-	+100	mV	at VCM= 1.2V
	Low	VTL	-100	-	-	mV	Note3
Terminating resistance		RT	-	100	-	Ω	-
Input voltage for	High	VFH	0.7VCC	-	VCC	V	CMOS level
DPS, FRC and MSL signals	Low	VFL	0	-	0.3VCC	V	CIVIOS level
Input current for	High	IFH	-	-	300	μΑ	
DPS, FRC and MSL signals	Low	IFL	-300	-	-	μΑ	-

Note1: Checkered flag pattern [by IEC 61747-6]

Note2: Pattern for maximum current

Note3: Common mode voltage for LVDS receiver





### 4.3.2 Backlight

(Note1, Note2, Note3)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	60	66	mA	per one circuit
		24.3	27.8	31.4		Ta= +25°C at IL= 60mA/One circuit
	VL	21.8	-	-	V	Ta= +80°C at IL= 60mA/One circuit
Forward voltage		-	-	34.4	V	Ta= -30°C at IL= 60mA/One circuit
		-	-	34.8		Ta= -30°C at IL= 66mA/One circuit

Note1: Please drive with constant current.

Note2: The above specifications are for one LED circuit of the backlight.

Note3: The Luminance uniformity may be changed depending on the current variation between 2 circuits. It is recommended that the current value difference among the circuits be less than 5%.

### 4.3.3 Power supply voltage ripple

This product works if the ripple voltage levels are over the permissible values as the following table, but there might be noise on the display image.

Power supp	ly voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

### 4.3.4 Fuse

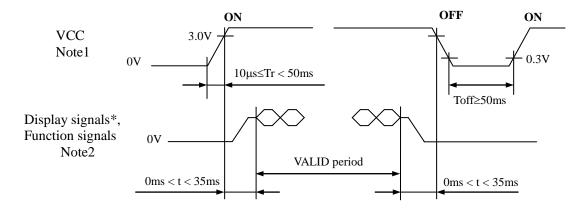
Parameter		Fuse	Dating	Euging ourrent	Remarks
Farameter	Туре	Supplier	Rating	Fusing current	Kemarks
VCC	FCC16202AB	KAMAYA ELECTRIC	2.0 A	4.0 A	Note1
VCC	FCC10202Ab	Co., Ltd.	36 V	4.0 A	Note1

Note1: The power supply's rated current must be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.



## 4.4 POWER SUPPLY VOLTAGE SEQUENCE

### 4.4.1 LCD panel signal processing board



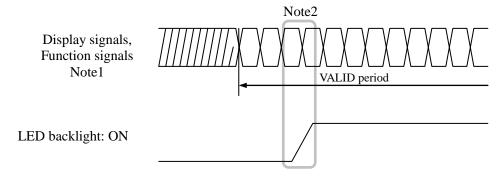
<sup>\*</sup> These signals should be measured at the terminal of  $100\Omega$  resistance.

Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below 3.0V, there is a possibility that a product does not work due to a protection circuit.

Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/-, CLK+/-) and function signals (DPS, FRC, MSL) must be set to Low or High-impedance, except the VALID period (See above sequence diagram), in order to avoid the circuitry damage.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

### 4.4.2 LED driver



Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the VALID period of display and function signals, in order to avoid unstable data display.



### 4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

### 4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): FI-SE20P-HFE (Japan Aviation Electronics Industry Limited (JAE))
Adaptable plug: FI-S20S (Japan Aviation Electronics Industry Limited (JAE))

7 10	iapia	ible plug:	1	1-S20S (Japa	n Aviation Electroni	es mausify Limited	(JAL))				
Pin	No	Symbol	Signal	Input data	signal: 8-bit	Input data signal:	Remarks				
FIII	INO.	Symbol	Signal	MAP A	MAP B	6-bit	Kemarks				
1	A	D3+	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7	-	Note1, Note2				
	В	GND	Ground		-	Ground	Note3				
2	A	D3-	Pixel data	R0-R1,G0-G1,B0-B1	R6-R7,G6-G7,B6-B7	-	Note1, Note2				
	В	GND	Ground		-	Ground	Note3				
3	3	DPS	Selection of scan direction	High: Low or Open:	Reverse scan Normal scan		Note4				
۷	1	FRC	Selection of the number of colors	Hi	igh	Low or Open	Note1 Note5				
4	5	GND	Ground		Ground		Note3				
ć	6 CLK+ Pixel clock				Pixel clock		Note				
7	7	CLK-	Pixel Clock			Note2					
8	3	GND	Ground		Ground		Note3				
ç	)	D2+		D4 D7 DF	D2 D	z DE	N O				
1	0	D2-	Pixel data	B4-B7,DE	B2-B.	5,DE	Note2				
1	1	GND	Ground		Ground		Note3				
1	2	D1+	Pixel data	G3-G7,B2-B3	C1 C5	DO D1	Note2				
1	3	D1-	Pixei data	G3-G7,B2-B3	G1-G5,	DU-D1	Note2				
1	4	GND	Ground		Ground		Note3				
1	5	D0+	D:1 d-4-	D2 D7 C2	DO D	5 CO	N-4-2				
1	6	D0-	Pixel data	R2-R7,G2	R0-R	J,GU	Note2				
1	7	GND	Ground			Note3					
1	8	MSL	Selection of LVDS input map	Low or Open	High	Low or Open	Note5				
1	9	VCC	Down questo		Note3						
2	0	VCC	Power supply	Power supply							

Note1: See "4.6 DISPLAY COLORS AND INPUT DATA SIGNALS".

Note2: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note3: All GND and VCC terminals should be used without any non-connected lines.

Note4: See "4.8 SCANNING DIRECTIONS".

Note5: See "4.5.4 Connection between receiver and transmitter for LVDS".

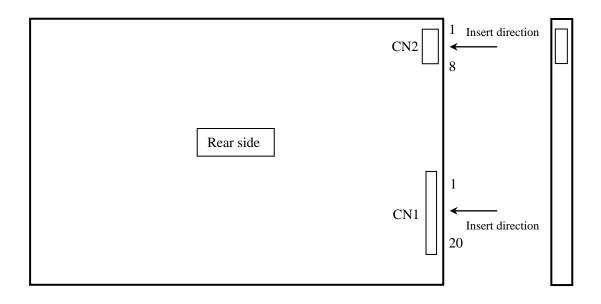


# 4.5.2 Backlight

CN2 socket (LCD module side): SM08B-SRSS-TB (J.S.T. Mfg. Co., Ltd.)
Adaptable plug: SHR-08V-S (J.S.T. Mfg. Co., Ltd.)

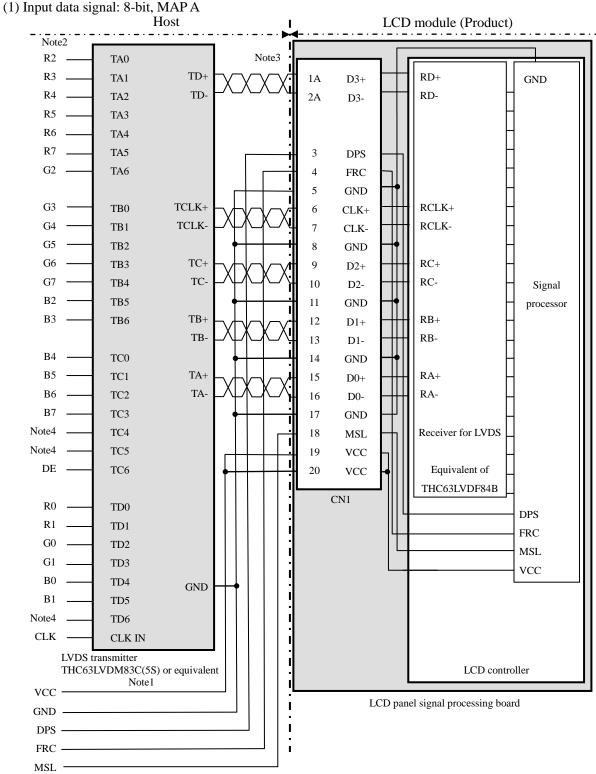
Pin No.	Symbol	Signal	Remarks
1	A1	Anode1	-
2	K1	Cathode1	-
3	A2	Anode2	-
4	K2	Cathode2	-
5	N. C.	-	Keep this pin Open.
6	N. C.	-	Keep this pin Open.
7	N. C.	-	Keep this pin Open.
8	N. C.	-	Keep this pin Open.

# 4.5.3 Positions of socket





### 4.5.4 Connection between receiver and transmitter for LVDS



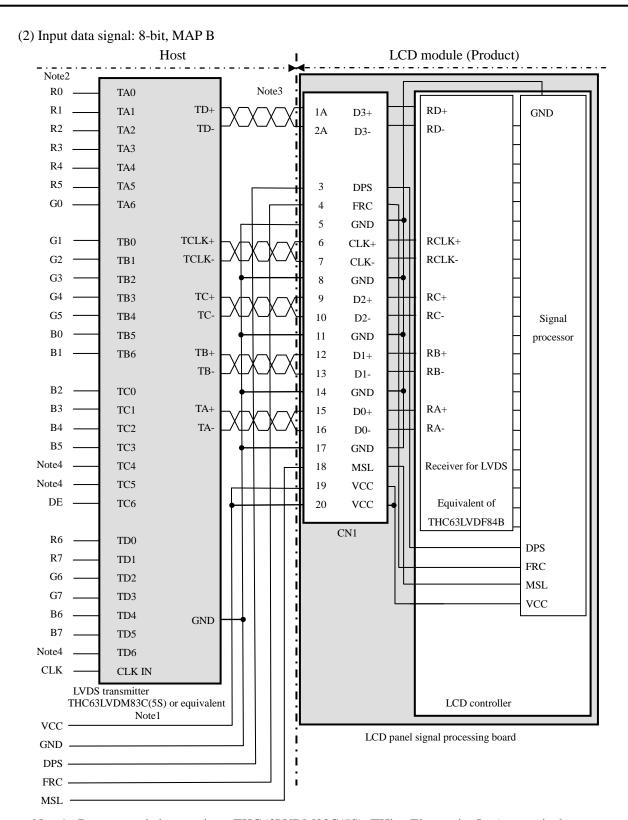
Note1: Recommended transmitter: THC63LVDM83C(5S) (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R7, G7, B7

Note3: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

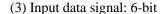
Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.

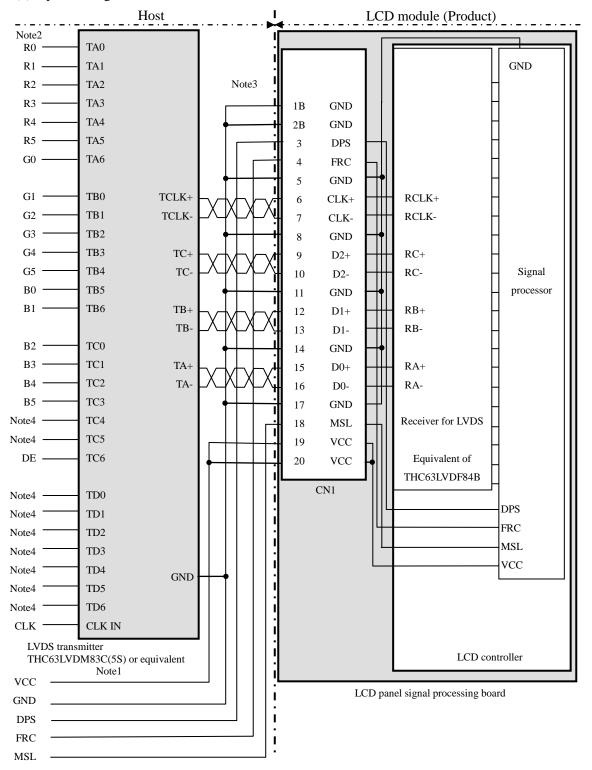




- Note1: Recommended transmitter: THC63LVDM83C(5S) (THine Electronics Inc.) or equivalent
- Note2: LSB (Least Significant Bit) R0, G0, B0 MSB (Most Significant Bit) R7, G7, B7
- Note3: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
- Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.







Note1: Recommended transmitter THC63LVDM83C(5S) (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) – R0, G0, B0 MSB (Most Significant Bit) – R5, G5, B5

Note3: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TC4, TC5 and TD0-6 are not used inside the product, but do not keep them open to avoid noise problem.

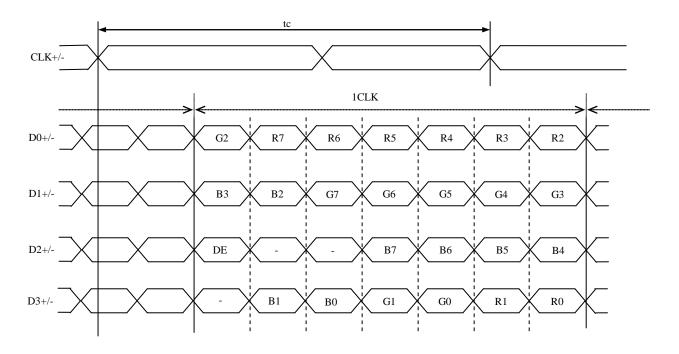
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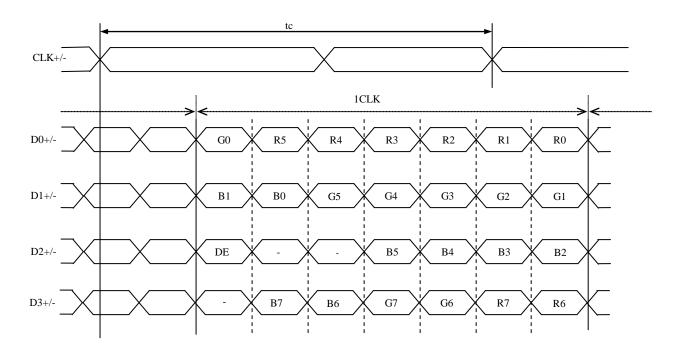


# 4.5.5 Input data mapping

# (1) Input data signal: 8-bit, MAP A

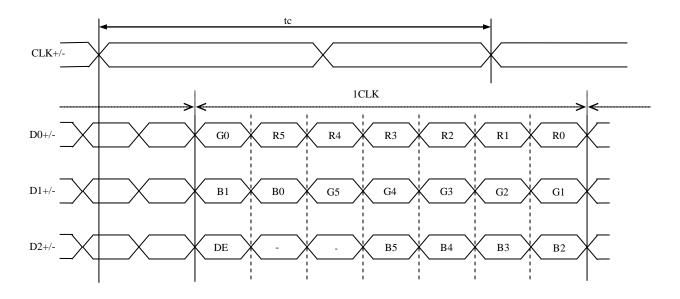


# (2) Input data signal: 8-bit, MAP B





### (3) Input data signal: 6-bit



### 4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

# 4.6.1 Combinations of input data signals, FRC and MSL signal

This product can display equivalent of 16,777,216 colors and 262,144 colors by combination of input data signals, FRC and MSL signals. See the following table.

Combination	Input data signals	Input Data mapping	CN1- Pin No.1 and 2	FRC terminal	MSL terminal	Display colors	Remarks
1	8-bit	MAP A	D3+/-	High	Low or Open	16,777,216	Note1
2	8-bit	MAP B	D3+/-	High	High	16,777,216	Note1
3	6-bit	-	GND	Low or Open	Low or Open	262,144	Note2

Note1: See "**4.6.2 16,777,216 colors**". Note2: See "**4.6.3 262,144 colors**".



4.6.2 16,777,216 colors

This product can display equivalent of 16,777,216 colors with 256 gray scales by combination ① or ②. (See "**4.6.1 Combinations of input data signals, FRC and MSL signal**".)

Also the relation between display colors and input data signals is as follows.

Display	colors	Data signal (0: Low level, 1: High leve						evel) B7 B6 B5 B4 B3 B2 B1 B0																	
Display	COIOIS	R7	R6	R5	R4	R3	R2	R1	R0	G7	' G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	B4	В3	B2	В1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
lors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Basic Colors	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
sic	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
$\mathbf{B}_{\mathbf{a}}$	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ay :	<b>↑</b>				:	:								:								:			
Red gray scale	$\downarrow$				:	:								:								:			
Rec	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
SC	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
gray	<b>↑</b>				:	:								:								:			
Green gray scale	$\downarrow$				:	:								:								:			
Greć	bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	<i>a</i>	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
le		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale	<b>↑</b>				:	:								:								:			
.e	$\downarrow$			_	:	:	_		_			_		:		_								_	
Blu	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



4.6.3 262,144 colors

This product can display 262,144 colors with 64 gray scales by combination ③. (See "**4.6.1 Combinations of input data signals, FRC and MSL signal**".)

Also the relation between display colors and input data signals is as follows.

Display colors							Data	a sign					ligh le	evel)					
Dispiay	7 COIOIS	R 5	R4	R3	R 2	R 1	R0	G5	G4	G3	G2	G1	G0	B 5	B4	В3	B 2	B 1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
ors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Basic colors	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
ısic	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Be	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ပ		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
ay s	$\uparrow$				:					:							:		
l gr	$\downarrow$				:					:							:		
Rec	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
scs	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green gray scale	$\uparrow$				:					:							:		
g u	$\downarrow$				:					:							:		
ìree	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
scal	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale	$\uparrow$				:					:							:		
rg e	$\downarrow$				:					:							:		
Blue	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



### 4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0,	0) B					
C(0, 0)	C( 1, 0)	• • •	C( X, 0)	• • •	C(1022, 0)	C(1023, 0)
C(0, 1)	C( 1, 1)	• • •	C( X, 1)	• • •	C(1022, 1)	C(1023, 1)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	• • •
•	•	•	•	•	•	•
C( 0, Y)	C( 1, Y)	• • •	C( X, Y)	• • •	C(1022, Y)	C(1023, Y)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	•
•	•	•	•	•	•	•
C(0, 766)	C(1, 766)	• • •	C(X, 766)	• • •	C(1022, 766)	C(1023, 766)
C( 0, 767)	C( 1, 767)	• • •	C( X, 767)	• • •	C(1022, 767)	C(1023, 767)

### 4.8 SCANNING DIRECTIONS

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The following figures are seen from a front view.

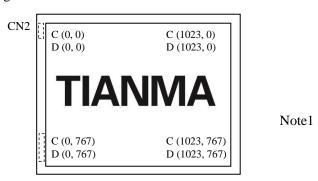


Figure 1. Normal scan (DPS: Low or Open)

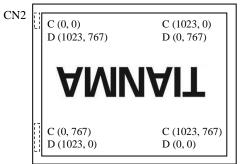


Figure 2. Reverse scan (DPS: High)

Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "4.7 DISPLAY POSITIONS".) D (X, Y): The data number of input signal for LCD panel signal processing board

Note1

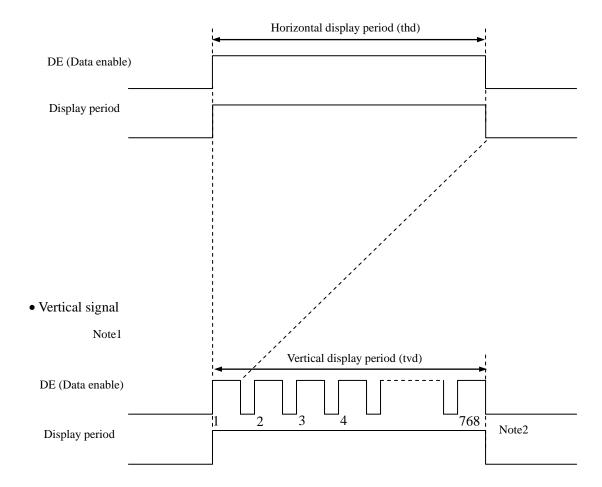


### 4.9 INPUT SIGNAL TIMINGS

# 4.9.1 Outline of input signal timings

• Horizontal signal

Note1



Note1: This diagram indicates virtual signal for set up to timing. Note2: See "**4.9.3 Input signal timing chart**" for the pulse number.



## 4.9.2 Timing characteristics

(Note1, Note2, Note3)

Parameter			Symbol	min.	typ.	max.	Unit	Remarks
	Frequency		1/tc	60.0	65.0	68.0	MHz	15.385 ns (typ.)
CLK	Du	ty ratio	-				-	
	Rise tim	ne, Fall time	-				ns	-
	CLK-DATA	Setup time	1				ns	
DATA	CLK-DAIA	Hold time		-			ns	-
	Rise tim	ne, Fall time	1				ns	
	Horizontal	Cycle	th	19.67	20.676	22.4	μs	48.363 kHz (typ.)
		Сусіе	un	-	1,344	1	CLK	46.303 KHZ (typ.)
		Display period	thd		1,024		CLK	-
		Cycle	tv	13.3	16.666	18.5	ms	60.0Hz (typ.)
DE	Vertical (One frame)	Cycle	ιν	780	806	1	Н	00.0Hz (typ.)
	(One frame)	Display period	tvd	768			Н	-
	CLK-DE	Setup time	1			ns		
	CLK-DE	Hold time	1	-			ns	-
Rise time, Fall time		ne, Fall time	-				ns	

Note1: Definition of parameters is as follows.

tc= 1CLK, th= 1H

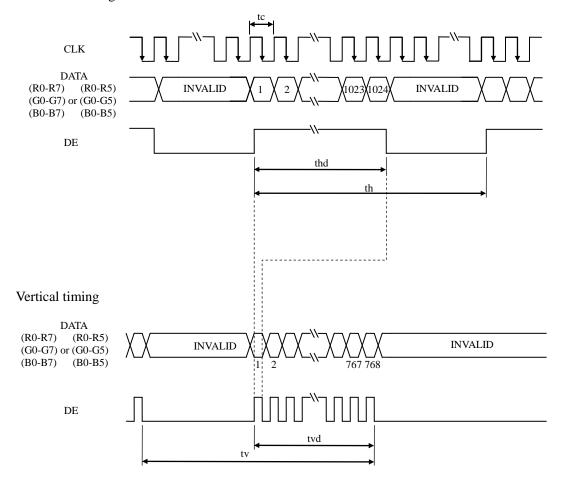
Note2: See the data sheet of LVDS transmitter.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).



# 4.9.3 Input signal timing chart

# Horizontal timing





### **4.10 OPTICS**

## 4.10.1 Optical characteristics

(Note1, Note2)

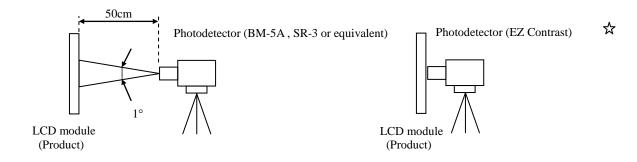
Parameter		Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks	
Luminance		White at center $\theta R = 0^{\circ}$ , $\theta L = 0^{\circ}$ , $\theta U = 0^{\circ}$ , $\theta D = 0^{\circ}$	L	230	400	-	cd/m <sup>2</sup>	BM-5A or equivalent	-	
Contrast ratio		White/Black at center $\theta R = 0^{\circ}$ , $\theta L = 0^{\circ}$ , $\theta U = 0^{\circ}$ , $\theta D = 0^{\circ}$	CR	400	900	1	-	BM-5A or equivalent	Note3	
Luminance uniformity		White $\theta R = 0^{\circ}$ , $\theta L = 0^{\circ}$ , $\theta U = 0^{\circ}$ , $\theta D = 0^{\circ}$	LU	-	1.25	1.4	-	BM-5A or equivalent	Note4	
	White	x coordinate	Wx	0.263	0.313	0.363	-			
	White	y coordinate	Wy	0.279	0.329	0.379	-			
	Red	x coordinate	Rx	-	0.543	-	-			
Cl		y coordinate	Ry	-	0.337	-	-			
Chromaticity	Green	x coordinate	Gx	-	0.357	-	-	SR-3 or	Note5	
		y coordinate	Gy	-	0.547	-	-	equivalent Not		☆
	Dl	x coordinate	Bx	-	0.156	-	-			
	Blue	y coordinate	By	-	0.124	-	-			
Color gamut		$\theta$ R= 0°, $\theta$ L= 0°, $\theta$ U= 0°, $\theta$ D= 0° at center, against NTSC color space	С	35	40	-	%			
Dagmanga	tima	White to Black	Ton	-	3	5	ms	BM-5A or	Note6	
Response	ume	Black to White	Toff	-	15	21	ms	equivalent	Note7	
	Right	θU= 0°, θD= 0°, CR≥ 10	θR	70	80	-	0			
<b>1</b> 77	Left	$\theta$ U= 0°, $\theta$ D= 0°, CR $\geq$ 10	θL	70	80	-	0	EZ	N O	
Viewing angle	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θU	70	80	-	0	Contrast Note		
	Down	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θD	70	80	-	0			

Note1: These are initial characteristics.

Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 60mA/one circuit, Display mode: XGA, Horizontal cycle= 1/48.363kHz, Vertical cycle= 1/60.0Hz, DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation 20minutes after the product works in the dark room. Also measurement methods are as follows.



Note3: See "4.10.2 Definition of contrast ratio".

Note4: See "4.10.3 Definition of luminance uniformity".

Note5: These coordinates are found on CIE 1931 chromaticity diagram.

Note6: Product surface temperature: TopF= 28°C Note7: See "**4.10.4 Definition of response times**".

Note8: See "4.10.5 Definition of viewing angles".



#### 4.10.2 Definition of contrast ratio

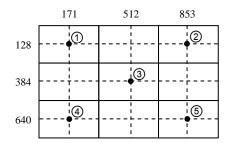
The contrast ratio is calculated by using the following formula.

### 4.10.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

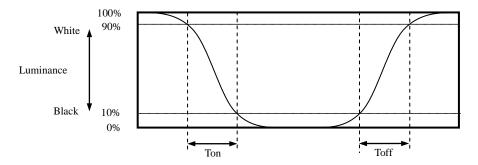
$$Luminance \ uniformity \ (LU) = \ \frac{Maximum \ luminance \ from \ \textcircled{1} \ to \ \textcircled{5}}{Minimum \ luminance \ from \ \textcircled{1} \ to \ \textcircled{5}}$$

The luminance is measured at near the 5 points shown below.

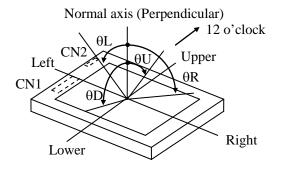


### 4.10.4 Definition of response times

Response time is measured at the time when the luminance changes from "white "to "black", or "black "to "white "on the same screen point, by photo-detector. Ton is the time when the luminance changes from 90% down to 10%. Also Toff is the time when the luminance changes from 10% up to 90% (See the following diagram.).



### 4.10.5 Definition of viewing angles





### 5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

# This lifetime is the estimated value, and is not guarantee value.

	Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit	
LED along out on substance	25°C (Ambient temperature of the product) Continuous operation, IL= 60mA/one circuit	70,000	h
LED elementary substance	80°C (Surface temperature at screen) Continuous operation, IL= 60mA/one circuit		h

Note1: Life time expectancy is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for LCD module but the value for LED elementary substance.

Note3: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.

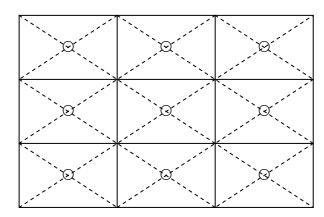


### 6. RELIABILITY TESTS

Test item	Condition	Judgment Note1		
High temperature and humidity (Operation)	① 60 ± 2°C, RH= 90%, 240hours Note3, Note4 ② Display data is black.			
High temperature (Operation)	① 80 ± 3°C, 240hours Note3, Note4 ② Display data is black.			
Heat cycle (Operation)	<ul> <li>① -30 ± 3°C1hour 80 ± 3°C1hour Note3, Note4</li> <li>② 50cycles, 4 hours/cycle</li> <li>③ Display data is black.</li> </ul>			
Thermal shock (Non operation)				
ESD (Operation)	② 0 places on a panel surface Note?			
Dust (Operation)	(2) 15 seconds stir			
Vibration (Non operation)	No display malfunctions			
Mechanical shock (Non operation)	No physical damages			

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.



Note3: Measured at LCD panel surface (including self-heat)

Note4: Measured at LCD module's rear shield surface (including self-heat)



#### 7. PRECAUTIONS

### 7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS"!** 



This sign has the meaning that a customer will be injured or the product will sustain damage if the customer practices wrong operations.



This sign has the meaning that a customer will be injured if the customer practices wrong operations.

### 7.2 CAUTIONS



\* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 539m/s<sup>2</sup> and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (φ16mm jig))

# 7.3 ATTENTIONS



# 7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.
- 3 When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ⓐ The torque for product mounting screws must never exceed 0.147 N·m. Higher torque might result in distortion of the bezel. And the length of product mounting screws must be ≤ 2.0mm.
- ⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- **⑤** Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ② Do not push or pull the interface connectors while the product is working.
- When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- (9) Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.



#### 7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left under the environment of an unpacking room. Evaluate the storage time sufficiently because dew condensation is affected by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with the original packing state after a customer receives the package)
- 3 Do not operate in high magnetic field. If not, circuit boards may be broken.
- ④ This product is not designed as radiation hardened.

#### 7.3.3 Characteristics

### The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flickering, vertical streams or tiny spots may be observed depending on display patterns.
- 3 Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.

### 7.3.4 Others

- ① All GND and VCC terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
- 4 Pack the product with the original shipping package, in order to avoid any damages during transportation, when returning the product to TMJ for repairing and so on.
- ⑤ The information of China RoHS (II) six hazardous substances or elements in this product is as follows.

	China RoHS (II) six hazardous substances or elements								
Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr VI)	Polybrominated Biphenys (PBB)	Polybrominated Biphenyl Ethers (PBDE)				
×	0	0	0	0	0				

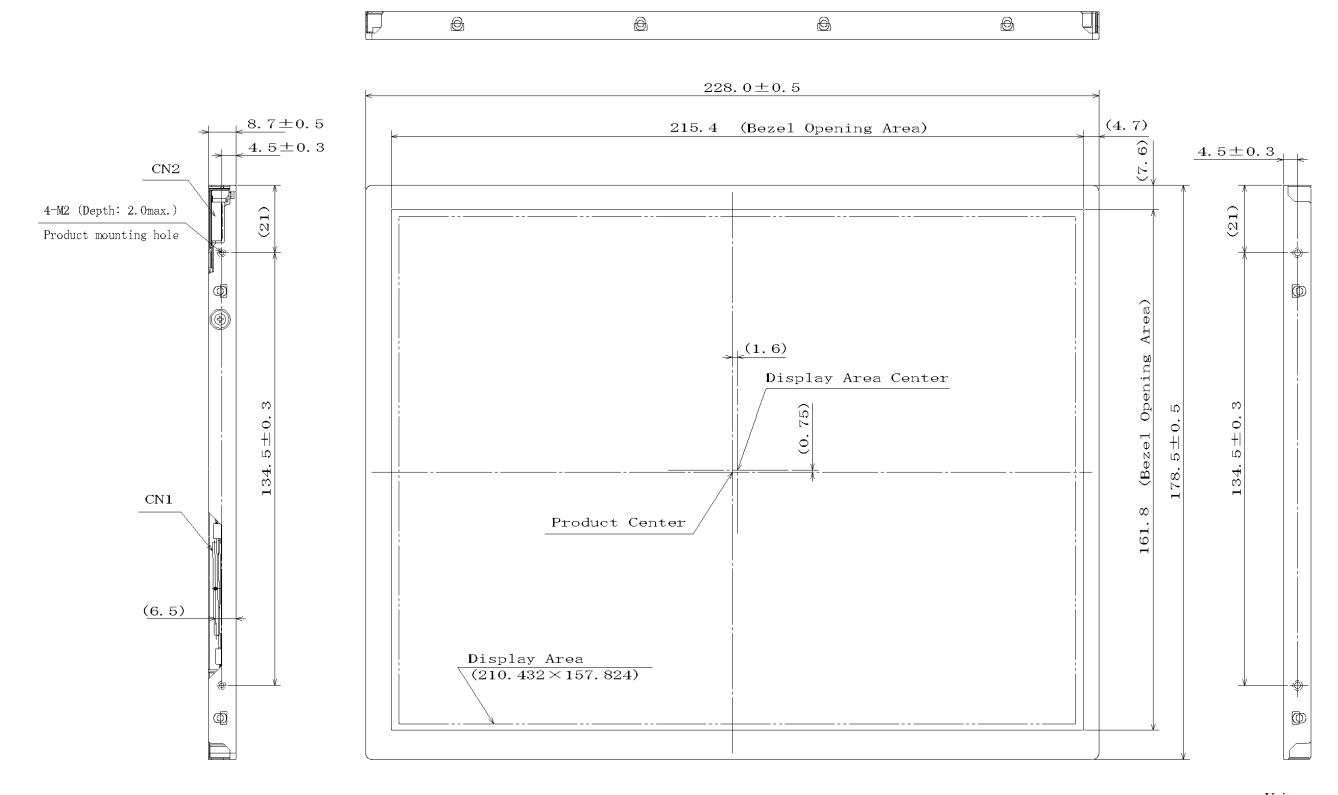
Note1: (): This indicates that the poisonous or harmful material in all the homogeneous materials for this part is equal or below the limitation level of GB/T26572-2011 standard regulation.

X: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is above the limitation level of GB/T26572-2011 standard regulation.



### 8. OUTLINE DRAWINGS

# 8.1 FRONT VIEW



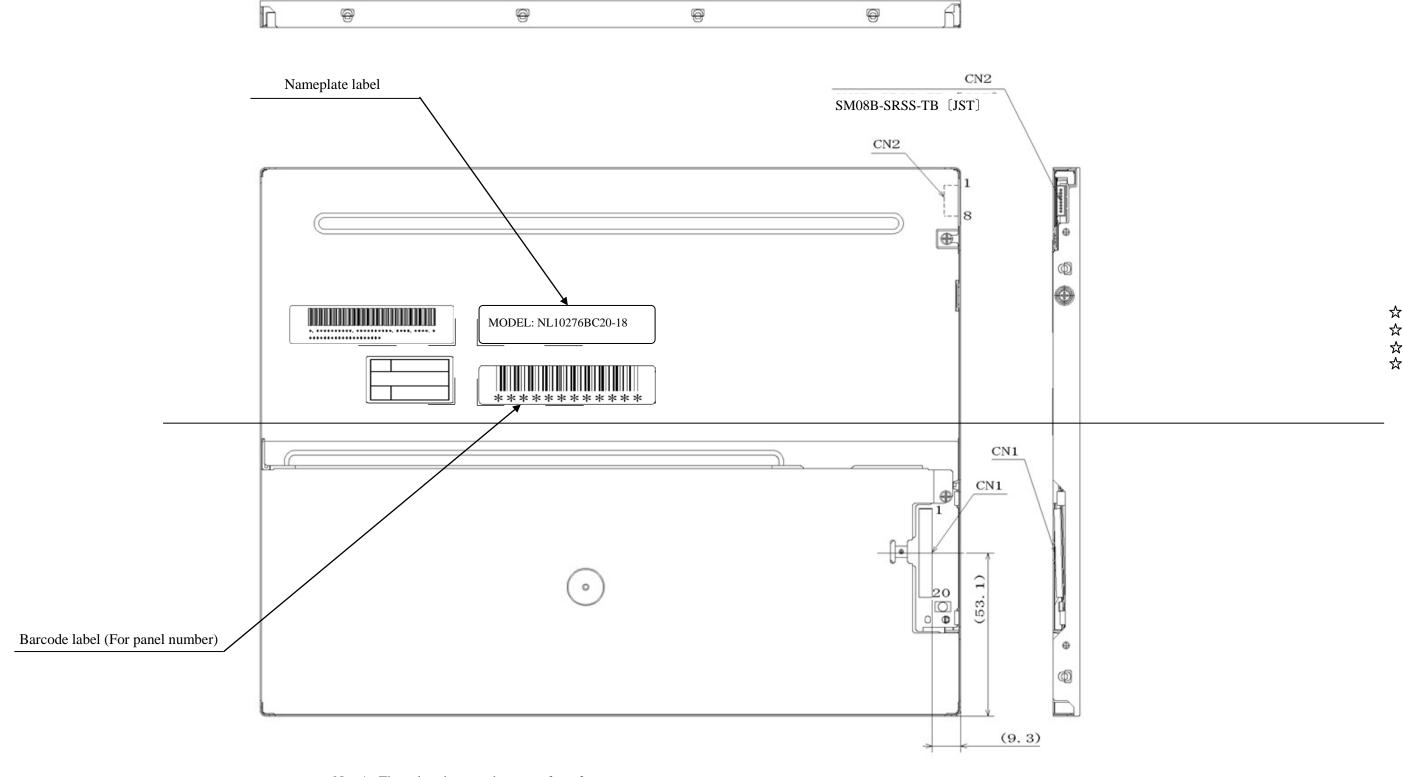
Note1: The values in parentheses are for reference.

Note2: The torque for product mounting screws must never exceed 0.147 N·m. And the length of product mounting screws must be  $\leq$  2.0mm.

Unit: mm



8.2 REAR VIEW



Note1: The values in parentheses are for reference.

Unit: mm

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