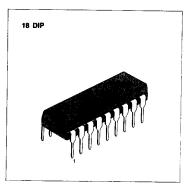
TONE/PULSE DIALER WITH REDIAL

The KS5820 is a DTMF/PULSE switchable dialer with a 32-digit redial memory. Through pin selection, switching from pulse to DTMF mode can be done using a slide switch. All necessary dual-tone frequencies are derived from a 3.579545MHz TV crystal, providing very high accuracy and stability. The required sinusoidal wave form for each individual tone is digitally synthesized on the chip. The wave form so generated has very low total harmonic distortion (7% Max). A voltage reference is generated on the chip which is stable over the operating voltage and temperature range and regulates the single levels of the dual tone to meet telephone industry specifications. CMOS technology is applied to this device, for very low power requirements high noise immunity, and easy interface to a variety of telephones requiring external components.



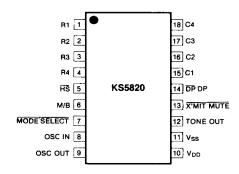
FEATURES

- Tone/Pulse switchable (slide switch).
- · 32 digit capacity for redial
- Automatic mix redialing (last number dial) of PULSE→DTMF with multiple auto access pause
- PABX auto-pause for 3.5 sec.
- 4 × 4 or (2 of 8) keyboard available
- Low power CMOS process (2.0 to 5.5V)
- Numbers dialed manually after redial are cascadable and stored as additional numbers for next redialing
- Uses inexpensive TV crystal (3.579545MHz)
- Make/Break ratio (33 1/3 ~ 66 2/3 or 40/60) pin selectable
- Touch key hooking (604ms)
- Low standby current

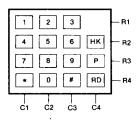
ORDERING INFORMATION

Device	Dial Pulse	PPS	Make/Break Ratio
KS58A20N	DP	10	V _{DD} : 33.3/66.6
N558AZUN	DP	10	V _{ss} : 40/60
L'OCODON!	DP	20	V _{DD} : 33.3/66.6
KS58B20N			V _{ss} : 40/60
W050000N	DΡ		V _{DD} : 33.3/16.6
KS58C20N	DP	10	V _{ss} : 40/60
W0500001	55		· V _{DD} : 33.3/66.6
KS58D20N	DP	20	V _{ss} : 40/60

PIN CONFIGURATION



ARRANGEMENT OF KEYBOARD

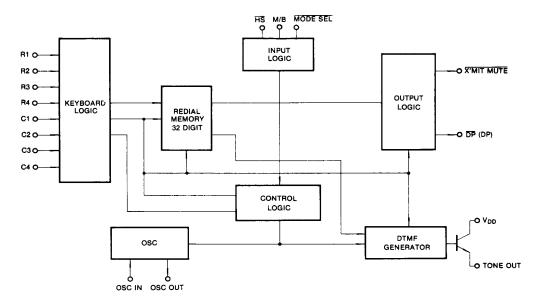


HK : HOOKING (604ms)
P : PAUSE (3.5 second)

RD : REDIAL



BLOCK DIAGRAM



TONE DURATION & PAUSE IN REDIAL

Characteristic	Symbol	Тур	Unit
Tone Duration	Τ _D	110	mS
Minimum Pause	ITP	110	mS
Cycle Time	Tc	220	mS

TONE FREQUENCIES

Input	Specified	Actual	% Error
R1	697	699.1	+ 0.31
R2	770	766.2	- 0.49
R3	852	847.4	- 0.54
R4	941	948.0	+ 0.74
C1	1209	1215.7	+ 0.57
C2	1336	1331.7	- 0.32
С3	1477	1471.9	- 0.35



ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Characteristics	Symbol	Value	Unit
Supply Voltage	· V _{DD}	6.0	V
Input Voltage	V _{IN}	$V_{SS} - 0.3, V_{DD} + 0.3$	
Output Voltage	V _{out}	$V_{SS} - 0.3, V_{DD} + 0.3$	V
Output Voltage	V _{out}	≦ V _{DD} ,(DP, X'MITMUTE)	٧
Tone Output Current	I _{TONE}	50	mA
Power Dissipation	P₀	500	mW
Operating Temperature	T _{opr}	- 20 ~ + 70	
Storage Temperature	T _{stg}	- 40 ~+ 125	°C

ELECTRICAL CHARACTERISTICS

 $(V_{SS} = 0V, V_{DD} = 3.5V, fx'tal = 3.579545MHz, Ta = 25^{\circ}C, unless otherwise noted)$

Characteristic	Symbol	Test Conditions		Min	Тур	Max	Unit		
Operating Voltage	V _{DDP}	Pulse Mo	de	All inputs connected to V _{DD} or V _{SS}		2.0		5.5	v
Range	V _{DDT}	Tone Mod	de			2.0		5.5	
Memory Retention Voltage	V _{DR}					1.0			
Operating Supply Courses	perating Supply Current		-		0.3	0.5	mA		
Operating Supply Current					0.5	1.0			
Standby Current	I _{SD1}	HS = V _{DD} = 1	1.5V	No key selected. All outputs unloaded				0.05	μΑ
Standby Current	I _{SD2}	HS = V _{ss}						50	
Output Current	I _{OD1}	ĎΡ	Ī.,	$= 0.4V$ $V_{DD} = 3.5V$ $V_{DD} = 2.5V$		1.7	5.0		mA
	I _{OD2}	X'MIT MUTE	V _{OL} =			0.5	1.5		
Input Voltage	V _{IH}	R1-R4. C1-C3. HS. M/B		0.8V _{DD}		V _{DD}	T		
mput voltage	VIL	M	ODE S	SELECT		Vss		0.2V _{DD}	\ \ \
Input Current	I _{IN1}	$V_{DD} = 3.5V V_{1N}$	V _{IN} = 0V				116	1.	
	I _{IN2}	$V_{DD} = 2.5V V_{IN}$	= 0V	R1-R4				50	μΑ
Valid Key Entry Time	T _{kd}					23		25.3	mS
Column and Row Scanning Frequency	F _{cr}				437		Hz		
Auto Access Pause Time	T _{ap}					3.5		sec	
Tone Output		ROW TONE	Vo	_{DD} = 2.5V R _L = 5K		- 16.0		- 12.0	Ť
	V _{or}	ONLY $V_{DD} = 3.5V R_L = 5K$		- 14.0		- 11.0	dBV		
Ratio of Column to Row Tone	dB _{cr}	V _{DD} = 3.5V		1.0	2.0	3.0	dB		
Distortion	%DIS	V _{DD} = 3.5V					7	%	
Tone Output Delay Time	T _{psd}				1.5		mS		



PIN DESCRIPTION

Pin	Name	Description					
1-4 15-18	R1-R4 C1-C4	These C1-C4 to low input of signal input	inputs can be & R1-R4 are and R1-R4 are operation. The s are presented key is release	set to low at O e set to high at oscillator starts ed at both colu d. Key inputs a	an XY matrix keyboard In Hook (HS = high). C1 OFF HOOK (HS = low)	-C4 key inputs are set which enables the key- ss is detected. Scanning (P: 437Hz) until the andard 2-of-8 form or	
5	ĦŜ	This in	Hook Switch This input detects the state of the hook switch contact. "Off Hook" corresponds to V_{SS} condition. "On Hook" corresponds to V_{DD} condition.				
6	M/B	This in		the selection of the to V _{DD} /V _{SS}	of the Make/Break ratio	(33.3: 66.6/40:60)	
7	MODE SELECT	Pulse/			shown in the following going Off Hook (HS→		
			MODE SELECT	INITIAL MODE	SWITCHING ENTRY MODE		
			V _{DD}	Pulse	MODE SELECT Input = V _{SS}		
			V _{ss}	Tone	N/A		
8-9	OSC IN OSC OUT	These	Oscillator Input/Output These pins are provided to connect an external 3.58MHz crystal. Oscillator starts (at Off Hook) and is sustained until pulse or DTMF signals are finished.				
10-11	V _{DD} , V _{SS}			r supply inputs	s. The device is design	ed to be operated on	
12	TONE OUT	When group	frequencies a	ess is detected	in DTMF mode approposition hybrid the Dual T		
13	X'MIT MUTE	X'mit	Mute Output				
			HS	X'mit	Mute Output		
			V _{DD}	"ON"			
			V _{ss}		mally "OFF" rulse and DTMF dialing		
			(N channel of	oen drain)			
14	DP, DP	DP: Th	OFF HOOK." ne normal out	The output will put will be "Ol	FF" during break and " be "ON" at "ON HOO N" during break and "C be "OFF" at "ON HOO	K," PFF" during make at	



KEYBOARD OPERATION

1. SINGLE MODE OPERATION

Pulse Mode Operation

Pulse mode is defined by the initial mode after going Off Hook and latched at D1 key entry. This is the condition under Mode Select = V_{DD}.

• Tone Mode Operation



Tone mode is defined by the initial mode after going Off Hook and latched at $\boxed{\text{D1}}$ key entry. This condition is under $\boxed{\text{Mode Select}} = V_{\text{SS}}$.

• Manual Dialing with Automatic Access Pause



Multiple Pause key entries can be accepted and stored in the redial memory, each as a digit. Each P key provides 3.5 seconds pause time, but the P key entry as the first digit after going Off Hook is ignored. The key can also be used as a pause key in the pulse mode. Pause (s) can be the cancelled with the P or RD key during pause time in redialing.

D = Any numeric key.

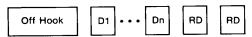
Redialing



Up to 32 digits can be dialed with the RD key. The RD key is disabled while pulse or DTMF signals are transmitted. When more then 32 digits are stored, redial is also inhibited.

The # key can be used as the RD key in the pulse mode.

Inhibiting Redial

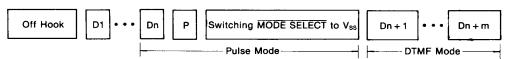


Redial can be inhibited by depressing the RD key twice after DTMF or pulse signals are transmitted.

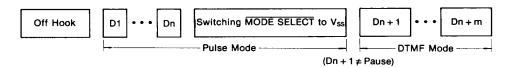


2. PULSE/TONE SWITCHABLE OPERATION

• Mode Switching by MODE SELECT Input



The pulse mode is initially defined $\overline{\text{MODE SELECT}} = V_{DD}$. Mode switching to the DTMF mode can be accepted by $\overline{\text{MODE SELECT}} = V_{SS}$, the DTMF mode will be set up after the pulse mode is finished. In this mode, digits $\overline{Dn+1}$... $\overline{Dn+m}$ are transmitted from Tone Out as DTMF signals by depressing the corresponding keys. If no \overline{P} key is contained serially before or after mode switching, the following condition is obtained.



If digit $\overline{Dn+1}$ is depressed after the pulse mode is finished, the DTMF mode will be set up after the last pulse signal (\overline{Dn}) is generated. In this mode, digits $\overline{Dn+1}$... $\overline{Dn+m}$ are transmitted from Tone Out as DTMF signals by depressing the corresponding keys. If digit $\overline{Dn+1}$ is depressed during dialing pulse signals, DTMF mode, in the Hold State, will be set after the last pulse signal \overline{Dn} is finished. MODE OUT will flash to indicate this Hold State $\overline{Dn+1}$... $\overline{Dn+m}$ is stored in the redial memory as DTMF data and not transmitted from Tone Out. When it is ready to transmit the DTMF data in redial memory, the \overline{T} , \overline{RD} or \overline{P} key is depressed to reset this Hold State and $\overline{Dn+1}$... $\overline{Dn+m}$ data are serially transmitted.

Single Tone Operation in DTMF Mode (Test mode)

The M/B pin is used to trigger the chip into the test mode by applying a positive or negative pulse after "Off
Hook." The test mode is sustained until On Hook. The single tone is shown in the following table which contrast with the normal mode.

Normal mode

R1	1	2	3
R2	4	5	6
R3	7	8	9
R4	*	0	#
	C1	C2	C3

Single tone mode

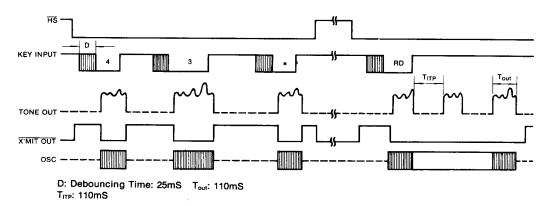
R1	R1	C2	СЗ
R2	C1	C2	R2
R3	R3	C2	СЗ
R4	C1	R4	СЗ
	C1	C2	C3

The single tone can be generated by simultaneously depressing two digit keys in the appropriate Row and Column. If tthe wo digit keys are not in the same Row or Column, the dual tone is disabled and no output is provided.

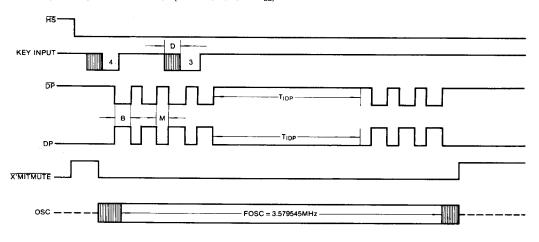


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TONE MODE TIMING (MODE SELECT = Vss)



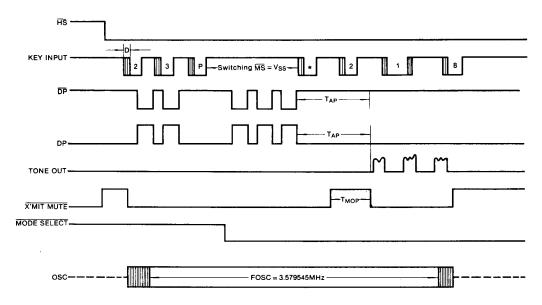
PULSE MODE TIMING (MODE SELECT = VDD)



D: Debouncing Time: 25mS T_{IDP}: Inter Digit Pause: 879mS



TIMING DIAGRAM (for Switching Mode Operation by MODE SELECT Input)



TAP: Auto Pause Time 3.5sec