



Ultrahigh-Speed Switching Applications

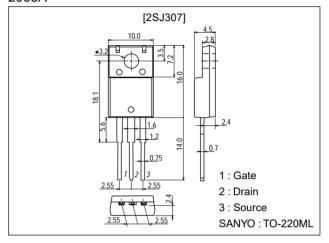
Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.
- · Micaless package facilitating mounting.

Package Dimensions

unit:mm

2063A



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-250	V
Gate-to-Source Voltage	V _{GSS}		±30	V
Drain Current (DC)	I _D		-6	Α
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	-24	Α
Allowable Power Dissipation	PD		2.0	W
		Tc=25°C	30	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

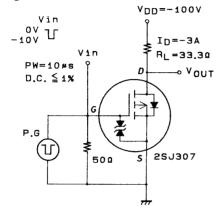
Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	I _D =-1mA, V _{GS} =0	-250			V
Gate-to-Source Breakdown Voltage	V _{(BR)GSS}	I _G =±100μA, V _{DS} =0	±30			V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-250V, V _{GS} =0			-100	μA
Gate-to-Souce Leakage Current	I _{GSS}	V _{GS} =±25V, V _{DS} =0			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =-10V, I _D =-1mA	-1.5		-2.5	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-3A	3	5		S
Static Drain-to-Source On-State Resistance	RDS(on)	I _D =-3A, V _{GS} =-10V		0.75	1.0	Ω

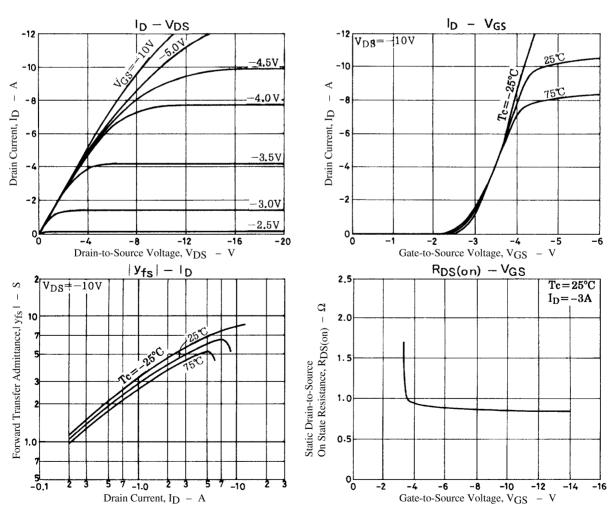
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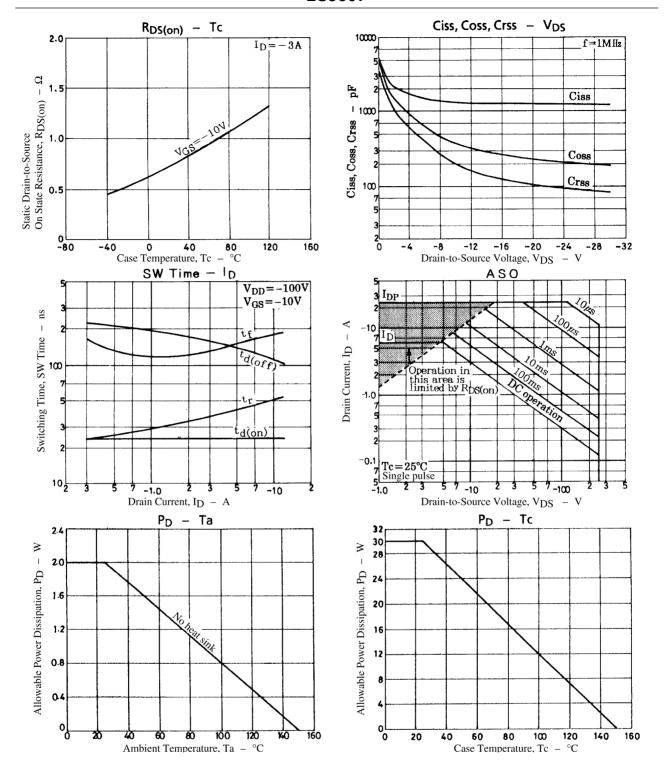
Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Office
Input Capacitance	Ciss	V _{DS} =–20V, f=1MHz		1250		pF
Output Capacitance	Coss	V _{DS} =–20V, f=1MHz		235		pF
Reverse Transfer Capacitance	Crss	V _{DS} =–20V, f=1MHz		105		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		24		ns
Rise Time	t _r	See specified Test Circuit		37		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		155		ns
Fall Time	t _f	See specified Test Circuit		130		ns
Diode Forward Voltage	V _{SD}	I _S =-6A, V _{GS} =0		-1.0	-1.5	V

Switching Time Test Circuit









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