

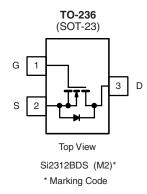
N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
	0.031 at V _{GS} = 4.5 V	5.0			
20	0.037 at V _{GS} = 2.5 V	4.6	7.5		
	0.047 at V _{GS} = 1.8 V	4.1			

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si2312BDS-T1-E3 (Lead (Pb)-free) Si2312BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted Symbol **Parameter** 5 s **Steady State** Unit Drain-Source Voltage V_{DS} ٧ Gate-Source Voltage V_{GS} ± 8 $T_A = 25$ °C 5.0 3.9 Continuous Drain Current (T_J = 150 °C)^a I_D T_A = 70 °C 4.0 3.1 Α Pulsed Drain Current^b I_{DM} 15 Avalanche Current^b 13 I_{AS} L = 0.1 mHSingle Avalanche Energy 8.45 E_{AS} mJ Continuous Source Current (Diode Conduction)^a 1.0 0.63 Α I_S $T_A = 25 \, ^{\circ}C$ 1.25 0.75 Power Dissipation^a P_{D} W $T_A = 70 \, ^{\circ}C$ 0.80 0.48 T_J , T_{stg} °С - 55 to 150 Operating Junction and Storage Temperature Range

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	t ≤ 5 s	R _{thJA}	80	100	°C/W	
waximum Junction-to-Ambient	Steady State		120	166		
Maximum Junction-to-Foot	Steady State	R _{thJF}	50	60		

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. Pulse width limited by maximum junction temperature.

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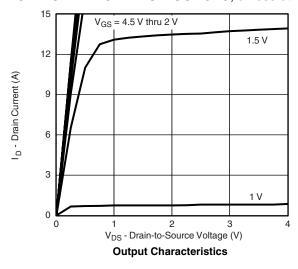
SPECIFICATIONS T _A = 25 °C, unless otherwise noted							
			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.45		0.85	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zoro Cata Valtaga Drain Current		V _{DS} = 20 V, V _{GS} = 0 V			1	μΑ	
Zero Gate Voltage Drain Current	DSS	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			75		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 4.5 \text{ V}$	15			Α	
		$V_{GS} = 4.5 \text{ V}, I_D = 5.0 \text{ A}$		0.025	0.031	Ω	
Drain-Source On-Resistance ^a	R _{DS(on)}	V _{GS} = 2.5 V, I _D = 4.6 A		0.030	0.037		
	(,	V _{GS} = 1.8 V, I _D = 4.1 A		0.036	0.047		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 5.0 A		30		S	
Diode Forward Voltage	V_{SD}	I _S = 1.0 A, V _{GS} = 0 V		0.8	1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			7.5	12	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 5.0 \text{ A}$		1.4			
Gate-Drain Charge	Q_{gd}			1.2			
Gate Resistance	R_{g}	f = 1.0 MHz	1.1	2.2	3.3	Ω	
Switching							
Turn-On Delay Time	t _{d(on)}			9	15		
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_{I} = 10 \Omega$		30	45	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1.0 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$		35	55		
Fall Time	t _f			10	15		
Source-Drain Reverse Recovery Time	t _{rr}	1 100 11/14 100 0/		13	25		
Body Diode Reverse Recovery Charge	Q _{rr}	$I_F = 1.0 \text{ A, dI/dt} = 100 \text{ A/}\mu\text{s}$		4.5	7	nC	

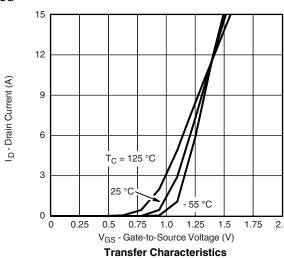
Notes

- a. Pulse test: Pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



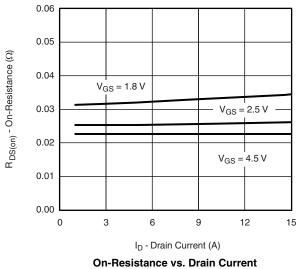


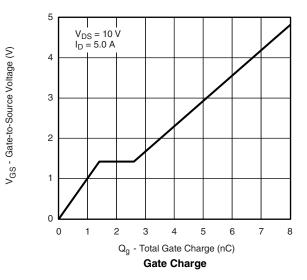


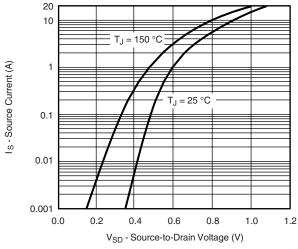




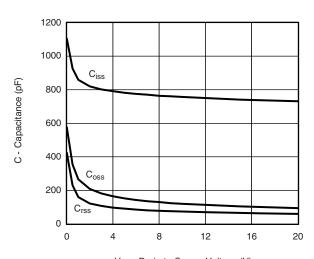
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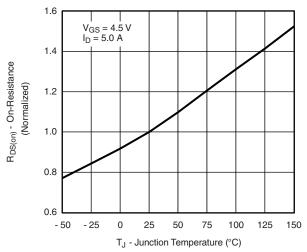


Source-Drain Diode Forward Voltage

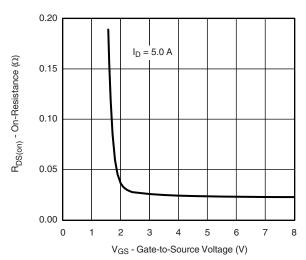


V_{DS} - Drain-to-Source Voltage (V)





On-Resistance vs. Junction Temperature

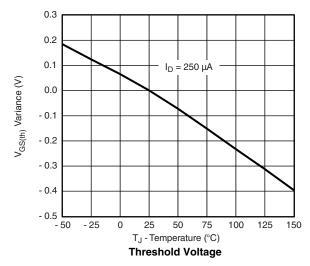


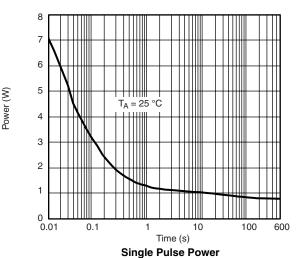
On-Resistance vs. Gate-to-Source Voltage

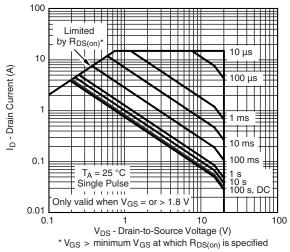
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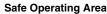
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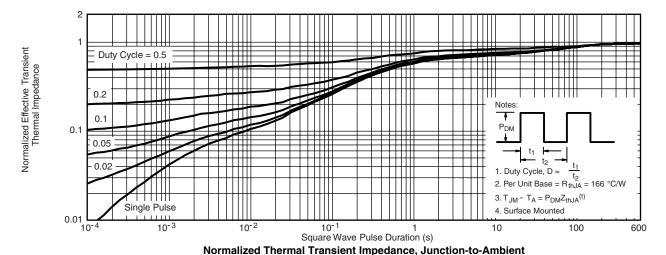
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted











Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppq?73235.

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SOT-23 (TO-236): 3-LEAD







Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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