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November 2013

## **FQB5N90**

## N-Channel QFET® MOSFET

900 V, 5.4 A, 2.3 Ω

### **Description**

This N-Channel enhancement mode power MOSFET is • 5.4 A, 900 V,  $R_{DS(on)}$  = 2.3  $\Omega$  (Max.) @  $V_{GS}$  = 10 V, produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state

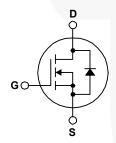
• Low Gate Charge (Typ. 31 nC) resistance, and to provide superior switching performance • Low Crss (Typ. 13 pF) and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power • 100% Avalanche Tested factor correction (PFC), and electronic lamp ballasts.

#### **Features**

- $I_D = 2.7 A$

- · RoHS Compliant





#### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Parameter		FQB5N90TM	Unit	
V <sub>DSS</sub>	Drain-Source Voltage		900	V	
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°C)		5.4	Α	
	- Continuous (T <sub>C</sub> = 100°C)		3.42	Α	
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	21.6	Α	
V <sub>GSS</sub>	Gate-Source Voltage		± 30	V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	660	mJ	
I <sub>AR</sub>	Avalanche Current (Note 1)		5.4	Α	
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	15.8	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.0	V/ns	
P <sub>D</sub>	Power Dissipation (T <sub>A</sub> = 25°C) *		3.13	W	
	Power Dissipation (T <sub>C</sub> = 25°C)		158	W	
	- Derate above 25°C		1.27	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C	
TL	Maximum lead temperature for soldering, 1/8" from case for 5 seconds		300	°C	

#### **Thermal Characteristics**

Symbol	Parameter	FQB5N90TM	Unit
$R_{\thetaJC}$	Thermal Resistance, Junction to Case, Max.	0.79	
В	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	62.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (*1 in <sup>2</sup> Pad of 2-oz Copper), Max.	40	

## **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQB5N90TM	FQB5N90	D <sup>2</sup> -PAK	Tape and Reel	330 mm	24 mm	800 units

#### **Electrical Characteristics**

T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Cha	racteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	900			V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C		1.0		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 900 V, V <sub>GS</sub> = 0 V			10	μΑ
		V <sub>DS</sub> = 720 V, T <sub>C</sub> = 125°C			100	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0 V			100	nA
$I_{GSSR}$	Gate-Body Leakage Current, Reverse	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Cha	racteristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10 V, I <sub>D</sub> =2.7 A		1.8	2.3	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 50 V, I <sub>D</sub> = 2.7 A	-	5.6		S
Dynami	ic Characteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		1200	1550	pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz		110	145	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			13	17	pF
Switchi	ng Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time	V = 450 V I = 5.4.0		28	65	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD} = 450 \text{ V}, I_{D} = 5.4 \text{ A},$ $R_{G} = 25 \Omega$		65	140	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	11G - 20 32		65	140	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4)		50	110	ns
Qg	Total Gate Charge	V <sub>DS</sub> = 720 V, I <sub>D</sub> = 5.4 A,		31	40	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> = 10 V		7.2		nC
Q <sub>gd</sub>	Gate-Drain Charge	(Note 4)		15		nC
Drain-S	ource Diode Characteristics ar	nd Maximum Patings				
I <sub>S</sub>	Maximum Continuous Drain-Source Did				5.4	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current				21.6	A
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 5.4 \text{ A}$			1.4	V
t <sub>rr</sub>	Reverse Recovery Time	$V_{GS} = 0 \text{ V, } I_S = 5.4 \text{ A,}$		610		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F / dt = 100 A/\mu s$		5.26	//	μС

#### Notes

- 1. Repetitive rating : pulse-width limited by maximum junction temperature.
- 2. L = 43 mH, I<sub>AS</sub> = 5.4 A, V<sub>DD</sub> = 50 V, R<sub>G</sub> = 25  $\Omega$ , starting T<sub>J</sub> = 25°C.
- 3. I  $_{SD} \leq$  5.4 A, di/dt  $\leq$  200 A/µs  $\,$  , V  $_{DD} \leq$  BV  $_{DSS,}$  starting  $\,$  T  $_{J}$  = 25°C.
- 4. Essentially independent of operating temperature.

## **Typical Characteristics**

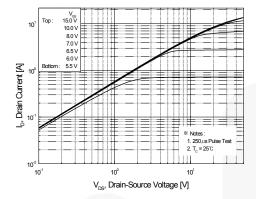


Figure 1. On-Region Characteristics

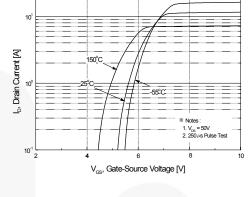


Figure 2. Transfer Characteristics

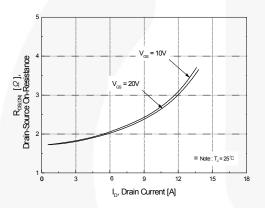


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

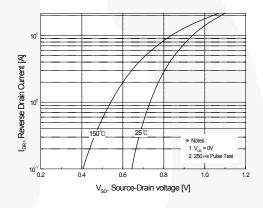


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

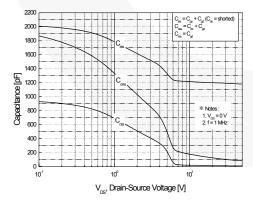


Figure 5. Capacitance Characteristics

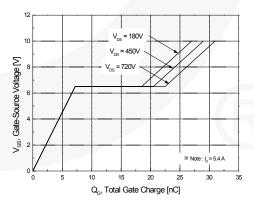


Figure 6. Gate Charge Characteristics

# Typical Characteristics (Continued)

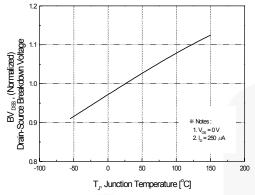


Figure 7. Breakdown Voltage Variation vs. Temperature

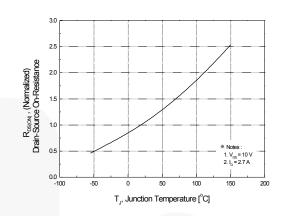


Figure 8. On-Resistance Variation vs. Temperature

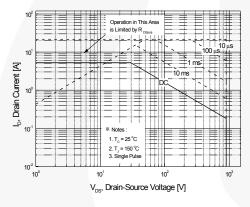


Figure 9. Maximum Safe Operating Area

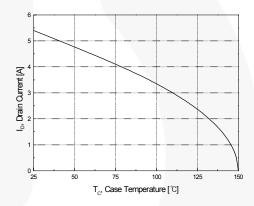


Figure 10. Maximum Drain Current vs. Case Temperature

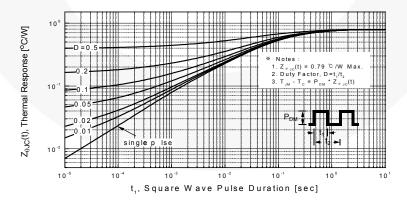


Figure 11. Transient Thermal Response Curve

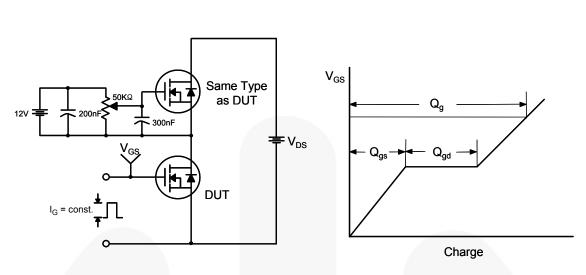


Figure 12. Gate Charge Test Circuit & Waveform

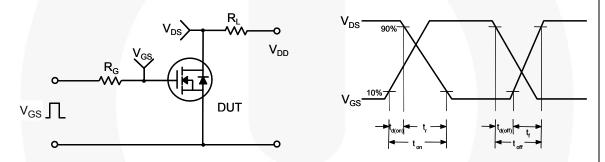


Figure 13. Resistive Switching Test Circuit & Waveforms

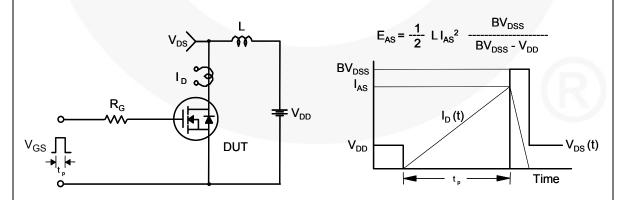
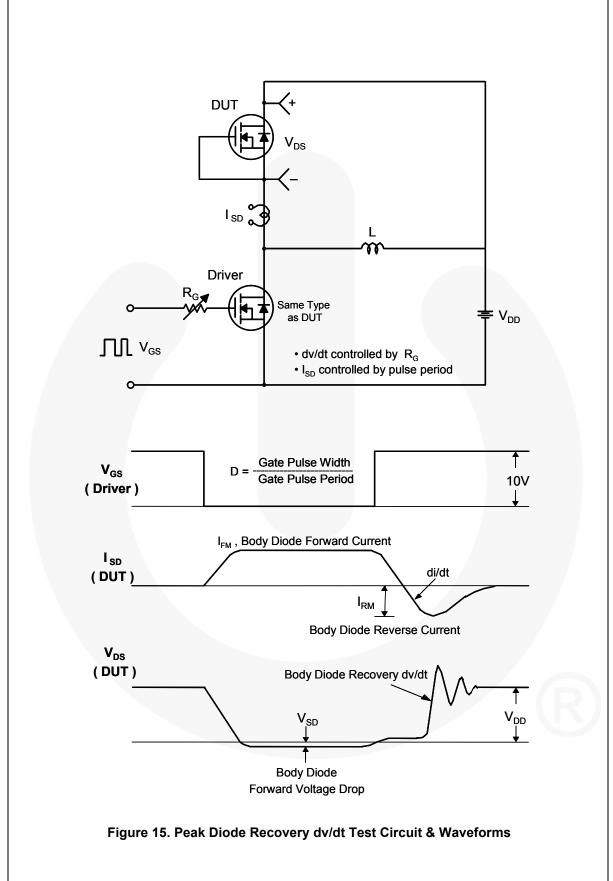


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



#### **Mechanical Dimensions**

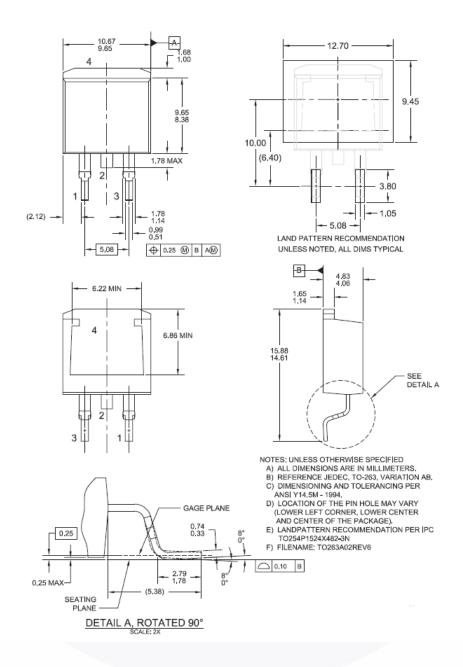


Figure 16. TO263 (D<sup>2</sup>PAK), Molded, 2-Lead, Surface Mount

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