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June 2015



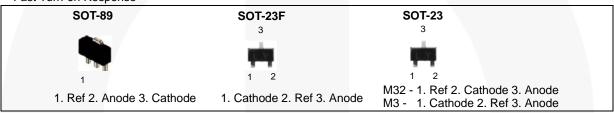
LM431SA / LM431SB / LM431SC Programmable Shunt Regulator

Features

- Programmable Output Voltage to 36 V
- Low Dynamic Output Impedance: 0.2 Ω (Typical)
- Sink Current Capability: 1.0 to 100 mA
- Equivalent Full-Range Temperature Coefficient of 50 ppm/°C (Typical)
- Temperature Compensated for Operation Over Full Rated Operating Temperature Range
- Low Output Noise Voltage
- Fast Turn-on Response

Description

The LM431SA / LM431SB / LM431SC are three-terminal the output adjustable regulators with thermal stability over operating temperature range. The output voltage can be set any value between V_{REF} (approximately 2.5 V) and 36 V with two external resistors. These devices have a typical dynamic output impedance of 0.2 Ω . Active output circuit provides a sharp turn-on characteristic, making these devices excellent replacement for zener diodes in many applications.



Ordering Information

Product Number	Output Voltage Tolerance	Operating Temperature	Top Mark ⁽¹⁾	Package	Packing Method
LM431SACMFX			43A	SOT-23F 3L	
LM431SACM3X	2%		43L	SOT-23 3L	
LM431SACM32X			43G	SOT-23 3L	
LM431SBCMLX			43B	SOT-89 3L	
LM431SBCMFX	1%		43B	SOT-23F 3L	
LM431SBCM3X	1%	-25 to +85°C	43M	SOT-23 3L	Tape and Reel
LM431SBCM32X			43H	SOT-23 3L	Tape and Reel
LM431SCCMLX			43C	SOT-89 3L	
LM431SCCMFX	0.5%		43C	SOT-23F 3L	
LM431SCCM3X	0.5%		43N	SOT-23 3L	
LM431SCCM32X	1		43J	SOT-23 3L	
LM431SAIMFX	2%	-40 to +85°C	43AI	SOT-23F 3L	

Note:

1. SOT-23 and SOT-23F have basically four-character marking except LM431SAIMFX.

(3 letters for device code + 1 letter for date code)

SOT-23F date code is composed of 1 digit numeric or alphabetic week code adding bar-type year code.

> Week code: Change in every two weeks > Year code (additional bar): Rotate in three year cycle

Week	01~02	03~04	05~06	07~08	09~10	11~12	13~14	15~16	17~18	19~20	21~22	23~24	25~26
Code	1	2	3	4	5	6	7	8	9	А	D	Е	F
Week	27~28	29~30	31~32	33~34	35~36	37~38	39~40	41~42	43~44	45~46	47~48	49~50	51~52
Code	Н	J	к	L	N	0	Ρ	R	S	Т	U	V	х

Year
2008
2009
2010
2011
2012
2013
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Block Diagram

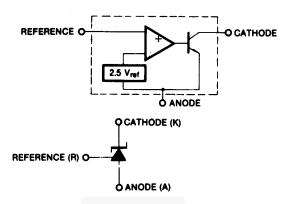


Figure 1. Block Diagram

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	F	Parameter	Value	Unit		
V _{KA}	Cathode Voltage		37	V		
I _{KA}	Cathode current Range (Co	ntinuous)	-100 to +150	mA		
I _{REF}	Reference Input Current Ra	nge	-0.05 to +10.00	mA		
		ML Suffix Package (SOT-89)	220			
$R_{ extsf{ heta}JA}$	R _{0JA} Thermal Resistance Junction-Air ^(2,3)	350	°C/W			
		M32, M3 Suffix Package (SOT-23)	400			
		ML Suffix Package (SOT-89)	560			
PD	Power Dissipation (4,5)	Dissipation ^(4,5) MF Suffix Package (SOT-23F) 350				
		M32, M3 Suffix Package (SOT-23)	310			
ТJ	Junction Temperature		150	°C		
Ŧ	Operating Temperature	All products except LM431SAIMFX	-25 to +85	°c		
T _{OPR}	Range	LM431SAIMFX	-40 to +85			
T _{STG}	Storage Temperature Range	9	-65 to +150	°C		

Notes:

- 2. Thermal resistance test board Size: 1.6 mm x 76.2 mm x 114.3 mm (1S0P) JEDEC Standard: JESD51-3, JESD51-7.
- JEDEC Standard: JESD51-3, JE
- 3. Assume no ambient airflow.
- 4. $T_{JMAX} = 150$ °C; ratings apply to ambient temperature at 25 °C.
- 5. Power dissipation calculation: $P_D = (T_J T_A) / R_{\theta JA.}$

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
V _{KA}	Cathode Voltage	V _{REF}	36	V
I _{KA}	Cathode Current	1	100	mA

LM431SA / LM431SB / LM431SC — Programmable Shunt Regulator

Electrical Characteristics⁽⁶⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Paramatar	Conditions		Parameter Conditions LM431SA			6A	L	M431S	в	LM431SC			Unit
Symbol	Parameter			Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Sint	
V _{REF}	Reference Input Voltage	$V_{KA} = V_{REF}, I_{KA}$	= 10 mA	2.450	2.500	2.550	2.470	2.495	2.520	2.482	2.495	2.508	V	
$\Delta V_{REF} / \Delta T$	Deviation of Reference Input Voltage Over-	$V_{KA} = V_{REF},$ $I_{KA} = 10 \text{ mA}$ $T_{MIN} \le T_A \le$	SOT-89 SOT-23F		4.5	17.0		4.5	17.0		4.5	17.0	mV	
	Temperature	T_{MAX}	SOT-23		6.6	24		6.6	24		6.6	24	mV	
⊿V _{REF} /	Ratio of Change in Reference		⊿V _{KA} = 10 V-V _{REF}		-1.0	-2.7		-1.0	-2.7		-1.0	-2.7		
ΔV _{KA}	Input Voltage to the Change in Cathode Voltage	I _{KA} =10 mA	⊿V _{KA} = 36 V-10 V		-0.5	-2.0		-0.5	-2.0		-0.5	-2.0	mV/V	
I _{REF}	Reference Input Current	$I_{KA} = 10 \text{ mA},$ $R_1 = 10 \text{ K}\Omega, R_2 = \infty$			1.5	4.0		1.5	4.0		1.5	4.0	μΑ	
	Deviation of Reference	$I_{KA} = 10 \text{ mA},$ R ₁ = 10 KΩ,	SOT-89 SOT-23F		0.4	1.2		0.4	1.2		0.4	1.2	μΑ	
⊿I _{REF} / ⊿T	Input Current Over Full Temperature Range	$R_2 = \infty$, $T_A = Full$ Range	SOT-23		0.8	2.0		0.8	2.0		0.8	2.0	μΑ	
I _{KA(MIN)}	Minimum Cathode Current for Regulation	V _{KA} = V _{REF}			0.45	1.00		0.45	1.00		0.45	1.00	mA	
I _{KA(OFF)}	Off -Stage Cathode Current	V _{KA} = 36 V, V _{REF} = 0			0.05	1.00		0.05	1.00		0.05	1.00	μΑ	
Z _{KA}	Dynamic Impedance	$\label{eq:VKA} \begin{split} V_{KA} &= V_{REF}, \\ I_{KA} &= 1 \text{ to } 100 \text{ m} \\ f &\geq 1.0 \text{ kHz} \end{split}$	nA,		0.15	0.50		0.15	0.50		0.15	0.50	Ω	

Note:

6. $T_{MIN} = -25^{\circ}C$, $T_{MAX} = +85^{\circ}C$.

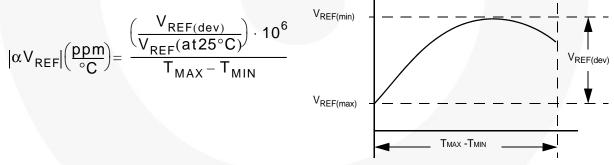
Electrical Characteristics^(7, 8) (Continued)

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions			LM431SAI		
Symbol	Farameter		Min.	Тур.	Max.	Unit	
V _{REF}	Reference Input Voltage	$V_{KA} = V_{REF}, I_{KA} =$: 10 mA	2.450	2.500	2.550	V
V _{REF(dev)}	Deviation of Reference Input Voltage Over- Temperature	$\label{eq:VKA} \begin{split} V_{KA} &= V_{REF}, \ I_{KA} = 10 \ mA, \\ T_{MIN} &\leq T_A \leq T_{MAX} \end{split}$			5	20	mV
	Ratio of Change in		$\Delta V_{KA} = 10 \text{ V} - V_{REF}$		-1.0	-2.7	
$\Delta V_{REF} / \Delta V_{KA}$	Reference Input Voltage to Change in Cathode Voltage	I _{KA} = 10 mA	⊿V _{KA} = 36 V - 10 V		-0.5	-2.0	mV/V
I _{REF}	Reference Input Current	I _{KA} = 10 mA, R ₁ =	I_{KA} = 10 mA, R ₁ =10 KΩ, R ₂ = ∞		1.5	4.0	μΑ
I _{REF(dev)}	Deviation of Reference Input Current Over Full Temperature Range	$\begin{split} I_{KA} &= 10 \text{ mA}, \text{R}_1 = 10 \text{K} \Omega, \text{R}_2 = \infty, \\ T_{MIN} &\leq T_A \leq T_{MAX} \end{split}$			0.8	2.0	μΑ
I _{KA(MIN)}	Minimum Cathode Current for Regulation	V _{KA} = V _{REF}			0.45	1.00	mA
I _{KA(OFF)}	Off -Stage Cathode Current	V _{KA} = 36 V, V _{REF} = 0			0.05	1.00	μΑ
Z _{KA}	Dynamic Impedance	V _{KA} = V _{REF} , I _{KA} = f ≥ 1.0 kHz	= 1 to 100 mA,		0.15	0.50	Ω

Notes:

- 7. $T_{MIN} = -40^{\circ}C$, $T_{MAX} = +85^{\circ}C$.
- The deviation parameters V_{REF(dev)} and I_{REF(dev)} are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, αV_{REF}, is defined as:

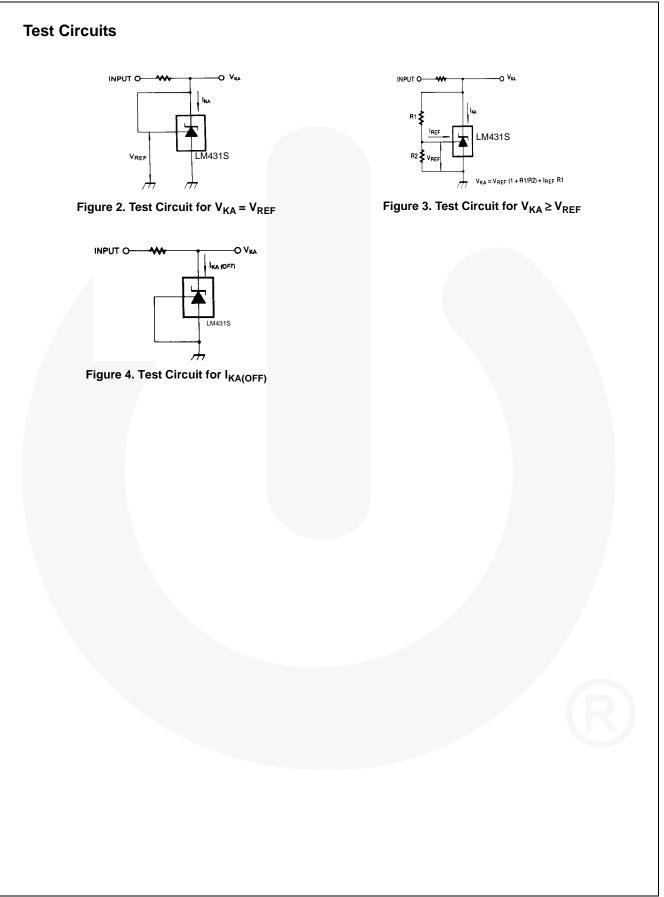


where T_{MAX} -T_{MIN} is the rated operating free-air temperature range of the device. αV_{REF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF}, respectively, occurs at the lower temperature.

Example: $V_{REF(dev)}$ = 4.5 mV, V_{REF} = 2500 mV at 25 °C, T_{MAX} - T_{MIN} = 125 °C for LM431SAI.

$$\left|\alpha V_{\mathsf{REF}}\right| = \frac{\left(\frac{4.5\,\mathrm{mV}}{2500\,\mathrm{mV}}\right) \cdot 10^{6}}{125\,^{\circ}\mathrm{C}} = 14.4\,\mathrm{ppm/^{\circ}C}$$

Because minimum $V_{\mbox{\scriptsize REF}}$ occurs at the lower temperature, the coefficient is positive.



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Typical Applications

$$V_{O} = \left(1 + \frac{R_{1}}{R_{2}}\right) V_{ref}$$

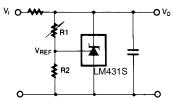


Figure 5. Shunt Regulator

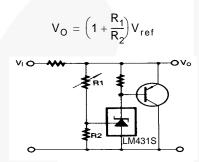


Figure 7. High Current Shunt Regulator

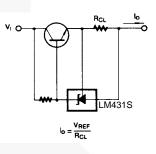


Figure 8. Current Limit or Current Source



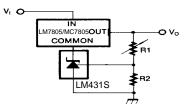


Figure 6. Output Control for Three- Terminal Fixed Regulator

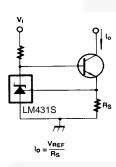
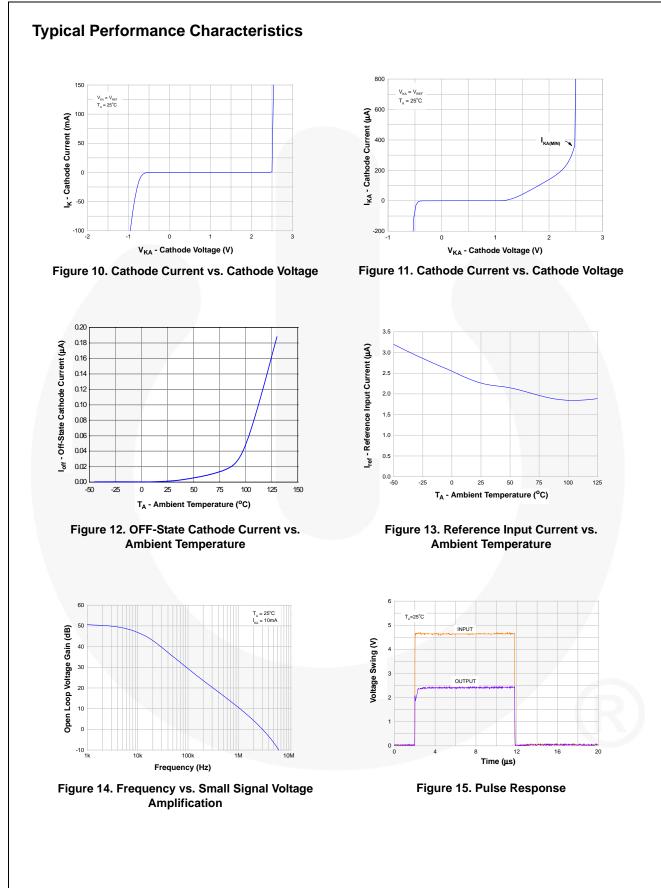
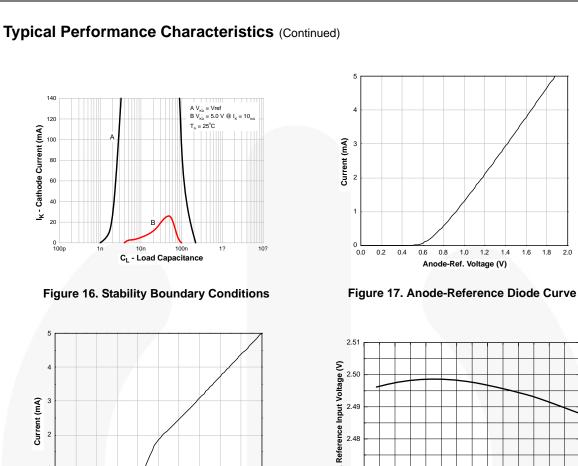


Figure 9. Constant-Current Sink



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2.48

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2.46

-50

-25

0 25 50 75 T_A, Ambient Temperature (°C)

Figure 19. Reference Input Voltage vs. Ambient Temperature

100

125



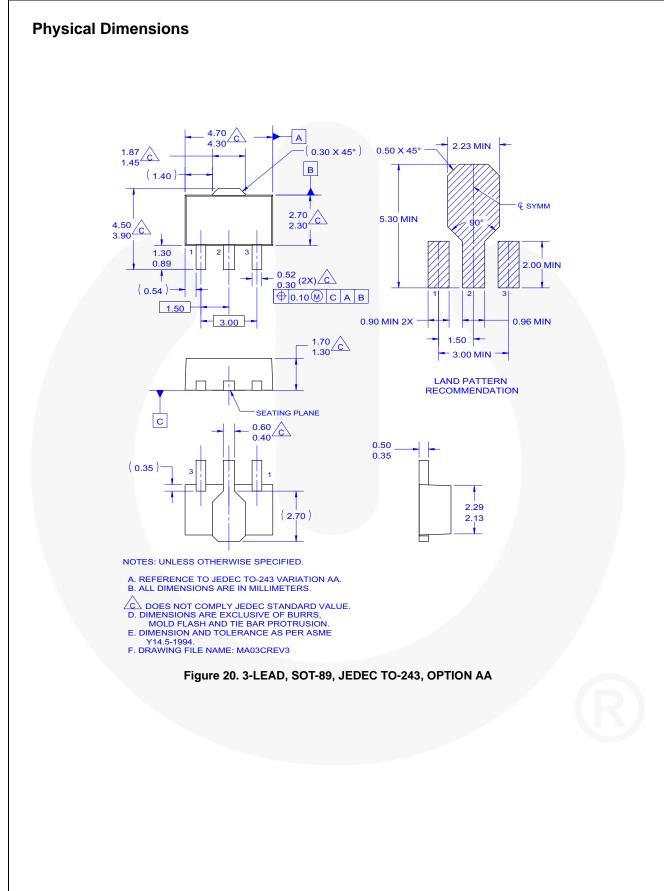
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0.0

0.2 0.4 0.6 0.8 1.0 1.2

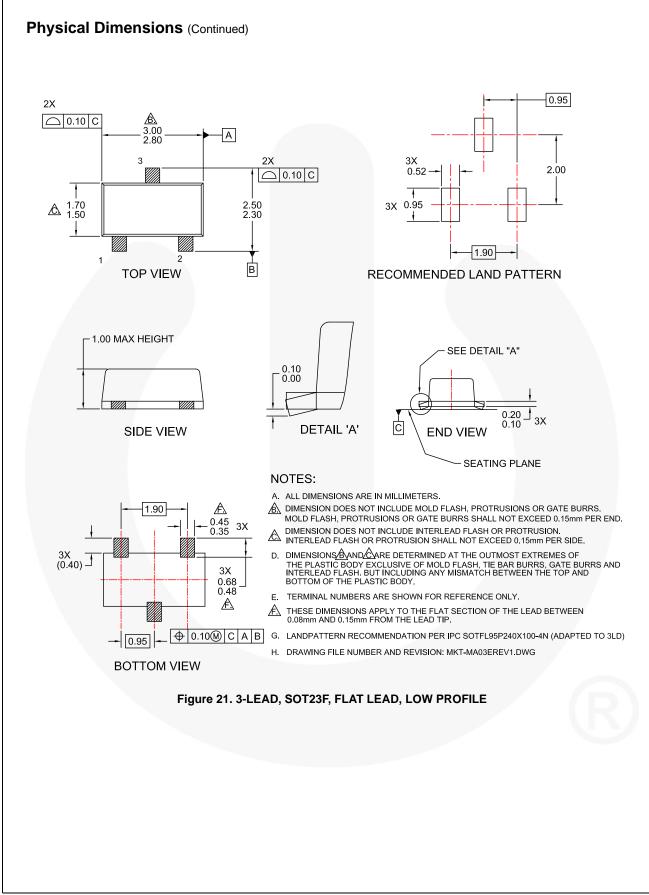
Ref.-Cathode Voltage (V) Figure 18. Reference-Cathode Diode Curve

1.4 1.6 1.8 2.0



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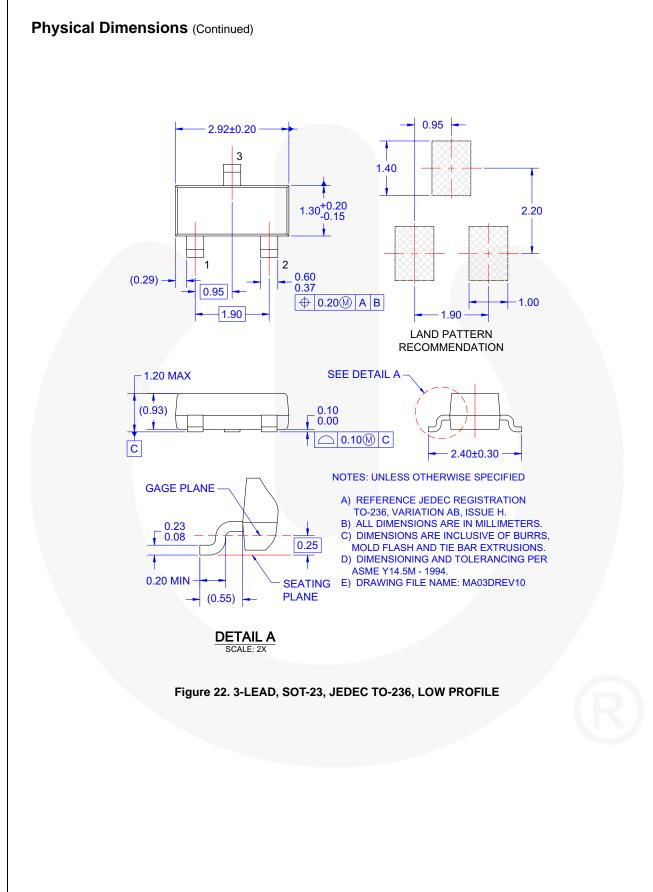
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Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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