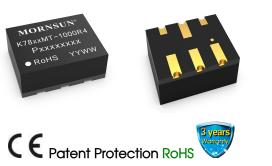


Wide input voltage non-isolated and regulated single output



FEATURES

- Ultra-small, ultra-thin DFN package(9.00 x 7.00 x 3.10mm)
- Operating ambient temperature range: -40°C to +105°C
- High efficiency up to 94%
- No-load input current as low as 0.1mA
- Continuous short circuit protection
- Meets AEC-Q100

EN62368-1

K78_MT-1000R4 series are high efficiency switching regulators. The converters feature high efficiency, low loss and short-circuit protection in a compact DFN package. These products are widely used in applications such as industrial control, instrumentation and electric power.

		Input Voltage (VDC)*	C	Full Load	Capacitive	
Certification	Part No.	Nominal (Range)	Voltage (VDC)	Current (mA) Max.	Efficiency (%) Typ. Vin Min./ Vin Nominal / Vin Max.	Load (µF) Max.
	K7803MT-1000R4	24 (4.75-36)	3.3	1000	89/84/81	680
	K7003IVII-1000R4	12 (8-27)	-3.3	-500	85/85/81	330
	K7805MT-1000R4	24 (6.5-36)	5	1000	92/87/84	680
		12 (8-27)	-5	-500	85/85/83	330
	K78X6MT-1000R4	24 (8-36)	6.5	1000	92/88/86	680
EN		12 (8-24)	-6.5	-500	83/85/84	330
LIN	1/70001 47 1000D 4	24 (12-36)	9	1000	92/90/87	680
	K7809MT-1000R4	12 (8-24)	-9	-500	81/85/84	330
	K7812MT-1000R4	24 (15-36)	12	1000	94/91/89	680
		12 (8-20)	-12	-300	83/85/84	330
	K7815MT-1000R4	24 (18-36)	15	1000	94/93/90	680
	K7010101-1000R4	12 (8-18)	-15	-300	82/84/84	330

Note: * For input voltage exceeding 30 VDC, an input capacitor of 22uF/50V is required.

Input Specification	S								
Item	Operating Conditions	Min.	Тур.	Max.	Unit				
No-load Input Current	bad Input Current Nominal input voltage 0.1								
Reverse Polarity at Input	ty at Input Avoid / Not protected								
Input Filter			Capacito	ance filter					
	Module on Ctrl pin open or pulled high(TTL 1.6~5								
Ctrl*	Module off	Ctrl pin p	Ctrl pin pulled low to GND(-Vo)(0~0.6VDC)						
	Nominal input voltage, input current when off		240		uA				

Note: *The positive output ctrl pin voltage is referenced to input GND; Negative output ctrl pin voltage is referenced to -Vo.

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2021.12.08-A/3 Page 1 of 6

DC/DC Converter

K78_MT-1000R4 Series

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Output Specification	าร					
Item	Operating Conditions		Min.	Typ.	Max.	Unit
Voltago Apolikaov	Full load, input	3.3 VDC output		±2	±4	
Voltage Accuracy	voltage range	Others		±2	±3	%
Linear Regulation Full load, input voltage range				±0.2		70
Load Regulation	Nominal input voltage,	10% -100% load		±1.0		
	20MHz bandwidth, nomi		75	150		
Ripple & Noise*	20MHz bandwidth, nomi external capacitor 22µF	nal input voltage, full load,		20	75	mVp-p
Temperature Coefficient	Operating temperature	-40 ℃ to +105℃		±0.02		%/ ℃
Transient Despense Deviation	Nominal input voltage,	3.3 V/5V/6.5V/9VDC output		50	150	
Transient Response Deviation	25% load step change	12V/15VDC output		100	300	mV
Transient Recovery Time	Nominal input voltage, 2	25% load step change		0.1	0.8	ms
Short-circuit Protection				Continuous,	self-recovery	,
Trim	Input voltage range			±10		%Vo
Note: * The "parallel cable" metho	d is used for ripple and noise	test, please refer to DC-DC Converter A	Application No	tes for specific	information;	

General Specifications ltem **Operating Conditions** Min. Typ. Max. Unit -40 +105 Operating Temperature See Fig. 1 ---°C Storage Temperature -55 ___ +125 5 95 %RH Storage Humidity Non-condensing ---Peak temperature \leq 245 °C , duration \leq 60s **Reflow Soldering Temperature** max. over 217°C. Also refer to IPC/JEDEC J-STD-020D.1. 1.0 MHz Switching Frequency Full load, nominal input voltage ------MTBF MIL-HDBK-217F@25°C 8552 k hours ------Moisture Sensitivity Level IPC/JEDEC J-STD-020D.1 Level 3 (MSL) PD3 Pollution Degree

Mechanical Specific	ations
Case Material	Black epoxy resin; flame-retardant and heat-resistant(UL94 V-0)
Dimensions	9.00 x 7.00 x 3.10 mm
Weight	0.58g(Typ.)
Cooling Method	Free air convection

Electron	lectromagnetic Compatibility (EMC)											
Emissions	CE	CISPR32/EN55032 CLASS B (see Fig. 3-2) for recommended circuit)										
ETTISSIONS	RE	CISPR32/EN55032 CLASS B (see Fig. 3-2) for recommended circuit)										
	ESD*	IEC/EN 61000-4-2 Contact ±6kV	perf. Criteria B									
	RS	IEC/EN 61000-4-3 10V/m	perf. Criteria A									
Immunity	CS	IEC/EN 61000-4-6 3Vr.m.s	perf. Criteria A									
	EFT	IEC/EN 61000-4-4 ±1kV (see Fig. 3-1) for recommended circuit)	perf. Criteria B									
	Surge	IEC/EN 61000-4-5 line to line ±1kV (see Fig. 3-1) for recommended circ	cuit) perf. Criteria B									

Note: * The static level of the Ctrl & Trim pin is ±2kV when they are not connected to external devices; It is suggested to connect an external capacitor (225k/50V) from Ctrl to GND/-Vo to meet ESD (±6kV) of the Ctrl pin, and to connect a varistor (22V/30A) from Trim to GND/-Vo to meet ESD(±6kV) of the Trim pin.

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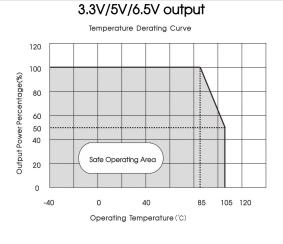
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2021.12.08-A/3 Page 2 of 6

DC/DC Converter K78_MT-1000R4 Series

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Typical Characteristic Curves



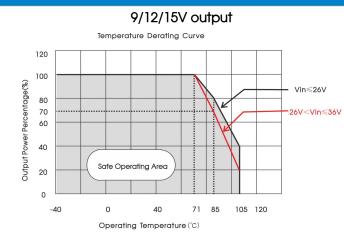
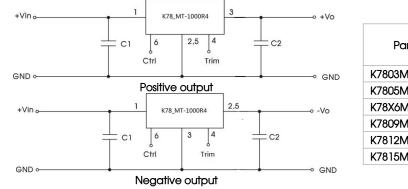


Fig. 1

Design Reference

1. Typical application



Part No.	C1 (ceramic capacitor)	C2 (ceramic capacitor)	Ra1/Ra2 (Trim resistance)	
K7803MT-1000R4	· · ·	22µF/10V		
K7805MT-1000R4	_	22µF/10V		
K78X6MT-1000R4	10.5/50/	22µF/16V	Refer to Trim	
K7809MT-1000R4	10µF/50V	22µF/16V	resistance calculation	
K7812MT-1000R4	-	22µF/25V	calculation	
K7815MT-1000R4	-	22µF/25V		

Table 1

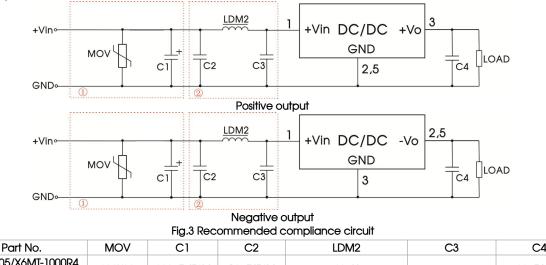
Fig. 2 Typical application circuit

Notes: 1. The required C1 and C2 capacitors must be connected as close as possible to the terminals of the module;

2. Refer to Table 1 for C1 and C2 capacitor values. For certain applications, increased values and/or tantalum or low ESR electrolytic capacitors may also be used instead;

3. Converter cannot be used for hot swap and with output in parallel.

2. EMC compliance circuit



Turrio,	1010 0		CZ	LDIVIZ	0	04
K7803/05/X6MT-1000R4 (Positive output)	S20K30	680µF /50V	10µF/50V	68µH		22µF/25V
Others	S20K30	680µF /50V	10µF/50V	68µH	10µF/50V	22µF/25V

Notes: For EMC tests we use Part \bigcirc in Fig.3 for immunity and part \oslash for emissions test. Selecting based on needs.



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DC/DC Converter K78_MT-1000R4 Series

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3. Trim Function for Output Voltage Adjustment (open if unused)

1. Positive output application: connect trim resistor to GND/Vo respectively for adjusting up/down.

2. Negative output application: connect trim resistor to GND/Vo- respectively for adjusting up/down. +V0 +V0 GND

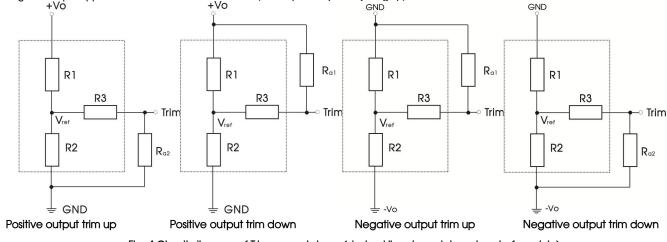


Fig. 4 Circuit diagram of Trim up and down (dashed line shows internal part of module)

Calculating Trim resistor values:

Trim up :
$$R_{a2} = \frac{aR_2}{R_2 - a} - R_3$$
, $a = R_2 / /(R_3 + R_{a2}) = \frac{V_{ref}}{V_o \cdot -V_{ref}} R_1$
Trim down : $R_{a1} = \frac{aR_1}{R_1 - a} - R_3$, $a = R_1 / /(R_3 + R_{a1}) = \frac{V_o \cdot -V_{ref}}{V_{ref}} R_2$

		ių u	• ref	
Vout(V)	R1(K ^Ω)	R2(K Ω)	R3(K Ω)	Vref(V)
3.3 150		33 180		0.6
5	100	13.66	82	0.6
6.5	6.5 32.4		20	0.6
9	100	7.14	47	0.6
12	12 100		43	0.6
15	180	7.5	51	0.6

Table:

Vout nom. ±3.3VDC		t nom. ±3.3VDC ±5.0VDC		±6.5	VDC	±9.0VDC		±12VDC		±15VDC		
Vout Trim.	Ra1 (KΩ)	Ra2 (KΩ)	Ra1 (KΩ)	Ra2 (KΩ)	Ra1 (KΩ)	Ra2 (KΩ)	Ral (KΩ)	Ra2 (ΚΩ)	Ra1 (KΩ)	Ra2 (KΩ)	Ral (KΩ)	Ra2 (KΩ)
2.97	815	-	-	(K ³²)	-	-	-	-	-	(K ³²)	-	-
3.63	-	117.3	-	-	-	-	-	-	-	-	-	-
4.5	-	-	710	-	-	-	-	-	-	-	-	-
5.5	-	-	-	36.2	-	-	-	-	-	-	-	-
5.85	-	-	-		245.4	-	-	-	-	-	-	-
7.15	-	-	-	-	-	9.5	-	-	-	-	-	-
8.1	-	-	-	-	-	-	783.2	-	-	-	-	-
9.9	-	-	-	-	-	-	-	19.9	-	-	-	-
10.8	-	-	-	-	-	-	-	-	833.5	-	-	-
13.2	-	-	-	-	-	-	-	-	-	5.5	-	-
13.5	-	-	-	-	-	-	-	-	-	-	1497	-
16.5	-	-	-	-	-	-	-	-	-	-	-	21

4. For additional information please refer to DC-DC converter application notes on

www.mornsun-power.com



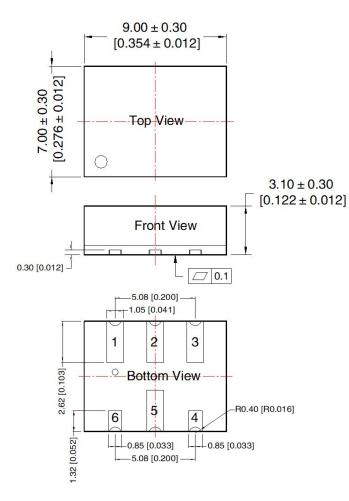
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Temperature Rise Test PCB Layout

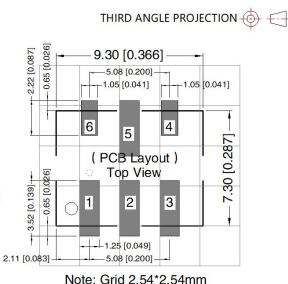


GND 0000 0000 00

Dimensions and Recommended Layout



Note: Unit: mm[inch] General tolerances: $\pm 0.10[\pm 0.004]$



Note: Grid 2.54*2.54mm	1
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	Pin-Out										
Pin	Positive output	Negative output									
1	+Vin	+Vin									
2	GND	-Vo									
3	+Vo	GND									
4	Trim	Trim									
5	GND	–Vo									
6	Ctrl	Ctrl									

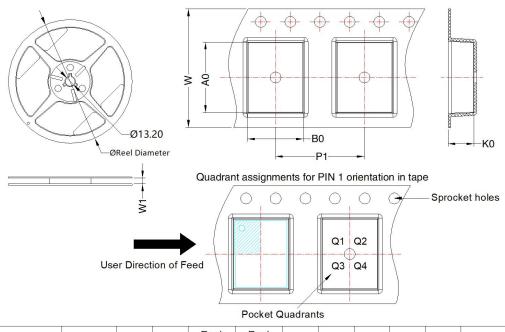
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2021.12.08-A/3 Page 5 of 6

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Tape/Reel packaging



Device	Package Type	Pin	MPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
K78xxMT-1000R4	DFN 7x9	7	400	180.0	16.4	9.56	7.56	3.5	12.0	16.0	Q1

Notes:

1. For additional information on Product Packaging please refer to <u>www.mornsun-power.com</u>. Tape/Reel packaging bag number: 58240031;

2. Refer to *IPC 7093* for the welding process design of this product. For detailed operation guidance, please refer to *Hot Air Gun Welding Operation Instruction for DFN Package Product*,

3. The maximum capacitive load offered were tested at nominal input voltage and full load;

4. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta= 25° , humidity<75%RH with nominal input voltage and rated output load;

5. All index testing methods in this datasheet are based on our company corporate standards;

6. We can provide product customization service, please contact our technicians directly for specific information;

7. Products are related to laws and regulations: see "Features" and "EMC";

8. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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2021.12.08-A/3 Page 6 of 6