

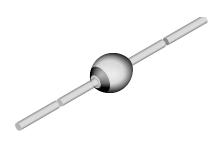
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## Vishay Semiconductors

25 000

25 000

# **Standard Avalanche Sinterglass Diode**



949539

#### **FEATURES**

- · Glass passivated junction
- · Hermetically sealed package
- Low reverse current

**APPLICATIONS** 

• High surge current loading

· Rectification, general purpose

Material categorization:
For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>





ROHS COMPLIANT HALOGEN FREE

#### **MECHANICAL DATA**

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750,

BYX86TR

BYX86TAP

method 2026

BYX86

BYX86

Polarity: color band denotes cathode end

**Mounting position:** any **Weight:** approx. 369 mg

ORDERING INFORMATION (Example)					
DEVICE NAME	ORDERING CODE	TAPED UNITS	MINIMUM ORDER QUANTITY		

5000 per 10" tape and reel

5000 per ammopack

PARTS TABLE				
PART	TYPE DIFFERENTIATION	PACKAGE		
BYX82	V <sub>R</sub> = 200 V; I <sub>F(AV)</sub> = 2 A	SOD-57		
BYX83	V <sub>R</sub> = 400 V; I <sub>F(AV)</sub> = 2 A	SOD-57		
BYX84	V <sub>R</sub> = 600 V; I <sub>F(AV)</sub> = 2 A	SOD-57		
BYX85	V <sub>R</sub> = 800 V; I <sub>F(AV)</sub> = 2 A	SOD-57		
BYX86	V <sub>R</sub> = 1000 V; I <sub>F(AV)</sub> = 2 A	SOD-57		

PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
Reverse voltage = repetitive peak reverse voltage		BYX82	$V_R = V_{RRM}$	200	V
		BYX83	$V_R = V_{RRM}$	400	V
	See electrical characteristics	BYX84	$V_R = V_{RRM}$	600	V
		BYX85	$V_R = V_{RRM}$	800	V
		BYX86	$V_R = V_{RRM}$	1000	V
Peak forward surge current	t <sub>p</sub> = 10 ms, half sine wave		I <sub>FSM</sub>	50	Α
Repetitive peak forward current			I <sub>FRM</sub>	10	Α
Average forward current	T <sub>amb</sub> ≤ 45 °C		I <sub>F(AV)</sub>	2	Α
i <sup>2</sup> t-rating			i² t	8	A <sup>2</sup> s
Junction and storage temperature range			$T_j = T_{stg}$	- 55 to + 175	°C

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<b>MAXIMUM THERMAL RESISTANCE</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	Lead length I = 10 mm, T <sub>L</sub> = constant	$R_{thJA}$	45	K/W	
Junction ambient	On PC board with spacing 25 mm	$R_{thJA}$	100	K/W	

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 1 A	V <sub>F</sub>	-	0.9	1	V
Reverse current	$V_R = V_{RRM}$	I <sub>R</sub>	-	0.1	1	μA
	$V_R = V_{RRM}, T_j = 100 ^{\circ}C$	I <sub>R</sub>	-	10	25	μΑ
Diode capacitance	V <sub>R</sub> = 4 V, f = 1 MHz	C <sub>D</sub>	-	20	-	pF
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_R = 0.25 \text{ A}$	t <sub>rr</sub>	-	2	4	μs
Reverse recovery charge	$I_F = I_R = 1 \text{ A, dI/dt} = 5 \text{ A/}\mu\text{s}$	$Q_{rr}$	-	3	6	μC

#### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

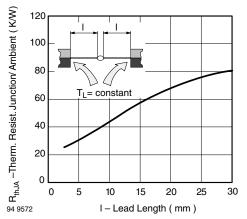


Fig. 1 - Max. Thermal Resistance vs. Lead Length

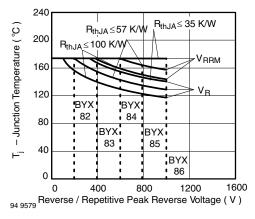


Fig. 2 - Junction Temperature vs. Reverse/Repetitive Peak Reverse Voltage

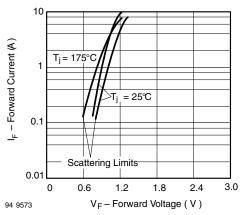


Fig. 3 - Forward Current vs. Forward Voltage

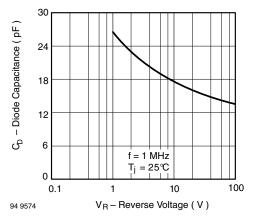


Fig. 4 - Typ. Diode Capacitance vs. Reverse Voltage

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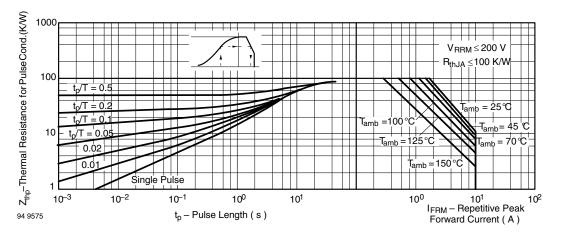


Fig. 5 - Thermal Response

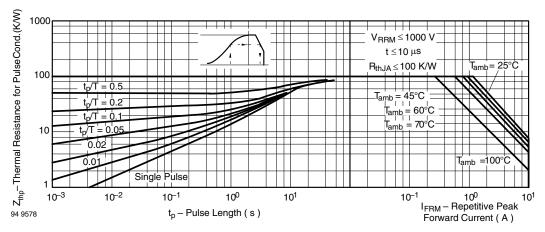


Fig. 6 - Thermal Response

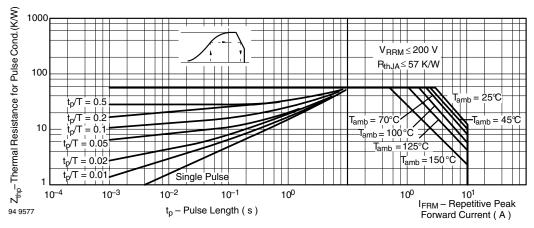


Fig. 7 - Thermal Response

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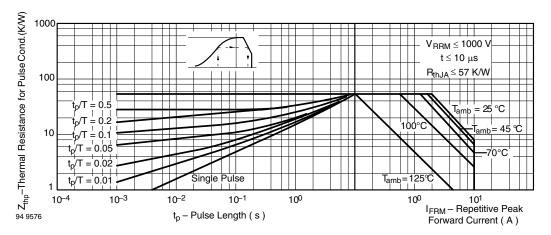
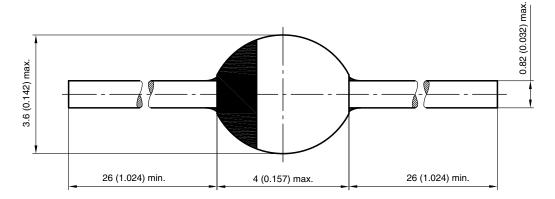


Fig. 8 - Thermal Response

#### PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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