

# Automotive Grade AC Line Rated Ceramic Disc Capacitors Class X1, 440 V<sub>AC</sub>, Class Y2, 300 V<sub>AC</sub>



#### **DESIGN SUPPORT TOOLS**

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QUICK REFERENCE DATA							
DESCRIPTION			VALUE				
Ceramic Class	1 2		2				
Ceramic Dielectric	U2J	U2J	Y5S, Y5U	Y5S, Y5U			
Voltage (V <sub>AC</sub> )	300	440	300	440			
Min. Capacitance (pF)	1	0	68				
Max. Capacitance (pF)	47		4700				
Mounting	Radial						

#### **OPERATING TEMPERATURE RANGE**

-55 °C to +125 °C

#### **TEMPERATURE CHARACTERISTICS**

Class 1: N750 (U2J) Class 2: Y5S, Y5U

### **SECTIONAL SPECIFICATIONS**

Climatic category (according to EN 60058-1) Class 1 and class 2: 40/125/21

#### **COATING**

According to UL 94 V-0 Epoxy resin, isolating, flame retardant

#### **APPROVALS**

IEC 60384-14.4 UL 60384-14 DIN EN 60384-14 CSA E60384-1:03. CSA E60384-14:09

#### **PACKAGING**

Bulk, tape and reel, taped ammopack

#### **FEATURES**

- AEC-Q200 qualified
- Withstands 85 / 85 / 1000 h test
- Can pass 3000 temperature cycles (from -55 °C to +125 °C)
- Complying with IEC 60384-14 4th edition
- · High reliability
- Vertical (inline) kinked or straight leads
- Singlelayer AC disc safety capacitors
- PPAP (AIAG version) is available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### AUTOMOTIVE GRADE





ROHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

#### **APPLICATIONS**

- X1, Y2 according to IEC 60384-14.4
- Application as Y capacitors for AC line filter and primary-secondary coupling on battery chargers for PHEV/EV
- Application as filter capacitors on DC/DC converters for PHEV/EV and HEV

#### **DESIGN**

The capacitor consists of a ceramic disc which is silver plated on both sides. Connection leads are made of tin plated copper-clad steel having a diameter of 0.6 mm.

The capacitors may be supplied with straight or kinked leads having a lead spacing of 5 mm, 7.5 mm, or 10.0 mm. Encapsulation is made of flame retardant epoxy resin in accordance with UL 94 V-0.

#### **CAPACITANCE RANGE**

10 pF to 4700 pF

#### RATED VOLTAGE UR

IEC 60384-14.4: (X1): 440  $V_{AC}$ , 50 Hz (Y2): 300  $V_{AC}$ , 50 Hz 1000  $V_{DC}$ 

#### **TEST VOLTAGE**

Component test (100 %): 2600  $V_{AC}$ , 50 Hz, 2 s Random sampling test (destructive test): 2600  $V_{AC}$ , 50 Hz, 60 s Voltage proof of coating (destructive test): 2600  $V_{AC}$ , 50 Hz, 60 s

#### **INSULATION RESISTANCE**

 $\geq$  10 000 M $\Omega$ 

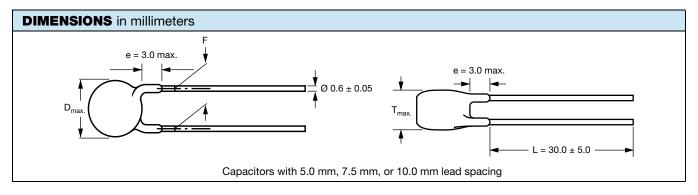
#### **CAPACITANCE TOLERANCE**

± 20 % (code M); ± 10 % (code K)

#### **DISSIPATION FACTOR**

Class 1: max. 0.3 % (1 MHz) Class 2: max. 2.5 % (1 kHz)





TECHNICAL DATA								
CAPACITANCE	CAPACITANCE	BODY	BODY	LEAD SPACING	PART NUMBER			
C (pF)	TOLERANCE (%)	DIAMETER D <sub>max.</sub> (mm)	THICKNESS T <sub>max.</sub> (mm)	F (mm) ± 1 mm	MISSING DIGITS SEE ORDERING CODE BELOW			
U2J (N750)								
10					AY2100K29U2JS6###			
15					AY2150K29U2JS6###			
22	± 10	7.5	5.0	5.0, 7.5, or 10.0	AY2220K29U2JS6###			
33					AY2330K29U2JS6###			
47					AY2470K29U2JS6###			
Y5S (2C3)								
68					AY2680K29Y5SS6###			
100					AY2101K29Y5SS6###			
150	± 10	7.5	5.0	5.0, 7.5, or 10.0	AY2151K29Y5SS6###			
220	± 10	7.5	3.0	3.0, 7.3, 01 10.0	AY2221K29Y5SS6###			
330					AY2331K29Y5SS6###			
470					AY2471K29Y5SS6###			
Y5U (2E3)								
680		7.5			AY2681#29Y5US6###			
1000		7.5			AY2102#29Y5US6###			
1500		8.5			AY2152#31Y5US6###			
2200	± 20	9.5	5.0	5.0, 7.5, or 10.0	AY2222#35Y5US6###			
3300		11.0			AY2332#41Y5US6###			
3900		11.5			AY2392#43Y5US6###			
4700		13.0			AY2472#49Y5US6###			

#### Note

(1) ± 10 % available on request

ORDERING CODE										
#	7 <sup>th</sup> digit		Capacitar	nce tolerance	<del>)</del>	± 10 % =	K, ± 20 % =	: M		
###	15 <sup>th</sup> to 17	7 <sup>th</sup> digit	Lead conf	figuration		Available	configuratio	ns see below		
Example	AY2	221	K	29	Y5S	S	6	U	٧	7
	Series	Capacitance value	Tolerance code	Size code	Temperature coefficient	Rated voltage	Lead wire diameter	Packaging / lead length	Lead style	Lead spacin
								3 = bulk T = tape and reel U = ammopack	L = straight V = inline kinked	5 = 5.0 7 = 7.5 0 = 10.



#### **LEADSPACING 5.0 mm and 7.5 mm**

PACKAGING							
CAPACITANCE		BODY DIAMETER	P/	ACKAGING QUANTITIE	ES		
VALUE	SIZE CODE	D <sub>max.</sub> (mm)	BULK	REEL	АММО		
10 pF to 4700 pF	29 to 49	13.0	1000	1000	1000		

#### **LEADSPACING 10.0 mm**

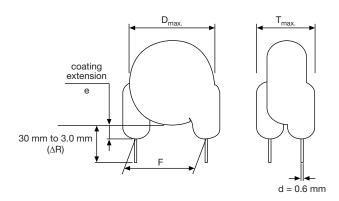
PACKAGING							
CAPACITANCE		BODY DIAMETER	P.	ACKAGING QUANTITI	ES		
VALUE	SIZE CODE	D <sub>max.</sub> (mm)	BULK	REEL	АММО		
10 pF to 4700 pF	29 to 49	13.0	1000	500	750		

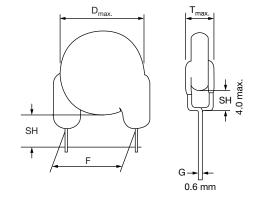
#### Note

• The capacitors are supplied in bulk packaging (cardboard boxes), in tape on reel in ammopack.

#### **STRAIGHT LEADS**

#### **INLINE KINKED LEADS**





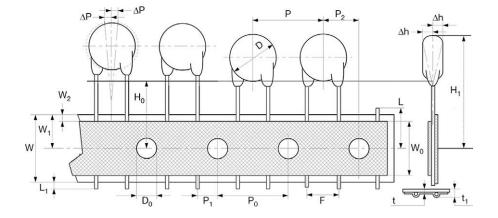


Fig. 1 - The hole pitch 12.7 mm for lead spacing 5.0 mm (0.2"), and hole pitch 15.0 mm for lead spacing 7.5 mm (0.3")

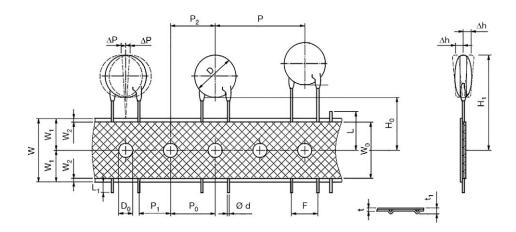


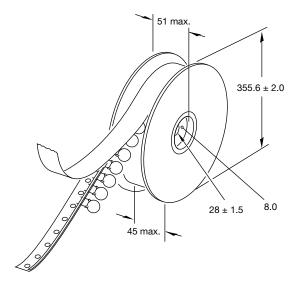
Fig. 2 - The hole pitch 12.7 mm for lead spacing 10.0 mm (0.4")

MENSION OF TAPE								
CYMPOL	DADAMETED		DIMENSIONS (mm)					
SYMBOL	PARAMETER	FIG. 1 (5 mm)	FIG. 1 (7.5 mm)	FIG. 2 (10 mm)				
D (1)	Body diameter	11.0 max.	14.0 max.	16.0 max.				
d	Lead diameter	0.6 ± 0.05	0.6 ± 0.05	$0.6 \pm 0.05$				
Р	Pitch of component	12.7 ± 1	15.0 ± 1	25.4 ± 1				
P <sub>0</sub> <sup>(2)</sup>	Pitch of sprocket hole	12.7 ± 0.3	15.0 ± 0.3	12.7 ± 0.3				
P <sub>1</sub> (3)	Distance, hole center to lead	3.85 ± 0.7	3.75 ± 0.7	7.7 ± 1.0				
P <sub>2</sub> <sup>(3)</sup>	Distance, hole to center of component	6.35 ± 1.3	7.5 ± 1.5	12.7 ± 1.5				
F	Lead spacing	5.0 (+ 0.6/- 0.4)	7.5 (+ 0.6/- 0.4)	10.0 (+ 0.6/- 0.4)				
Δh	Average deviation across tape	± 1.0 max.	± 1.0 max.	± 1.0 max.				
ΔΡ	Average deviation in direction of reeling	± 1.0 max.	± 1.0 max.	± 1.0 max.				
W	Carrier tape width	18.0 + 1/- 0.5	18.0 + 1/- 0.5	18.0 + 1/- 0.5				
W <sub>0</sub>	Hold-down tape width	5.0 min.	5.0 min.	5.0 min.				
W <sub>1</sub>	Position of sprocket hole	9.0 + 0.75/- 0.5	9.0 + 0.75/- 0.5	9.0 + 0.75/- 0.5				
W <sub>2</sub>	Distance of hold-down tape	3.0 max.	3.0 max.	3.0 max.				
H <sub>1</sub>	Maximum component height	32	40	40				
H <sub>0</sub>	Height to seating plane (for kinked leads)	16.0 ± 0.5	16.0 ± 0.5	16.0 ± 0.5				
H <sub>0</sub>	Height to seating plane (for straight leads)	20.0 ± 0.5	20.0 ± 0.5	20.0 ± 0.5				
L	Length of cut leads	11.0 max.	11.0 max.	11.0 max.				
L <sub>1</sub> Length of lead protrusion		1.0 max.	1.0 max.	1.0 max.				
D <sub>0</sub>	Diameter of sprocket hole	4.0 ± 0.2	4.0 ± 0.2	4.0 ± 0.2				
t Total tape thickness		0.9 max.	0.9 max.	0.9 max.				
t <sub>1</sub>	Maximum thickness of tape and wires	1.5 max.	1.5 max.	1.5 max.				

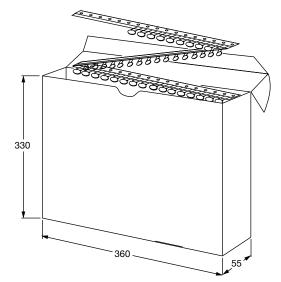
#### Notes

- (1) See "Technical Data" table
- (2) Cumulative pitch error: ± 1 mm/20 pitches
- (3) Obliquity maximum 3°

#### **REEL AND TAPE DATA** in millimeters



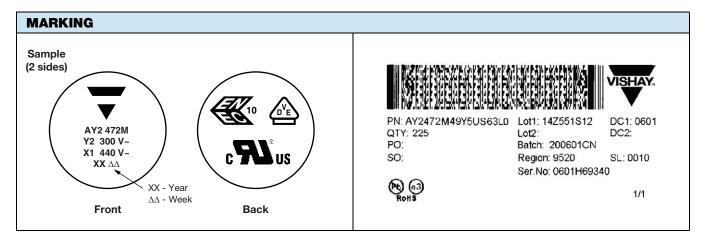
Reel with capacitors on tape



Ammopack with capacitors on tape

APPROVALS				
IEC 60384-14.4 - Safety tests This approval together with CB test certificate substi	tutes all national approvals			
CB Certificate				
Y2-capacitor: CB test certificate:	US-26163-UL	10 pF to 4.7 nF	300 V <sub>AC</sub>	<i>(</i> Ui )
X1-capacitor: CB test certificate:	US-26163-UL	10 pF to 4.7 nF	440 V <sub>AC</sub>	
VDE				^
Y2-capacitor: VDE marks approval:	40009669	10 pF to 4.7 nF	300 V <sub>AC</sub>	
X1-capacitor: VDE marks approval:	40009669	10 pF to 4.7 nF	$440  V_{AC}$	
DIN EN 60384-14 VDE 0565-1-1:2006-04 - Safety te	sts			
Underwriters Laboratories Inc./Canadian Standar	ds Association			
Y2-capacitor: UL-test certificate:	E183844	10 pF to 4.7 nF	300 V <sub>AC</sub>	<b>6-1 1</b> 8
X1-capacitor: UL-test certificate:	E183844	10 pF to 4.7 nF	440 V <sub>AC</sub>	c <b>FL</b> iis
UL 60384-14, CSA E60384-1:03 2 <sup>nd</sup> edition, CSA E6	0384-14:09 2 <sup>nd</sup> edition			0 10
Across-the-line, antenna-coupling and line-by-pass	component			
CQC				
Y2-capacitor: CQC test certificate:	CQC05001012316	10 pF to 4.7 nF	300 V <sub>AC</sub>	
X1-capacitor: CQC test certificate:	CQC05001012316	10 pF to 4.7 nF	440 V <sub>AC</sub>	





PER	FORMANCE					
NO.	ITEMS		SPECIFICATION	TEST METHOD		
1	Visual and mechanical examination		No visible damage. The marking shall be legible. Dimensions are within specification.	Capacitors shall be visible evidence of c Dimensions shall be calipers or microme	measured with	
2	Capacitance		Within the specified tolerance.	The capacitance shall be measured at 25 °C $\pm$ 3 °C, 75 % RH maximum 1.0 $V_{RMS} \pm$ 0.2 $V_{RMS}$ , 1 kHz for Y5U, \ and 1 MHz for U2J.		
3	Dissipation factor (D.F.)		U2J: 0.3 % max. Y5U, Y5S: 2.5 % max.	at 25 °C ± 3 °C, 75	or shall be measured % RH maximum with , 1 kHz for Y5U, Y5S,	
4	Insulation resistance (I.R.)		10 G $\Omega$ min.	Insulation resistance shall be measured within 60 s $\pm$ 5 s of charging at 500 $V_{DO}$		
5	Dielectric strength (between lead wires)		No damage.	2600 V <sub>AC</sub> are applie 50 mA max. (destru		
6	Temperature characteristic		No visible damage. The marking shall be legible.	The capacitance shall be measured at each step specified in table below.		
		Capacitance change Dissipation factor	n/a U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz		ange from the value of eed the limit specified.	
				Step	Temperature	
		Insulation resistance	10 GΩ min. at 500 V <sub>DC</sub> 60 s at 25 °C and -40 °C	1	25 °C ± 3 °C	
			500 M $\Omega$ min. at 500 V <sub>DC</sub> 60 s at 125 °C	2	-40 °C ± 3 °C	
				3	25 °C ± 3 °C	
		Dielectric strength (between lead wires)	5 s 250 % rated voltage	4	125 °C ± 3 °C	
				5	25 °C ± 3 °C	



PER	FORMANCE				
NO.	ITEMS		SPECIFICATION	TEST METHOD	
7	High temperature operation life	External appearance	No visible damage. The marking shall be legible.	Test voltage: 1.5 kV <sub>AC</sub> , 60 s Impulse voltage: each individual capacitor shall be subjected to a 5 kV impulse for three times. Before the capacitors are applied to life test.	
		Capacitance change	± 15 % max.	100 % 90 % 50 % 30 % T <sub>1</sub> = 1.2 μs T <sub>2</sub> = 50 μs	
		Dissipation factor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz	0 % T <sub>1</sub> T <sub>2</sub>	
				The specimen capacitors shall be	
		Insulation resistance	3 GΩ min. at 500 V <sub>DC</sub> , 60 s	submitted to an endurance test of 1000 h + 48 h / - 0 h in a chamber at 125 °C ± 3 °C with a voltage of 550 V <sub>AC</sub> .	
				Pre-treatment: capacitor shall be backed	
		Dielectric strength (between lead wires)	No failure at 1.5 kV <sub>AC</sub> , 60 s	at 125 °C ± 3 °C for 1 h before initial measurements.	
				Post-treatment: capacitors shall be placed at room condition for 24 h $\pm$ 2 h before measurements.	
8	Life Test	External appearance	No visible damage. The marking shall be legible.	Test voltage: 1.5 kV <sub>AC</sub> , 60 s Impulse voltage: each individual capacitor shall be subjected to a 5 kV impulse for three times. Before the capacitors are applied to life test.	
		Capacitance change	± 15 % max.	100 % 90 % T <sub>1</sub> = 1.2 μs T <sub>2</sub> = 50 μs	
		Dissipation factor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz	0 %   T <sub>1</sub>   T <sub>2</sub>	
		Insulation resistance	3 GΩ min. at 500 V <sub>DC</sub> , 60 s	The specimen capacitors shall be submitted to an endurance test of 1000 h + 48 h / - 0 h in a chamber at 125 °C ± 3 °C with a voltage of 550 V <sub>AC</sub> , except that once every hour the voltage shall be increase to 1000 V <sub>AC</sub> for 0.1 s.	
		Dielectric strength (between lead wires)	No failure at 1.5 kV <sub>AC</sub> , 60 s	Pre-treatment: capacitor shall be backed at 125 °C ± 3 °C for 1 h before initial measurements.	
				Post-treatment: capacitors shall be placed at room condition for 24 h $\pm$ 2 h before measurements.	



PER	FORMANCE				
NO.	ITEMS			SPECIFICATION	TEST METHOD
9	Humidity test (under steady state)	External appe		No visible damage.  U2J: ± 10 % Y5U, Y5S: ± 20 %	Ambient temperature: 40 °C ± 2 °C Relative humidity: 90 % to 95 % RH Duration: 500 h + 48 h / - 0 h Without loading
		Dissipation fa	ctor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz	Pre-treatment: capacitor shall be stored at 40 °C ± 2 °C for 24 h ± 5 h before initial measurements.
		Insulation res	istance	3 G $\Omega$ min. at 500 V $_{DC}$ , 60 s	
		Dielectric stre (between lead		No failure at 1.5 kV <sub>AC</sub> , 60 s	Post-treatment: capacitor shall be stored for 2 h at room conditions before final measurements.
10	Humidity test (under load state)	External appe	earance	No visible damage. The marking shall be legible.	Ambient temperature: 40 °C ± 2 °C Relative humidity: 90 % to 95 % RH Duration: 500 h + 48 h / - 0 h
		Capacitance	change	U2J: ± 10 % Y5U, Y5S: ± 15 %	Loading voltage: 440 V <sub>AC</sub>
		Dissipation fa	ctor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz	Pre-treatment: capacitor shall be stored at 40 °C ± 5 °C for 24 h ± 2 h before initial measurements.
		Insulation res	istance	3 G $\Omega$ min. at 500 V <sub>DC</sub> , 60 s	Post-treatment: capacitor shall be stored
		Dielectric stre (between lead		No failure at 1.5 kV <sub>AC</sub> , 60 s	for 2 h at room conditions before final measurements.
11	Biased humidity	External appearance		No visible damage. The marking shall be legible.	Loading voltage: 440 V <sub>AC</sub> Ambient temperature: 85 °C ± 3 °C Relative humidity: 85 % RH
		Capacitance	change	U2J: ± 10 % Y5U, Y5S: ± 15 %	Duration: 1000 h + 48 h / - 0 h
		Dissipation factor  Insulation resistance		U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz	Pre-treatment: capacitor shall be stored at 40 °C ± 5 °C for 24 h ± 2 h, then place at room condition for 24 h ± 2 h before
				3 G $\Omega$ min. at 500 V $_{DC}$ , 60 s	initial measurements.
		Dielectric strength (between lead wires)		No failure at 1.5 kV <sub>AC</sub> , 60 s	Post-treatment: capacitor shall be stored for 24 h at room conditions before final measurements.
12	Termination strength	Pull test	External appearance	Lead wire should not be cut off, capacitor should not be broken.	As a figure, fix the body of capacitor, apply a tensile weight gradually to each lead wire in the radial direction of
			Capacitance change	Within specification	capacitor up to 20 N, and keep it for 10 s ± 1 s.
			Dissipation factor	Within specification	- 
			Insulation resistance	Within specification	w
		Bending test	External appearance	Lead wire should not be cut off, capacitor should not be broken.	Bending each lead wire to 90° from the lead egress with 2.5 N force, then back to original position and bent again from the same direction.  Totally 3 bends, 3 s each time.  1 bend: bending to 90° the return to normal position is one bend.  Start from 1.6 mm to 3.2 mm from the part body.



PER	PERFORMANCE							
NO.	ITEMS		SPECIFICATION	TEST METHOD				
13	Resistance to solder heat	Visual	No visible damage. The marking shall be legible.	The lead wire shall be immersed into the melted solder of 260 °C ± 5 °C up to about 1.5 mm to 2 mm from the main body for 10 s ± 2 s.  Inspect under 10 x magnification				
		Capacitance change	Within ± 10 %	Thermal screen				
		Dissipation factor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz	1.5 mm to 2.0 mm  Molten solder				
		Insulation resistance	1 GΩ min. at 500 V <sub>DC</sub> , 60 s					
				Pre-treatment: Capacitor shall be stored at 125 °C ± 5 °C for 1 h, then placed at room condition for 24 h ± 2 h before initial measurements.				
		Dielectric strength (between lead wires)	No failure at 1.5 kV <sub>AC</sub> , 60 s	Post-treatment: Capacitor shall be stored for 24 h ± 2 h at room condition.				
14	Solderability	External appearance	95 % of terminations evenly covered with solder under 10 x magnification.	Method A at category 3, steam aging for 8 h $\pm$ 15 min. Solder and temperature:				
				a) Lead (Pb)-free solder (Sn-3Ag-0.5Cu) 245 °C ± 5 °C				
				b) H63 eutectic solder 235 °C ± 5 °C dip lead wire into an ethanol solution of 25 % ± 0.5 % rosin and then into molten solder for 5 s + 0 s / - 0.5 s.				
				Depth of immersion within 1.25 mm, immerse and withdraw at 25 mm/s ± 6 mm/s				
15	Vibration test	Visual	No visible damage. The marking shall be legible.	Resin (adhesive)				
		Capacitance change	Within ± 10 %					
		5		Solder the capacitor and gum up the body				
		Dissipation factor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz	to the test jig by resin (adhesive).  The capacitor should be firmly soldered to the supporting lead wire.  Vibration change from 10 Hz to 2000 Hz,				
		Insulation resistance	10 GΩ min. at 500 V <sub>DC</sub> , 60 s	then back to 10 Hz.  Total amplitude: 1.5 mm with 5 g max., 12 cycles, 20 min for each mutually perpendicular directions, 3 directions.				



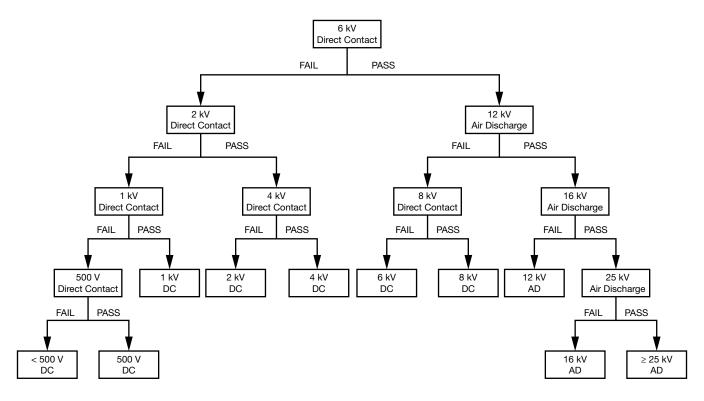
PER	PERFORMANCE								
NO.	ITEMS		SPECIFICATION	TEST METHOD					
16	Mechanical shock	External appearance	No visible damage. The marking shall be legible.	Resin (adhesive)					
		Capacitance change	Within the specified tolerance.						
		Dissipation factor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz	Solder the capacitor and gum up the body to the test jig by resin (adhesive).  3 shocks in 2 directions should be applied, totally 3 mutually perpendicular					
		Insulation resistance	10 GΩ min. at 500 V <sub>DC</sub> , 60 s.	axes, 18 shocks. Shock from: half-sine Duration: 6 ms Acceleration: 100 g					
17	Resistance to solvents	External appearance	No visible damage. The marking shall be legible.	Leave parts in solvent for 3 to 8 min at 25 °C ± 5 °C, 1 min air-drying Rub parts against wet bristle 10 times (3 x for marking, 10 x for part damage)					
				Solvent 1: 1 part (by volume) of isopropyl alcohol, 3 parts (by volume) of mineral spirits					
				Solvent 2: Terpene defluxer					
				Solvent 3: 42 parts (by volume) of water, 1 part (by volume) of propylene glycol, 1 part (by volume) of monoethanolomine					
18	Temperature cycle	Capacitance change	Within ± 10 % for U2J Within ± 20 % for Y5U and Y5S	The capacitor should be run 3000 temperature cycles. Step as below: Step 1 -55 °C + 0 °C / - 3 °C,					
		Dissipation factor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz						
		Insulation resistance	3 G $\Omega$ min at 500 V $_{DC}$ , 60 s	Step 4 Transition time ≤ 1 min  Pre-treatment:					
		Dielectric strength	No failure at 1.5 kV <sub>AC</sub> , 60 s	capacitor shall be stored at 125 °C ± 3 °C for 1 h, then placed at room condition for 24 h ± 2 h before initial measurement.					
		External appearance	No visible damage. The marking shall be legible.	Post-treatment: capacitor shall be stored for 24 h $\pm$ 2 h at room condition.					
				Note • 6800 pF and 10 000 pF only 1000 cycles					
19	19 High temperature exposure	External appearance	No visible damage. The marking shall be legible.	Storage capacitor at 125 °C ± 3 °C for 1000 h + 48 h / - 0 h without loading.					
	(storage)	Capacitance change	Within ± 10 % for U2J Within ± 20 % for Y5U and Y5S	Pre-treatment: capacitor shall be stored at 125 °C ± 3 °C for 1 h, then placed at room condition for					
		Dissipation factor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz	24 h ± 2 h before initial measurement.  Post-treatment:					
		Insulation resistance	1 GΩ min. at 500 V <sub>DC</sub> , 60 s	capacitor shall be stored for 24 h $\pm$ 2 h at room condition.					



### Vishay BCcomponents

PERFORMANCE					
NO.	ITEMS		SPECIFICATION	TEST METHOD	
20	ESD	External appearance	No visible damage. The marking shall be legible.	See chart "ESD Test Method" below	
		Capacitance change	Within ± 10 %		
		Dissipation factor	U2J: 0.5 % max. at 1 V, 1 MHz Y5U, Y5S: 5 % max. at 1 V, 1 kHz		
		Insulation resistance	1 GΩ min. at 500 V <sub>DC</sub> , 60 s.		

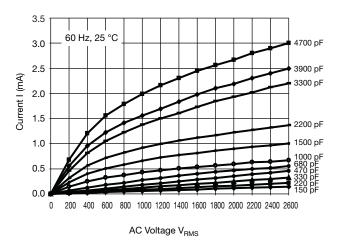
#### **ESD TEST METHOD**

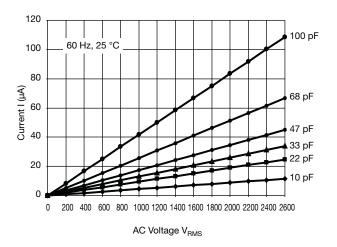


#### Notes

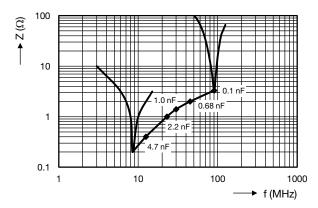
- DC means "direct contact discharge".
- AC means "air discharge".
- Classify the components according to the highest ESD voltage level survived during ESD testing.

### **LEAKAGE CURRENT VS. VOLTAGE (Typical)**





#### **IMPEDANCE VS. FREQUENCY** (Typical)



Lead configuration: length = 30 mm, lead spacing: standard, lead diameter: standard, inline crimp

#### Note

The capacitors meet the essential requirements of "EIA 198". Unless stated otherwise all electrical values apply at an ambient temperature
of 25 °C ± 3 °C, at normal atmospheric conditions.

RELATED DOCUMENTS				
General Information	www.vishay.com/doc?28536			
CB Test Certificate	www.vishay.com/doc?22254			
VDE Marks Approval	www.vishay.com/doc?22256			
UL Test Certificate	www.vishay.com/doc?22253			
CQC Test Certificate	www.vishay.com/doc?22255			

SAMPLE KIT		
Part Number	AY21-KIT-HF	
Link	www.vishay.com/doc?28553	



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