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October 2013

# S320 3 A, 200 V, Surface-Mount Package Schottky Rectifier

# **Features**

- Low-Profile, Mini-Surface-Mount Package: SMB / DO-214AA
- High-Reverse Voltage: V<sub>RRM</sub> = 200 V
- Low-Power Loss, High Efficiency
- High-Surge Current: I<sub>FSM</sub> = 80 A
- RoHS 2002/95/EC Compliant

# Description

The S320 is a high-efficiency, low power loss, generalpropose Schottky rectifier. The clip-bonded leg structure provides high thermal performance and low electrical resistance. This rectifier is suited for free wheeling, secondary rectification, and reverse polarity protection applications.



SMB / DO-214AA Color Band Denotes Cathode Mark: S320

# **Ordering Information**

Part Number	Marking	Package	Packing Method
S320	S320	DO-214AA	Tape and Reel

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Units
V <sub>RRM</sub>	Maximum Repetitive Peak Reverse Voltage	200	V
V <sub>RMS</sub>	Maximum RMS Voltage	140	V
V <sub>DC</sub>	Maximum DC Blocking Voltage	200	V
I <sub>F(AV)</sub>	Maximum Average Forward Current	3.0	A
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current: 8.3 ms Single Half-Sine Wave Superimposed on Rated Load (JEDEC Method)	80	А
T <sub>STG,</sub> T <sub>J</sub>	Operating Junction and Storage Temperature Range	-65 to +150	°C

# S320 — 3 A, 200 V, Surface-Mount Package Schottky Rectifier

# Thermal Characteristics<sup>(1)</sup>

Symbol	Parameter	Тур.	Units
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient160		°C/W
Ψ <sub>JL</sub>	Junction to Lead Thermal Characteristics 20		°C/W

Note:

1. Test condition - test environment & PCB type: JESD51-2,3, board size: 76.2 x 114.3 mm,

pad size: 2.5 x 2.2 mm, trace width: 30 mils.

# **Electrical Characteristics**

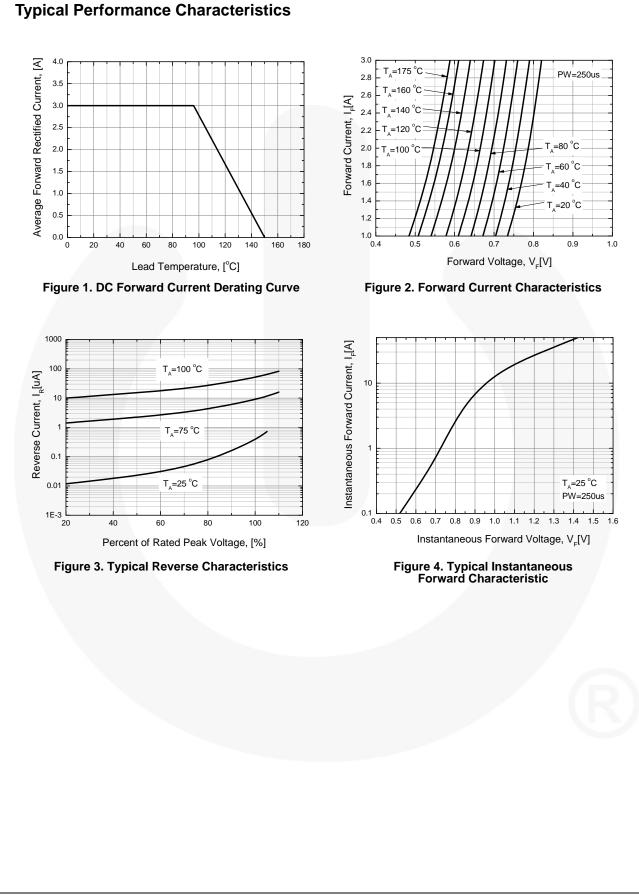
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Test Condition	Тур.	Max.	Units
V <sub>F</sub>	Forward Voltage <sup>(2)</sup>	3.0 A		0.9	V
	DC Reverse Current at Rated	$T_A = 25^{\circ}C$		7	μA
IR V <sub>D</sub>	V <sub>DC</sub>	$T_A = 100^{\circ}C$		120	μπ
t <sub>rr</sub>		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{RR} = 0.25 \text{ A}$	14		ns
	Reverse-Recovery Time <sup>(3)</sup>	$I_F = 1 \text{ A}, V_R = -30 \text{ V},$ $I_{RR} = 10\% I_{RM}, \text{ di/dt} = 50 \text{ A/}\mu\text{s}$	30		ns

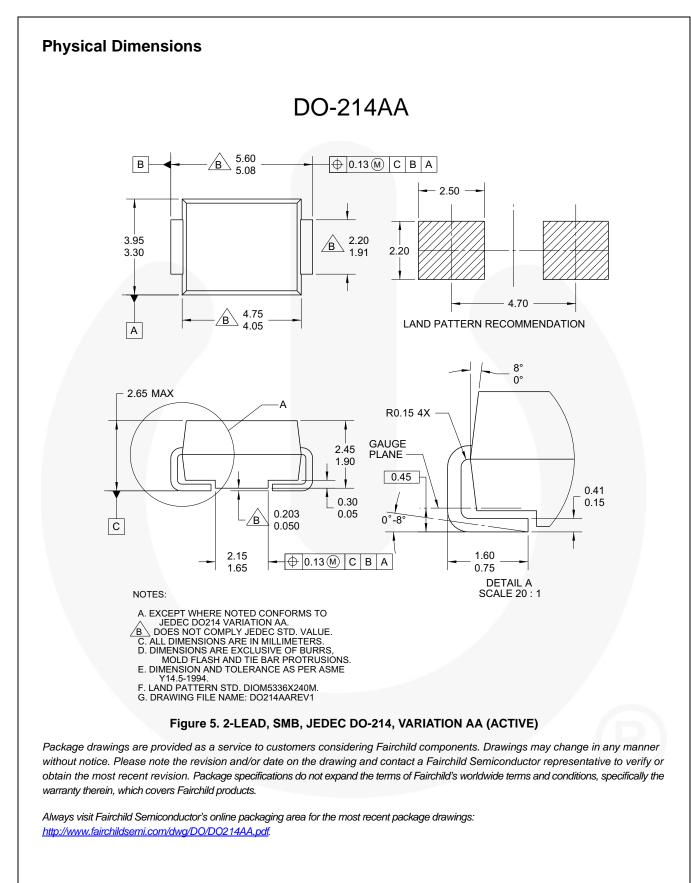
Notes:

2. Pulse test with PW = 250  $\mu$ s, 2% duty cycle.

3.  $I_R$  < 1 A due to fast reverse recovery.



3



**S320** 

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# FAIRCHILD

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Definition of	Terms
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Datasheet Identification	Product Status	Definition
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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Rev. 166

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