74ABT126

Quad buffer; 3-state Rev. 6 — 8 October 2020

Product data sheet

1. General description

The 74ABT126 is a quad buffer/line driver with 3-state outputs controlled by the output enable inputs (nOE). A LOW on nOE causes the outputs to assume a high impedance OFF-state. This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

2. Features and benefits

- Supply voltage range from 4.5 V to 5.5 V
- · BiCMOS high speed and output drive
- · Direct interface with TTL levels
- · Power-up 3-state
- I_{OFF} circuitry provides partial Power-down mode operation
- · Quad bus interface
- 3-state buffers
- · Live insertion and extraction permitted
- Output capability: +64 mA and -32 mA
- Inputs are disabled during 3-state mode
- Latch-up protection:
 - · JESD78: exceeds 500 mA
- ESD protection:
 - MIL STD 883 method 3015: exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V

3. Ordering information

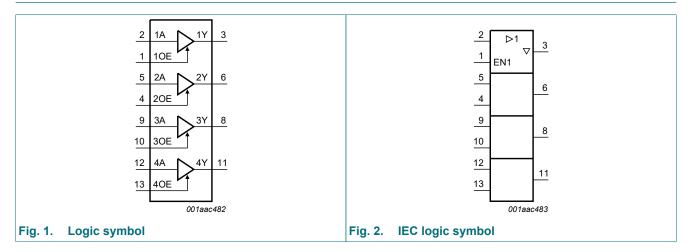
Table 1. Ordering information

.a a. aoi ing ini	able it of defining information											
Type number	Package											
	Temperature range	Name	Description	Version								
74ABT126D	-40 °C to +85 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	SOT108-1								
74ABT126PW	-40 °C to +85 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1								



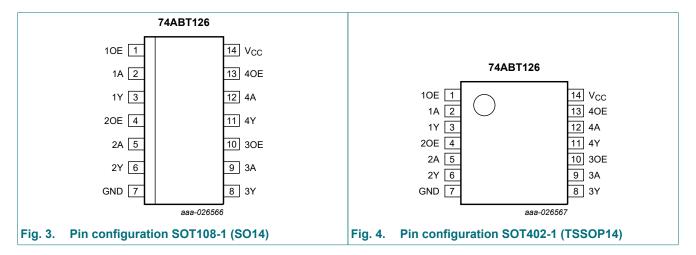
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4. Functional diagram



5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
10E, 20E, 30E, 40E	1, 4, 10, 13	output enable inputs
1A, 2A, 3A, 4A	2, 5, 9, 12	data inputs
1Y, 2Y, 3Y, 4Y	3, 6, 8, 11	data outputs
GND	7	ground (0 V)
V _{CC}	14	supply voltage

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6. Functional description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

Input nOE	Output			
nOE	nA	nY		
Н	L	L		
Н	Н	Н		
L	X	Z		

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7.0	V
V _I	input voltage	[1]	-1.2	+7.0	V
Vo	output voltage	output in OFF-state or HIGH-state [1]	-0.5	+5.5	V
I _{IK}	input clamping current	V _I < 0 V	-18	-	mA
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Io	output current	output in LOW-state	-	128	mA
Tj	junction temperature		-	150	°C
T _{stg}	storage temperature		-65	+150	°C

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		4.5	-	5.5	V
VI	input voltage		0	-	V _{CC}	V
I _{OH}	HIGH-level output current		-32	-	-	mA
I _{OL}	LOW-level output current		-	-	64	mA
Δt/ΔV	input transition rise and fall rate		0	-	10	ns/V
T _{amb}	ambient temperature	in free air	-40	-	+85	°C

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9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Ta	_{mb} = 25	°C	T _{an}	_{nb} = o +85 °C	Unit
				Min	Тур	Max	Min	Max	
V _{IK}	input clamping voltage	V _{CC} = 4.5 V; I _{IK} = -18 mA		-1.2	-0.9	-	-1.2	-	٧
V _{IH}	HIGH-level input voltage			2.0	-	-	2.0	-	٧
V _{IL}	LOW-level input voltage			-	-	0.8	-	0.8	V
V _{OH}	HIGH-level output	V_{CC} = 4.5 V; V_I = V_{IL} or V_{IH}							
	voltage	I _{OH} = -3 mA		2.5	2.9	-	2.5	-	٧
		I _{OH} = -32 mA		2.0	2.4	-	2.0	-	V
		V_{CC} = 5.0 V; V_I = V_{IL} or V_{IH}							
		I _{OH} = -3 mA		3.0	3.4	-	3.0	-	V
V _{OL}	LOW-level output voltage	$V_{CC} = 4.5 \text{ V}; V_I = V_{IL} \text{ or } V_{IH}$							
	I _{OL} = 64mA			-	0.35	0.55	-	0.55	V
l _l	input leakage current	V _{CC} = 5.5 V; V _I = GND or 5.5 V		-	±0.01	±1.0	-	±1.0	μΑ
I _{OFF}	power-off leakage current	$V_{CC} = 0 \text{ V}; V_{O} \text{ or } V_{I} \le 4.5 \text{ V}$	-	±5.0	±100	-	±100	μΑ	
I _{O(pu/pd)}	power-up/power-down output current	V_{CC} = 2.1 V; V_{O} = 0.5 V; V_{I} = GND or V_{CC} ; nOE = don't care	-	±5.0	±50	-	±50	μΑ	
l _{OZ}	OFF-state output current	V_{CC} = 5.5 V; V_I = V_{IL} or V_{IH}							
		output HIGH-state at V _O = 2.7 V		-	1.0	50	-	50	μΑ
		output LOW-state at V _O = 0.5 V		-50	-1.0	-	-50	-	μΑ
I _{CEX}	output high leakage current	$V_{CC} = 5.5 \text{ V}; V_{O} = 5.5 \text{ V};$ $V_{I} = \text{GND or } V_{CC}$		-	5.0	50	-	50	μΑ
Io	output current	V _{CC} = 5.5 V; V _O = 2.5 V	[2]	-180	-100	-50	-180	-50	mA
I _{CC}	supply current	V_{CC} = 5.5 V; V_I = GND or V_{CC}							
		outputs HIGH-state		-	65	250	-	250	μΑ
		outputs LOW-state		-	12	15	-	15	mA
		outputs 3-state		-	65	250	-	250	μΑ
ΔI _{CC}	additional supply current	per data input pin; one data input at 3.4 V and other inputs at V_{CC} or GND; V_{CC} = 5.5 V	[3]						
		outputs enabled		-	0.5	1.5	-	1.5	mA
		outputs 3-state		-	50	250	-	250	μΑ
		per enable input pin; one enable input at 3.4 V and other inputs at V _{CC} or GND; V _{CC} = 5.5 V	[3]						
		outputs 3-state		-	0.5	1.5	-	1.5	mA
Cı	input capacitance	V _I = 0 V or V _{CC}		-	4	-	-	-	pF
Co	output capacitance	outputs disabled; V _O = 0 V or V _{CC}		-	7	-	-	-	pF

^[1] This parameter is valid for any V_{CC} between 0 V and 2.1 V, with a transition time of up to 10 ms. From V_{CC} = 2.1 V to V_{CC} = 5 V ± 10 % a transition time of up to 100 μ s is permitted.

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^[2] Not more than one output should be tested at a time, and the duration of the test should not exceed one second.

^[3] This is the increase in supply current for each input at 3.4 V.

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10. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 7.

Symbol	Parameter	Conditions	T _{amb} = 2	5 °C; V _C	_C = 5.0 V	T _{amb} = -40 ° V _{CC} = 5.0	Unit	
			Min	Тур	Max	Min	Max	
t _{PLH}	LOW to HIGH propagation delay	nA to nY; see Fig. 5	1.0	2.9	4.2	1.0	4.4	ns
t _{PHL}	HIGH to LOW propagation delay	nA to nY; see Fig. 5	1.0	3.0	4.3	1.0	4.6	ns
t _{PZH}	OFF-state to HIGH propagation delay	see Fig. 6	1.5	3.2	5.8	1.5	6.5	ns
t _{PZL}	OFF-state to LOW propagation delay	see Fig. 6	1.9	4.4	5.9	1.9	6.5	ns
t _{PHZ}	HIGH to OFF-state propagation delay	see Fig. 6	1.0	4.2	5.2	1.0	5.8	ns
t _{PLZ}	LOW to OFF-state propagation delay	see Fig. 6	1.0	2.9	4.9	1.0	5.5	ns

10.1. Waveforms and test circuit

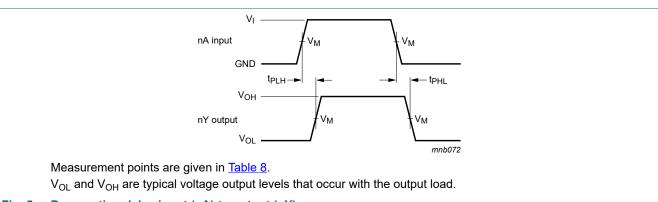
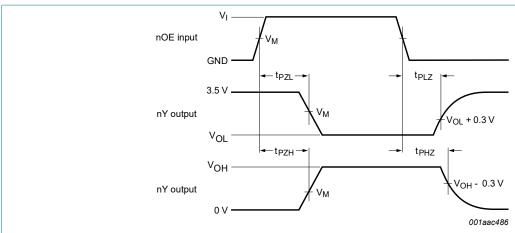


Fig. 5. Propagation delay input (nA) to output (nY)

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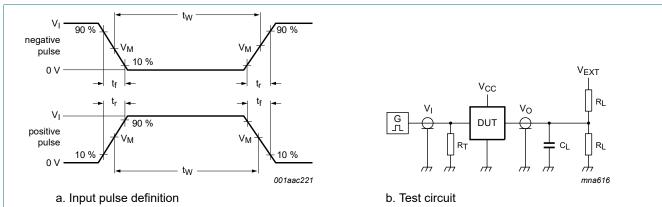
Measurement points are given in Table 8.

 V_{OL} and V_{OH} are typical voltage output levels that occur with the output load.

Enable and disable times of 3-state outputs Fig. 6.

Table 8. Measurement points

Input	Output
V_{M}	V_{M}
1.5 V	1.5 V



Test data is given in Table 9.

Definitions test circuit:

R_L = Load resistance.

C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator.

 V_{EXT} = Test voltage for switching times.

Test circuit for measuring switching times Fig. 7.

Table 9. Test data

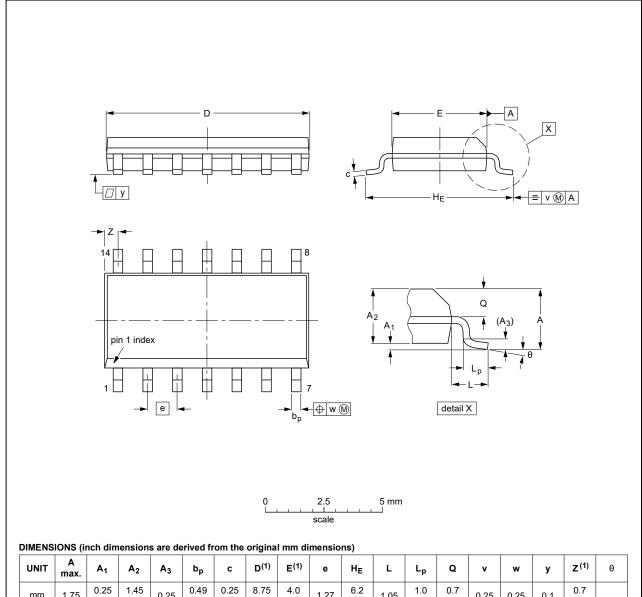
Input				Load		V _{EXT}			
V _I	f _i	t _W	t _r , t _f	CL	R_L	t _{PHZ} , t _{PZH}	t_{PLZ} , t_{PZL}	t _{PLH} , t _{PHL}	
3.0 V	≤ 1 MHz	500 ns	≤ 2.5 ns	50 pF	500 Ω	open	7.0 V	open	

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11. Package outline

SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1



	UNIT	A max.	A ₁	A ₂	A ₃	b _p	С	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	q	v	w	у	Z ⁽¹⁾	θ
	mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	8.75 8.55	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
iı	nches	0.069	0.010 0.004	0.057 0.049	0.01		0.0100 0.0075	0.35 0.34	0.16 0.15	0.05	0.244 0.228	0.041	0.039 0.016	0.028 0.024	0.01	0.01	0.004	0.028 0.012	0°

Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

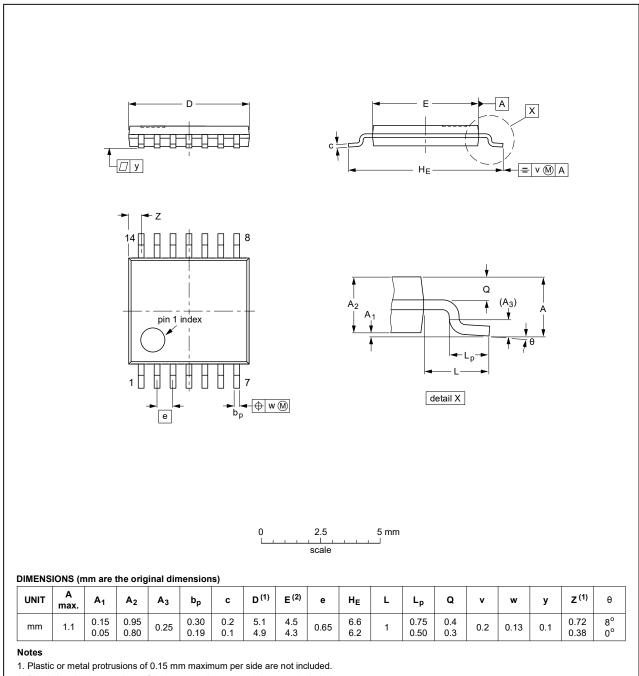
OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT108-1	076E06	MS-012				99-12-27 03-02-19	

Fig. 8. Package outline SOT108-1 (SO14)

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TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1



2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT402-1		MO-153				99-12-27 03-02-18	

Fig. 9. Package outline SOT402-1 (TSSOP14)

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12. Abbreviations

Table 10. Abbreviations

Acronym	Description		
BiCMOS	Bipolar Complementary Metal Oxide Semiconductor		
DUT	Device Under Test		
ESD	ElectroStatic Discharge		
MIL	Military		
MM	Machine Model		
TTL	Transistor-Transistor Logic		

13. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74ABT126 v.6	20201008	Product data sheet	-	74ABT126 v.5	
Modifications:	 <u>Section 1</u> and <u>Section 2</u> updated. Type number 74ABT126DB (SOT337-1 / SSOP14) removed. 				
74ABT126 v.5	20170404	Product data sheet	-	74ABT126 v.4	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 				
74ABT126 v.4	20050217	Product data sheet	-	74ABT126 v.3	
Modifications:	 The format of this data sheet has been redesigned to comply with the new presentation andinformation standard of Philips Semiconductors. Section 2: modified 'JEDEC Std 17' into 'JESD78'. Table 7: changed min value of t_{PZH} from 1.9 ns into 1.5 ns for both conditions V_{CC} = 5.0 V at T_{amb} = 25 °C and V_{CC} = 5.0 V ± 0.5 V at T_{amb} = -40 °C to +85 °C. 				
74ABT126 v.3	20021213	Product specification	-	74ABT126 v.2	
74ABT126 v.2	19980116	Product specification	-	74ABT126 v.1	
74ABT126 v.1					

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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