

Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild guestions@onsemi.com.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer



November 2013

FQNL2N50B

N-Channel QFET® MOSFET

500 V, 0.35 A, 5.3 Ω

Description

This N-Channel enhancement mode power MOSFET is • 0.35 A, 500 V, $R_{DS(on)}$ = 5.3 Ω (Max.) @ V_{GS} = 10 V, produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state

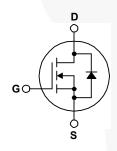
• Low Gate Charge (Typ. 6 nC) resistance, and to provide superior switching performance

• Low Crss (Typ. 4 pF) and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- $I_D = 0.175 A$





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQNL2N50BTA	Unit
V _{DSS}	Drain-Source Voltage		500	V
I _D	Drain Current - Continuous (T _C = 25°C)		0.35	А
	- Continuous (T _C = 100°C)		0.22	Α
I _{DM}	Drain Current - Pulsed (Note 1)	1.4	Α
V _{GSS}	Gate-Source Voltage		± 30	V
I _{AR}	Avalanche Current (Note 1)	0.35	Α
E _{AR}	Repetitive Avalanche Energy (Not		0.15	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 2)		4.5	V/ns
P_{D}	Power Dissipation (T _C = 25°C)		1.5	W
	- Derate above 25°C		0.012	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T _L	Maximum lead temperature for soldering, 1/8" from case for 5 seconds.		300	°C

Thermal Characteristics

Symbol	Parameter	FQNL2N50BTA	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	83	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQNL2N50BTA	FQNL2N50B	TO-92L	AMMO	N/A	N/A	2000 units

Electrical Characteristics

T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	500			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		0.48		V/°C
I _{DSS} Zero Gate Voltage Drain Current		V _{DS} = 500 V, V _{GS} = 0 V		-	1	μΑ
		V _{DS} = 400 V, T _C = 125°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V		I	-100	nA
On Cha	racteristics					
V _{GS(th)} Gate Threshold Voltage		$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	2.3	3.0	3.7	V
33()		$V_{DS} = V_{GS}, I_{D} = 250 \text{ mA}$	3.6	4.3	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 0.175 A		4.2	5.3	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 50 V, I _D = 0.175 A		0.72		S
	ic Characteristics			100		
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		180	230	pF
C _{oss}	Output Capacitance	f = 1.0 MHz		30	40	pF
C _{rss}	Reverse Transfer Capacitance			4	6	pF
Switchi	ing Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 250 V, I _D = 2.1 A,		6	20	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		25	60	ns
+	Turn Off Dolov Time	- 1.6		10	30	ns
$t_{d(off)}$	Turn-Off Delay Time					
t _f	Turn-Off Fall Time	(Note 3)		20	50	ns
t _f	· ·	, ,		20 6.0	50 8.0	
t _f Q _g	Turn-Off Fall Time	V _{DS} = 400 V, I _D = 2.1 A,		_		ns
	Turn-Off Fall Time Total Gate Charge	, ,		6.0	8.0	ns nC
t _f Q _g Q _{gs} Q _{gd}	Turn-Off Fall Time Total Gate Charge Gate-Source Charge	$V_{DS} = 400 \text{ V}, I_D = 2.1 \text{ A},$ $V_{GS} = 10 \text{ V}$ (Note 3)		6.0	8.0	ns nC nC
t _f Q _g Q _{gs} Q _{gd}	Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DS} = 400 \text{ V}, I_D = 2.1 \text{ A},$ $V_{GS} = 10 \text{ V}$ (Note 3)		6.0	8.0	ns nC nC
$\begin{aligned} & t_f & \\ & Q_g & \\ & Q_{gs} & \\ & Q_{gd} & \\ & \textbf{Drain-S} & \end{aligned}$	Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge Source Diode Characteristics and	V _{DS} = 400 V, I _D = 2.1 A, V _{GS} = 10 V (Note 3)		6.0 1.3 3.0	8.0	ns nC nC
$\begin{aligned} & t_f & \\ & Q_g & \\ & Q_{gs} & \\ & Q_{gd} & \\ & & \textbf{Drain-S} \\ & I_{SM} & \end{aligned}$	Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge Source Diode Characteristics and Maximum Continuous Drain-Source Diode	V _{DS} = 400 V, I _D = 2.1 A, V _{GS} = 10 V (Note 3)		6.0 1.3 3.0	8.0	ns nC nC nC
$t_{\rm f}$ $Q_{\rm g}$ $Q_{\rm gs}$ $Q_{\rm gd}$ Drain-S	Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge Source Diode Characteristics at Maximum Continuous Drain-Source Diode Faxing Characteristics at Maximum Pulsed Drain-Source Diode Faxing Characteristics at Maximum Pulsed Drain-Source Diode Faxing Characteristics	V _{DS} = 400 V, I _D = 2.1 A, V _{GS} = 10 V (Note 3) And Maximum Ratings and Forward Current Forward Current		6.0 1.3 3.0	8.0 0.35 1.4	ns nC nC nC

- **Notes:**1. Repetitive rating : pulse-width limited by maximum junction temperature. 2. $I_{SD} \le 2.1$ A, $di/dt \le 200$ A/µs, $V_{DD} \le BV_{DSS}$, starting $T_J = 25^{\circ}C$.
 3. Essentially independent of operating temperature.

Typical Characteristics

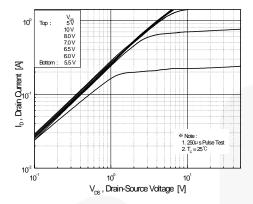


Figure 1. On-Region Characteristics

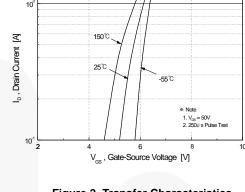


Figure 2. Transfer Characteristics

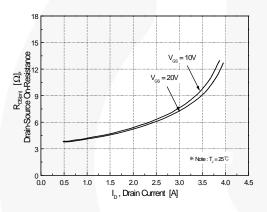


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

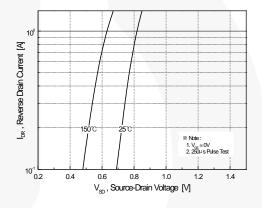


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

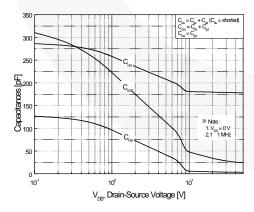


Figure 5. Capacitance Characteristics

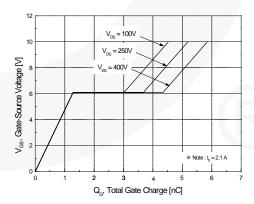


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

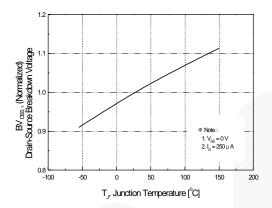
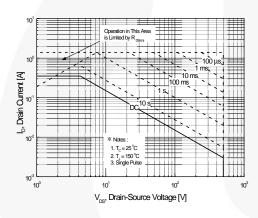


Figure 7. Breakdown Voltage Variation vs. Temperature

Figure 8. On-Resistance Variation vs. Temperature



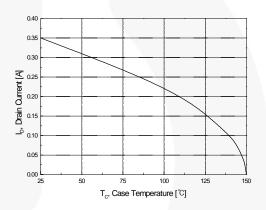


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

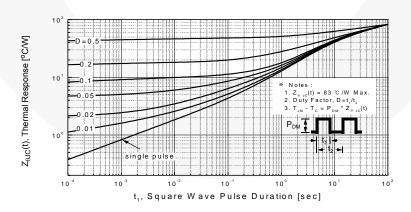


Figure 11. Transient Thermal Response Curve

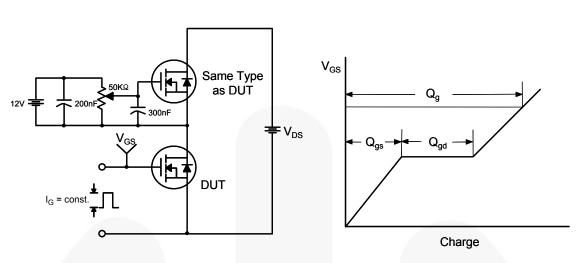


Figure 12. Gate Charge Test Circuit & Waveform

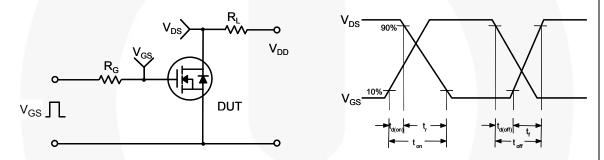


Figure 13. Resistive Switching Test Circuit & Waveforms

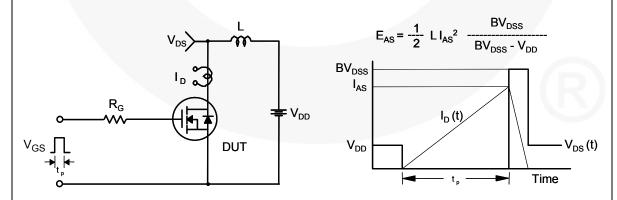
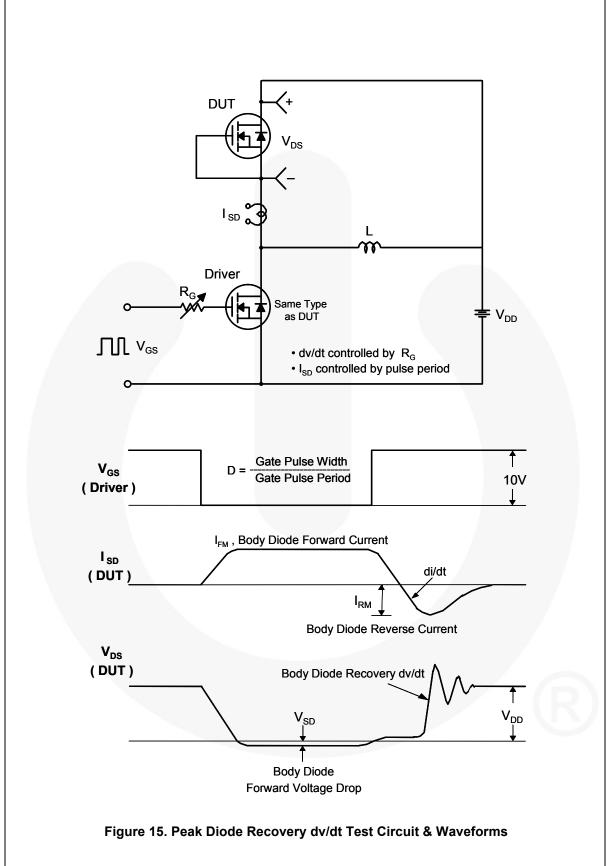
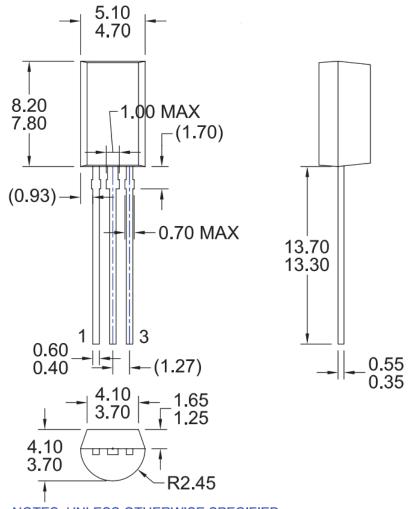


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions



NOTES: UNLESS OTHERWISE SPECIFIED

- A) THIS PACKAGE DOES NOT CONFORM TO ANY STANDARD
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- D) FORMERLY NAMED BD1409
- E) DRAWING FILE NAME: MKT-ZA03HREV1

Figure 16. TO92L, 3-Lead, 8 mm Long Body

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

http://www.fairchildsemi.com/package/packageDetails.html?id=PN_TO92-H03





TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™ AX-CAP® BitSiC™ Build it Now™ CorePLUS™ CorePOWER™ $CROSSVOLT^{\text{TM}}$

CTI ™ Current Transfer Logic™ DEUXPEED® Dual Cool™

EcoSPARK® EfficentMax™ ESBC™

Fairchild[®] Fairchild Semiconductor® FACT Quiet Series™

FACT® FAST® FastvCore™ FETBench™ FPS™

F-PFS™ FRFET® Global Power ResourceSM

GreenBridge™ Green FPS™

Green FPS™ e-Series™

G*max*™ GTO™ IntelliMAX™ ISOPLANAR™

Marking Small Speakers Sound Louder

and Better™ MegaBuck™ MICROCOUPLER™ MicroFET™ MicroPak™

MicroPak2™ MillerDrive™ MotionMax™ mWSaver® OptoHiT™ OPTOLOGIC® OPTOPLANAR® PowerTrench® PowerXS™

Programmable Active Droop™

QFET QS™ Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™ SignalWise™

SmartMax™ SMART START™

Solutions for Your Success™

STEALTH™ SuperFET® SuperSOT™-3 SuperSOT™-6

SuperSOT™-8 SupreMOS® SvncFET™

Sync-Lock™ SYSTEM ®* TinyBoost[®] TinyBuck[®] TinyCalc™ TinyLogic[®] TINYOPTO™ TinvPower™ TinyPWM™ TinyWire™ TranSiC™ TriFault Detect™ TRUECURRENT®*

UHC[®] Ultra FRFET™ UniFFT™ VCX™ VisualMax™ VoltagePlus™ XSTM

μSerDes™

*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY
FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE
EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used here in:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS Definition of Terms

Datasheet Identification Product Status		Definition		
Advance Information Formative / In Design		Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
Obsolete Not In Production		Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.		

Rev. 166

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdt/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and exp

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative